

CMR3-02 CMR3-08  
 CMR3-04 CMR3-10  
 CMR3-06 CMR3-11

**SURFACE MOUNT SILICON  
 GENERAL PURPOSE RECTIFIERS  
 3.0 AMP, 200 THRU 1100 VOLT**



[www.centrasemi.com](http://www.centrasemi.com)

**DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CMR3-02 series 3.0 Amp surface mount silicon rectifiers are highly reliable components designed for use in all types of commercial, industrial, entertainment, computer, and automotive applications. To order devices on 16mm Tape and Reel (3000/13" Reel), add TR13 suffix to part number.



**SMC CASE**

**MARKING CODE: SEE MARKING CODE TABLE ON FOLLOWING PAGE**

**FEATURES:**

- High reliability
- Glass passivated chip
- Special selections available
- "C" bend construction provides strain relief when mounted on PC board

**MAXIMUM RATINGS:** ( $T_A=25^\circ\text{C}$  unless otherwise noted)

		CMR3 -02	CMR3 -04	CMR3 -06	CMR3 -08	CMR3 -10	CMR3 -11	UNITS
Peak Repetitive Reverse Voltage	$V_{RRM}$	200	400	600	800	1000	1100	V
DC Blocking Voltage	$V_R$	200	400	600	800	1000	1100	V
RMS Reverse Voltage	$V_{R(RMS)}$	140	280	420	560	700	770	V
Average Forward Current ( $T_A=75^\circ\text{C}$ )	$I_O$				3.0			A
Peak Forward Surge Current, $t_p=8.3\text{ms}$	$I_{FSM}$				200			A
Operating and Storage Junction Temperature	$T_J, T_{stg}$				-65 to +175			$^\circ\text{C}$
Thermal Resistance	$\theta_{JL}$				10			$^\circ\text{C/W}$

**ELECTRICAL CHARACTERISTICS:** ( $T_A=25^\circ\text{C}$  unless otherwise noted)

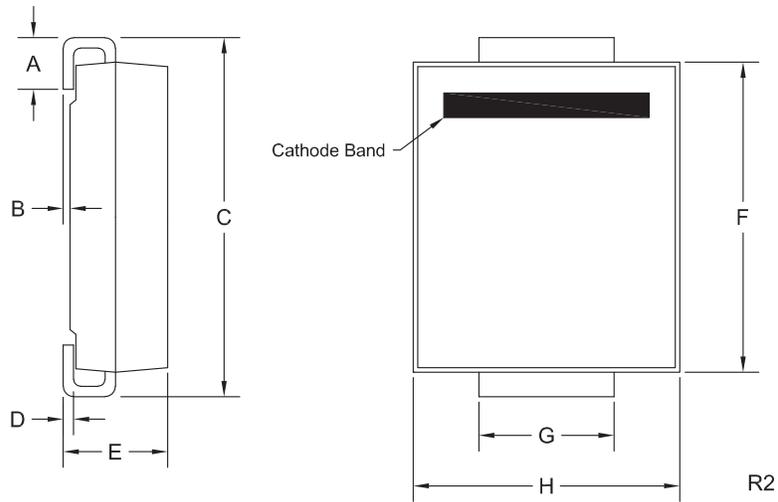
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
$I_R$	$V_R=\text{Rated } V_{RRM}$		5.0	$\mu\text{A}$
$I_R$	$V_R=\text{Rated } V_{RRM}, T_A=125^\circ\text{C}$		250	$\mu\text{A}$
$V_F$	$I_F=3.0\text{A}$		1.2	V

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**SMC CASE - MECHANICAL OUTLINE**



DEVICE	MARKING CODE
CMR3-02	C302
CMR3-04	C304
CMR3-06	C306
CMR3-08	C308
CMR3-10	C310
CMR3-11	C311

SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.030	0.060	0.76	1.52
B	0.002	0.008	0.05	0.20
C	0.305	0.320	7.75	8.13
D	0.006	0.012	0.15	0.31
E	0.079	0.103	2.00	2.62
F	0.260	0.280	6.60	7.11
G	0.108	0.128	2.75	3.25
H	0.220	0.245	5.59	6.22

SMC (REV: R2)

R5 (11-September 2013)

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