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April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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2SK1808

Silicon N Channel MOS FET

REJ03G0975-0200
(Previous: ADE-208-1322)
Rev.2.00
Sep 07, 2005

Application

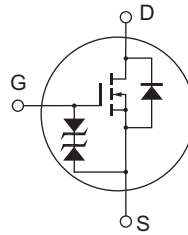
High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter

Outline

RENESAS Package code: PRSS0003AD-A
(Package name: TO-220FM)



1. Gate
2. Drain
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	900	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	4	A
Drain peak current	I _{D(pulse)} ^{*1}	10	A
Body to drain diode reverse drain current	I _{DR}	4	A
Channel dissipation	P _{ch} ^{*2}	35	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1 %
 2. Value at Tc = 25°C

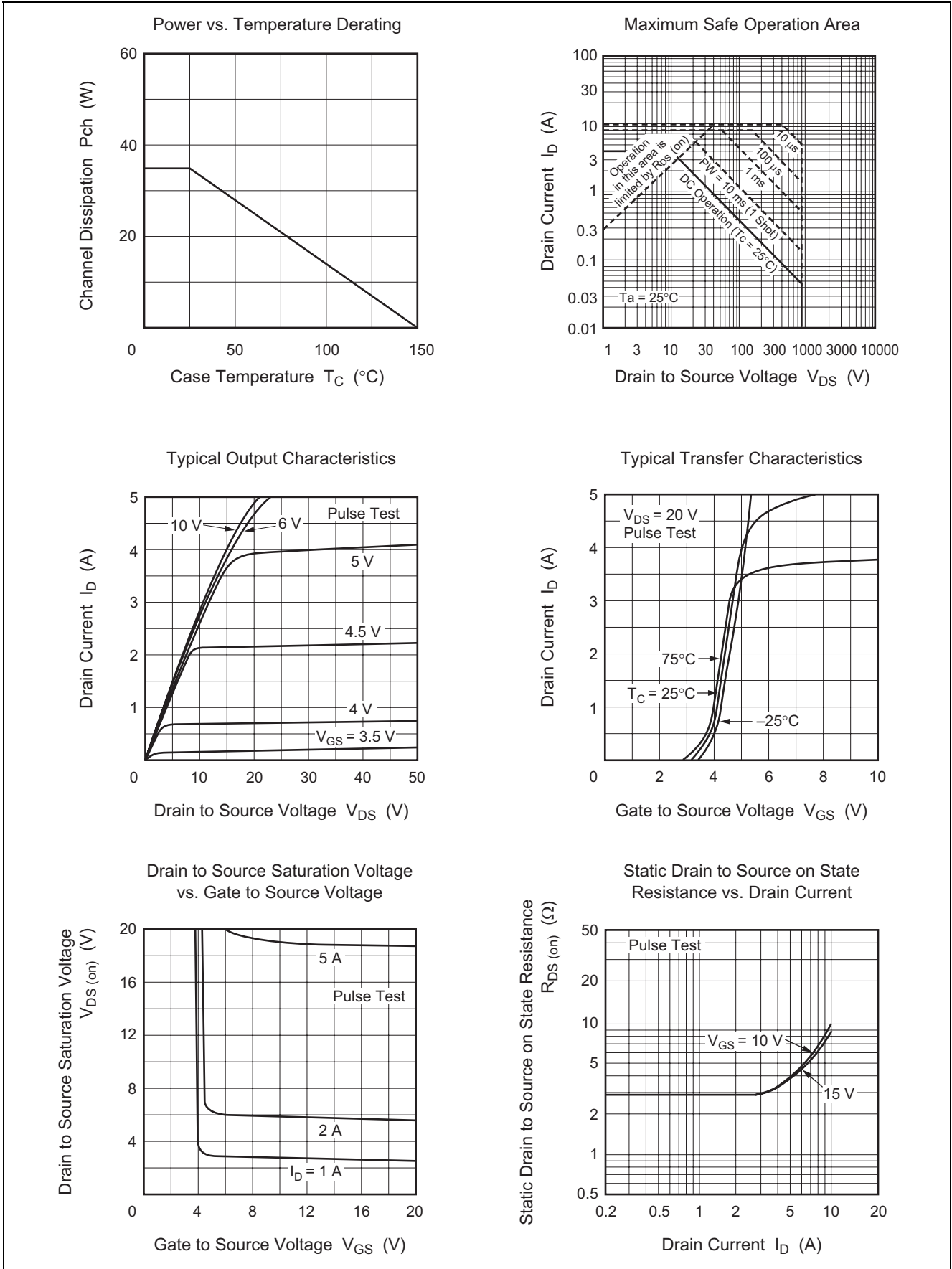
Electrical Characteristics

(Ta = 25°C)

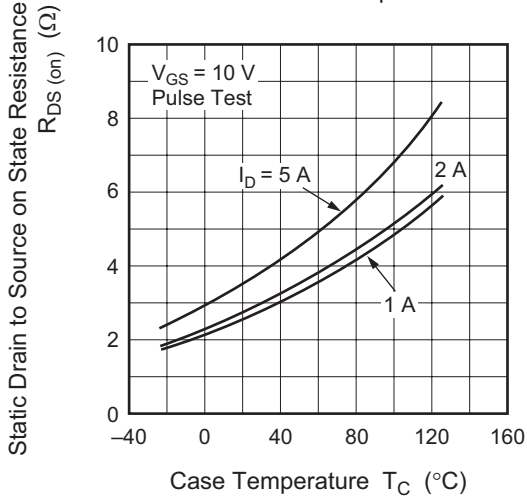
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	900	—	—	V	I _D = 10 mA, V _{GS} = 0
Gate to source breakdown voltage	V _{(BR)GSS}	±30	—	—	V	I _G = ±100 μA, V _{DS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	μA	V _{GS} = ±25 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	250	μA	V _{DS} = 720 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	2.0	—	3.0	V	I _D = 1 mA, V _{DS} = 10 V
Static drain to source on state resistance	R _{DS(on)}	—	3.0	4.0	Ω	I _D = 2 A, V _{GS} = 10 V ^{*3}
Forward transfer admittance	y _{fs}	1.7	2.7	—	S	I _D = 2 A, V _{DS} = 20 V ^{*3}
Input capacitance	C _{iss}	—	740	—	pF	V _{DS} = 10 V, V _{GS} = 0, f = 1 MHz
Output capacitance	C _{oss}	—	305	—	pF	
Reverse transfer capacitance	C _{rss}	—	150	—	pF	
Turn-on delay time	t _{d(on)}	—	15	—	ns	I _D = 2 A, V _{GS} = 10 V, R _L = 15 Ω
Rise time	t _r	—	60	—	ns	
Turn-off delay time	t _{d(off)}	—	100	—	ns	
Fall time	t _f	—	80	—	ns	
Body to drain diode forward voltage	V _{DF}	—	0.9	—	V	I _F = 4 A, V _{GS} = 0
Body to drain diode reverse recovery time	t _{rr}	—	800	—	ns	I _F = 4 A, V _{GS} = 0, di _F /dt = 100 A/μs

Note: 1. Pulse Test

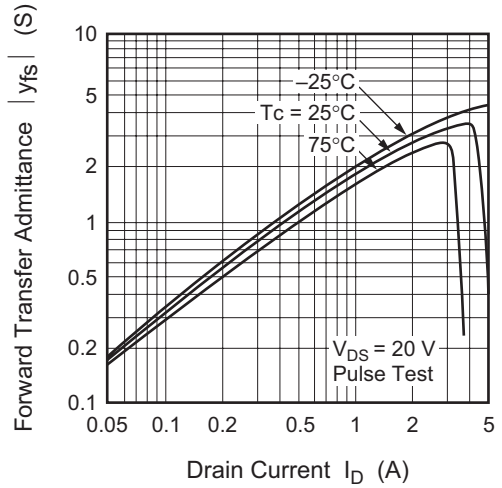
Main Characteristics



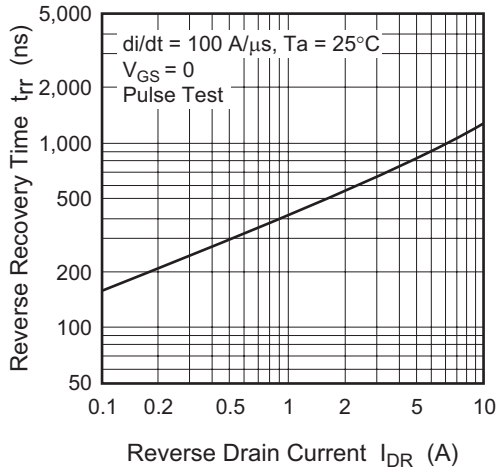
Static Drain to Source on State Resistance vs. Temperature



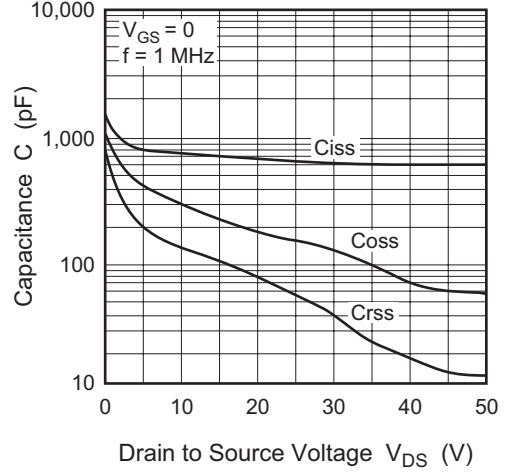
Forward Transfer Admittance vs. Drain Current



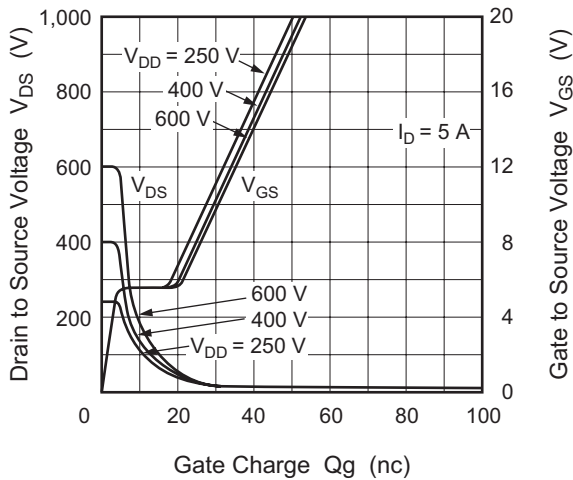
Body to Drain Diode Reverse Recovery Time



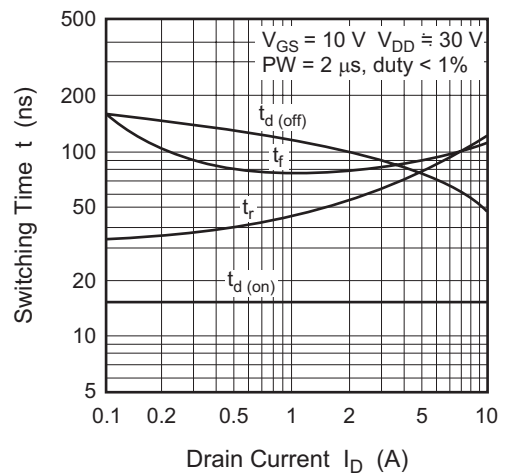
Typical Capacitance vs. Drain to Source Voltage



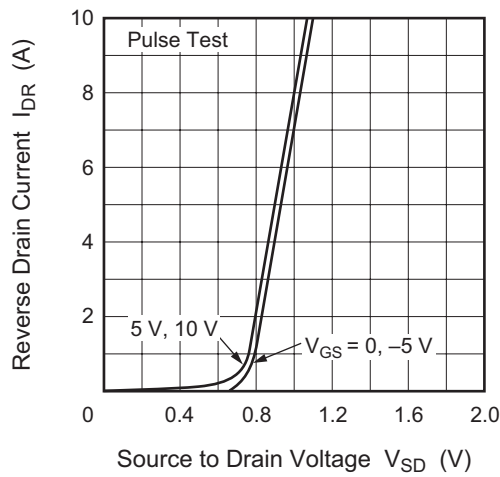
Dynamic Input Characteristics



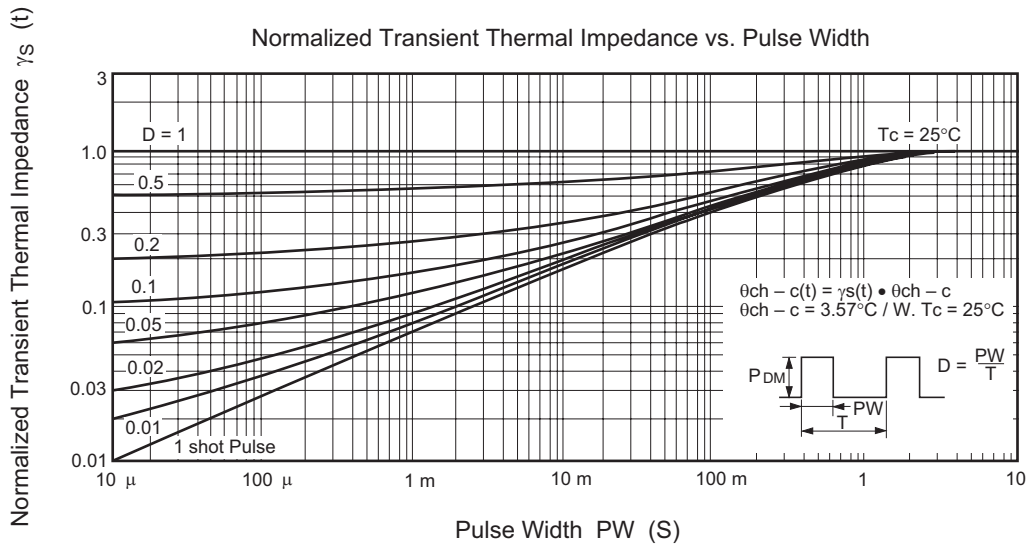
Switching Characteristics



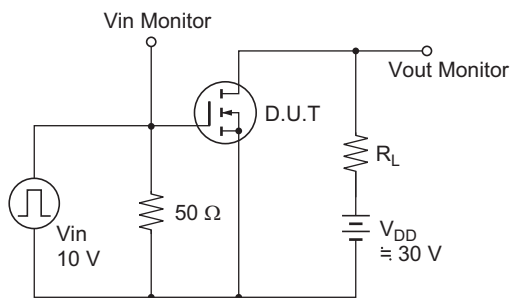
Reverse Drain Current vs. Source to Drain Voltage



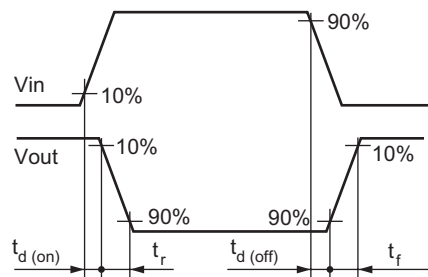
Normalized Transient Thermal Impedance vs. Pulse Width



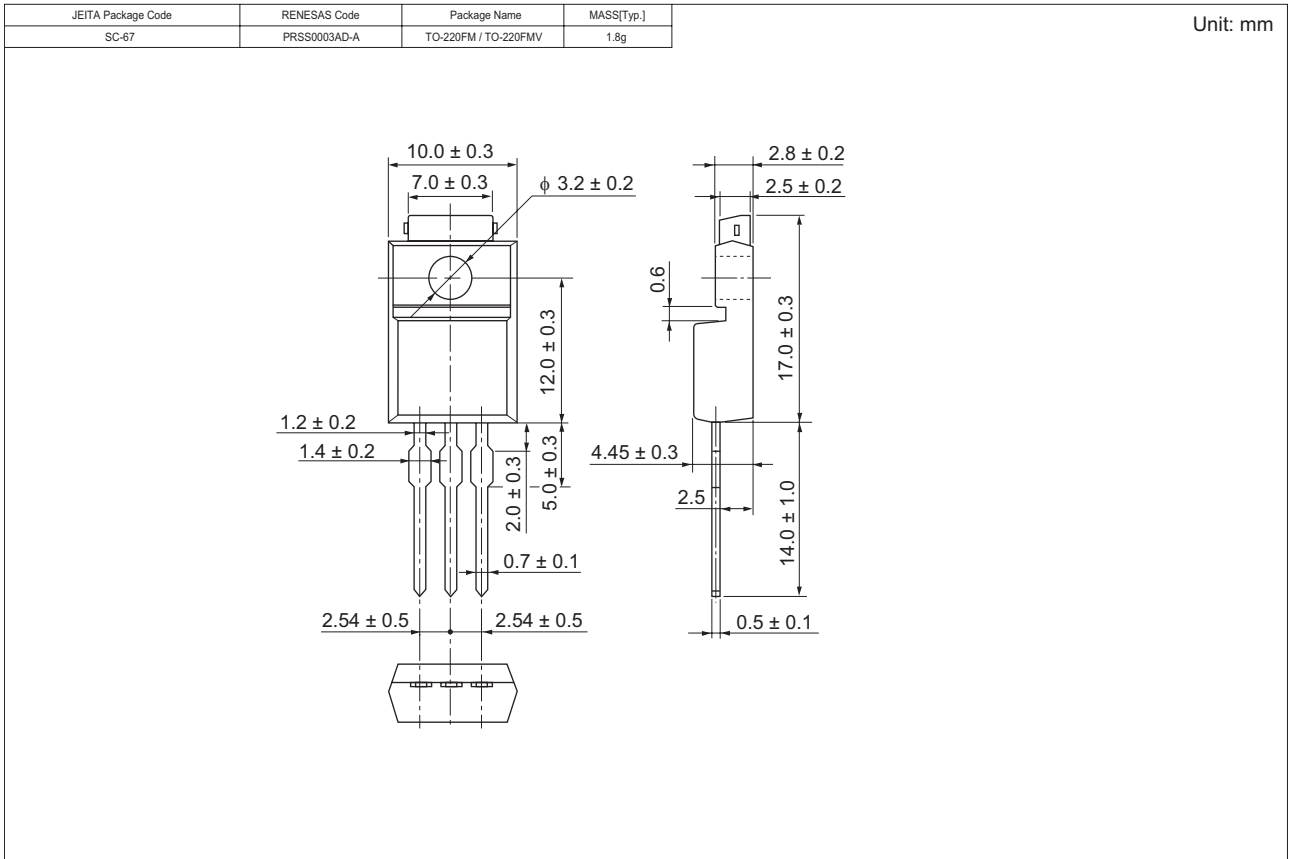
Switching Time Test Circuit



Waveforms



Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SK1808-E	500 pcs	Box (Sack)

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