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April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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2SK2937

Silicon N Channel MOS FET
High Speed Power Switching

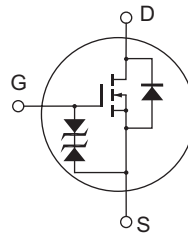
REJ03G1051-0500
(Previous: ADE-208-560C)
Rev.5.00
Sep 07, 2005

Features

- Low on-resistance
 $R_{DS} = 0.026 \Omega$ typ.
- High speed switching
- 4 V gate drive device can be driven from 5 V source

Outline

RENESAS Package code: PRSS0003AD-A
(Package name: TO-220FM)



1. Gate
2. Drain
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DS}	60	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	25	A
Drain peak current	I _{D(pulse)} ^{Note1}	100	A
Body-drain diode reverse drain current	I _{DR}	25	A
Avalanche current	I _{AP} ^{Note3}	20	A
Avalanche energy	E _{AR} ^{Note3}	34	mJ
Channel dissipation	P _{ch} ^{Note2}	25	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Notes: 1. PW ≤ 10μs, duty cycle ≤ 1 %
 2. Value at Tc = 25°C
 3. Value at Tch = 25°C, Rg ≥ 50 Ω

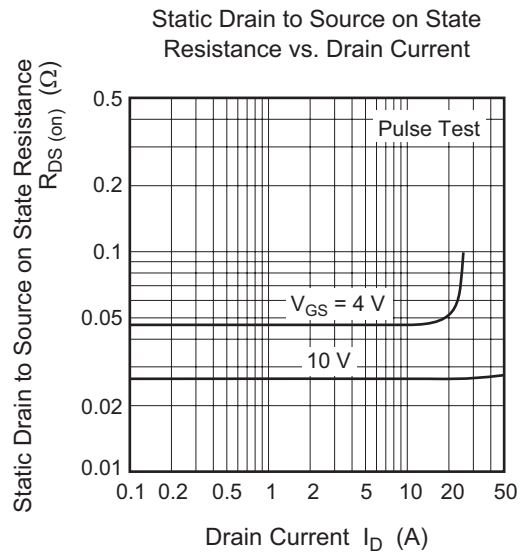
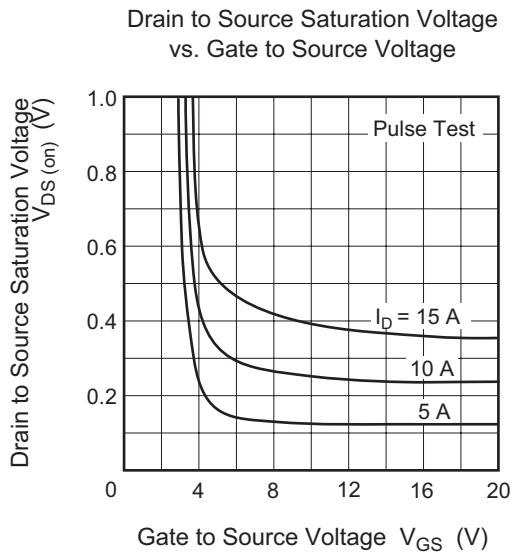
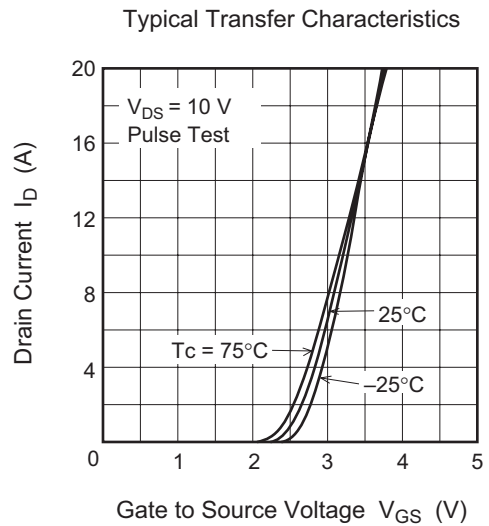
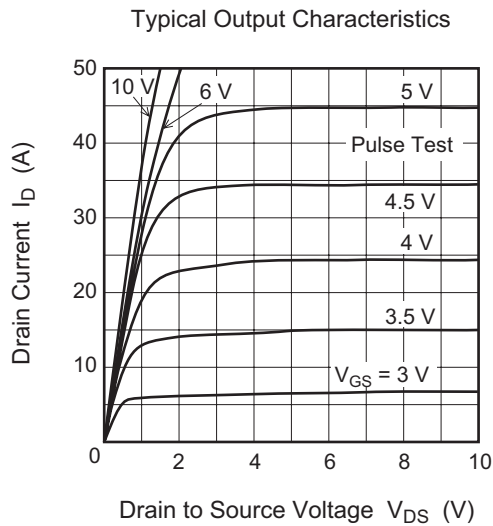
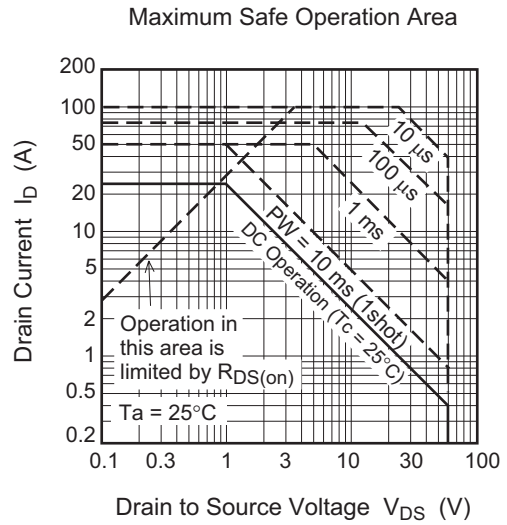
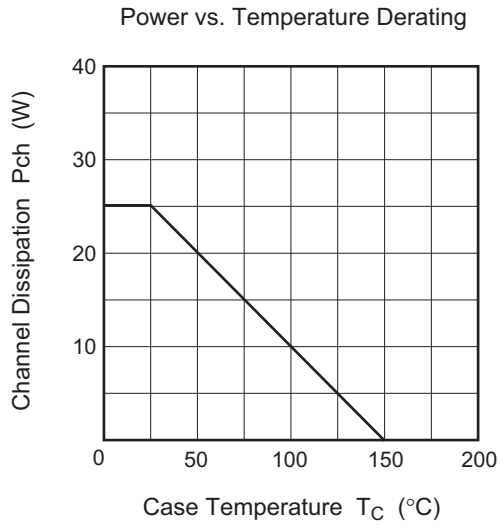
Electrical Characteristics

(Ta = 25°C)

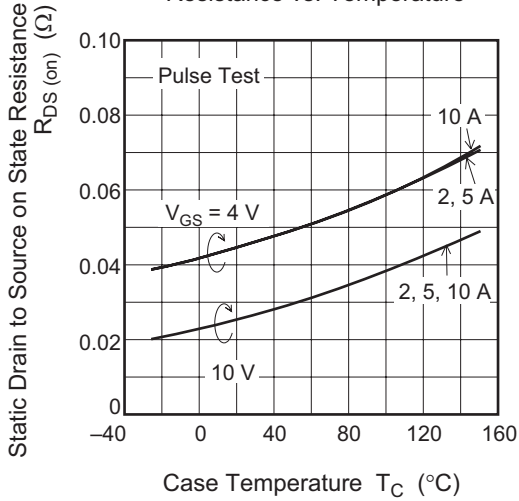
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	60	—	—	V	I _D = 10 mA, V _{GS} = 0
Gate to source breakdown voltage	V _{(BR)GSS}	±20	—	—	V	I _G = ±100 μA, V _{DS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	μA	V _{GS} = ±16 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	10	μA	V _{DS} = 60 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	1.5	—	2.5	V	I _D = 1 mA, V _{DS} = 10 V
Static drain to source on state resistance	R _{DS(on)}	—	0.026	0.034	Ω	I _D = 15 A, V _{GS} = 10 V ^{Note4}
	R _{DS(on)}	—	0.045	0.070	Ω	I _D = 15 A, V _{GS} = 4 V ^{Note4}
Forward transfer admittance	y _{fs}	11	17	—	S	I _D = 15 A, V _{DS} = 10 V ^{Note4}
Input capacitance	C _{iss}	—	740	—	pF	V _{DS} = 10 V, V _{GS} = 0, f = 1 MHz
Output capacitance	C _{oss}	—	380	—	pF	
Reverse transfer capacitance	C _{rss}	—	140	—	pF	
Turn-on delay time	t _{d(on)}	—	10	—	ns	I _D = 15 A, V _{GS} = 10 V, R _L = 2 Ω
Rise time	t _r	—	160	—	ns	
Turn-off delay time	t _{d(off)}	—	100	—	ns	
Fall time	t _f	—	150	—	ns	
Body-drain diode forward voltage	V _{DF}	—	0.95	—	V	I _F = 25A, V _{GS} = 0
Body-drain diode reverse recovery time	t _{rr}	—	40	—	ns	I _F = 25A, V _{GS} = 0 di _F / dt = 50 A/μs

Note: 4. Pulse test

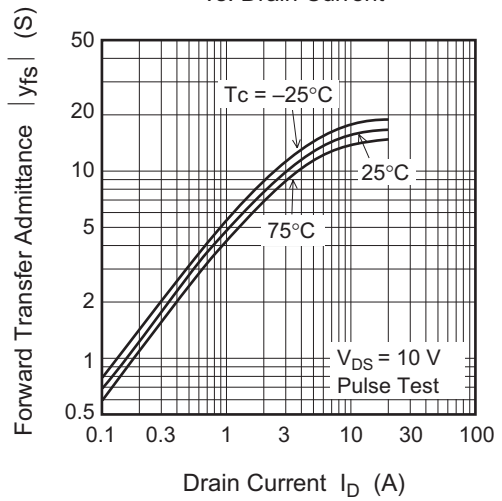
Main Characteristics



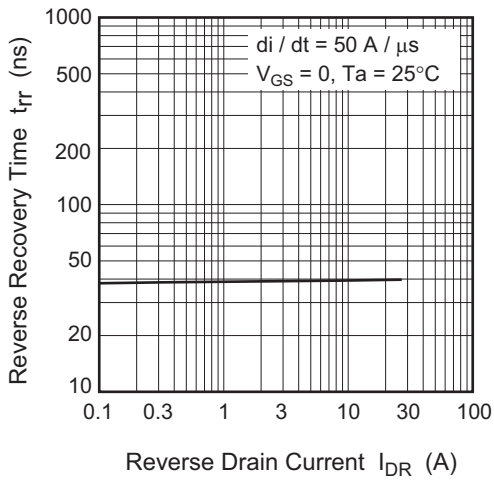
Static Drain to Source on State Resistance vs. Temperature



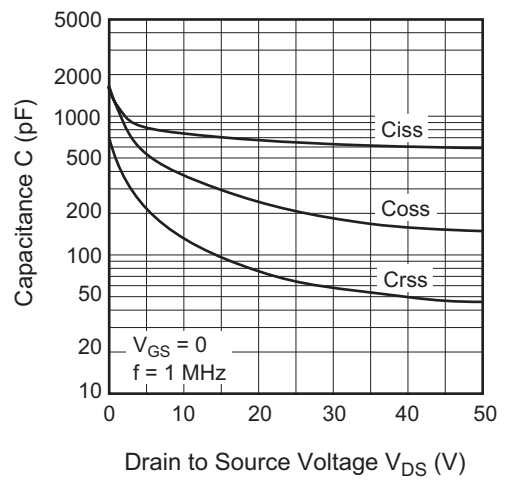
Forward Transfer Admittance vs. Drain Current



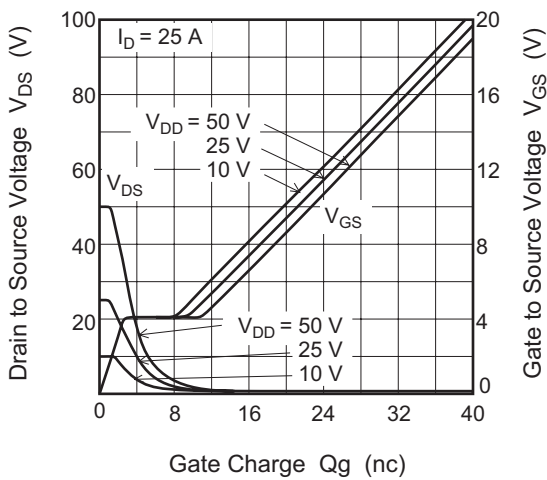
Body to Drain Diode Reverse Recovery Time



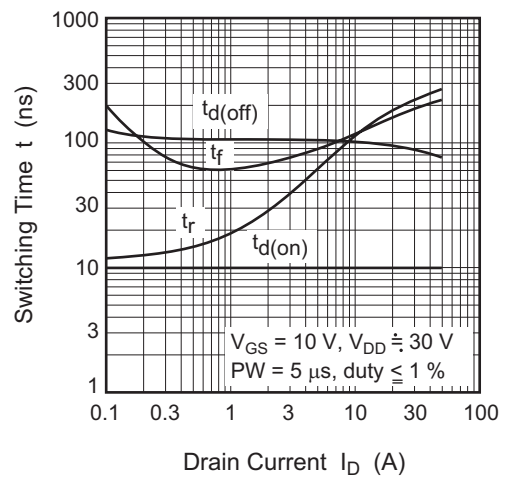
Typical Capacitance vs. Drain to Source Voltage

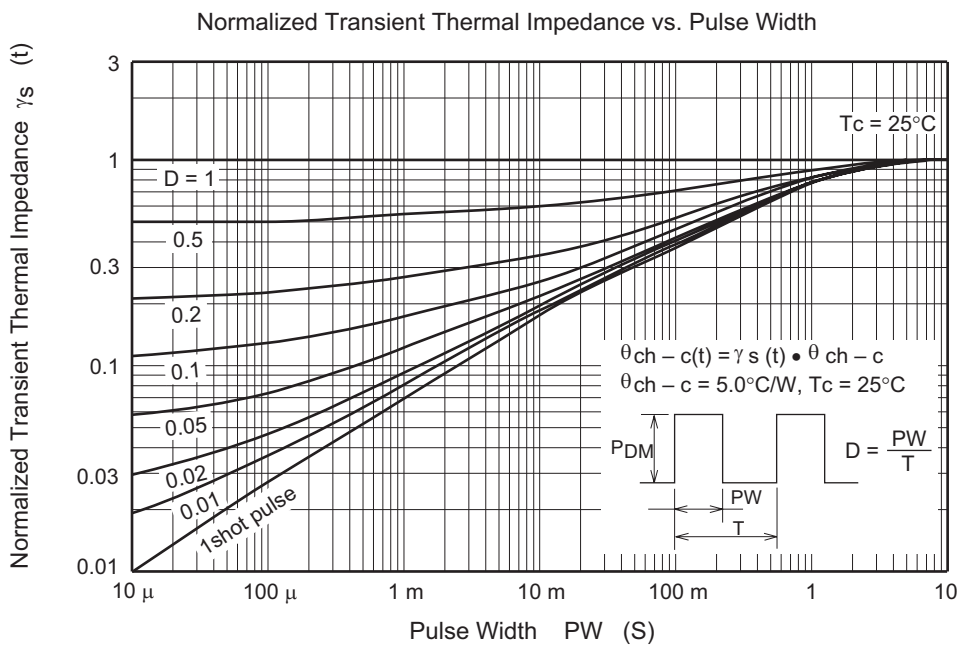
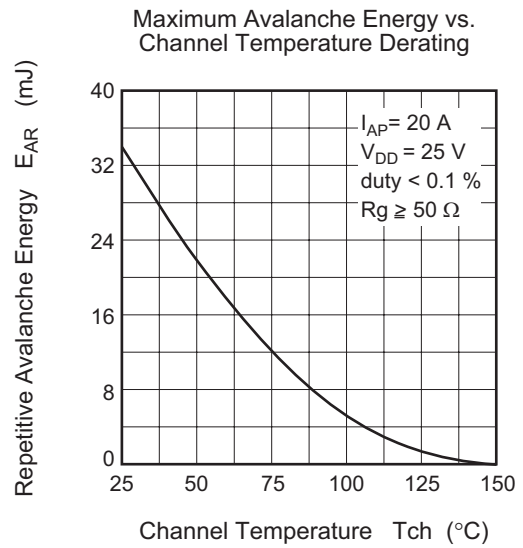
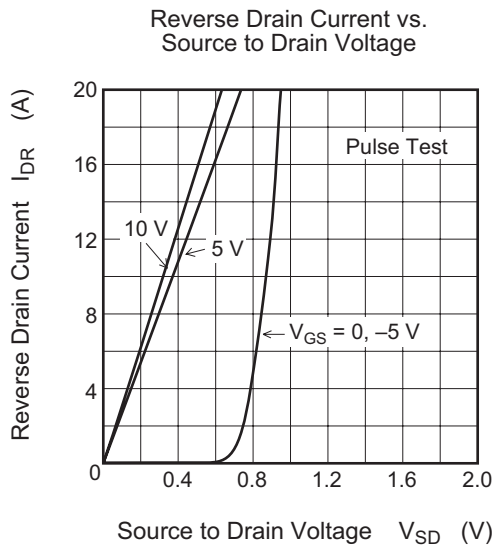


Dynamic Input Characteristics

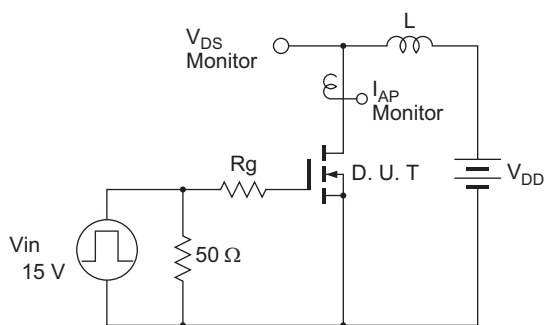


Switching Characteristics

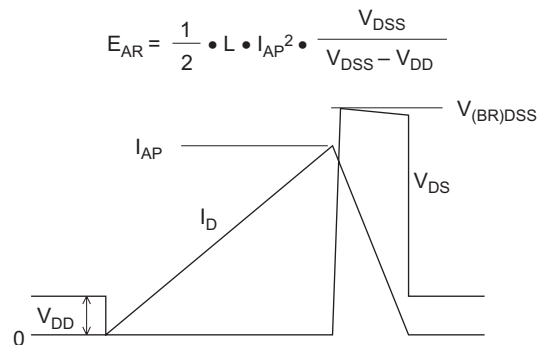


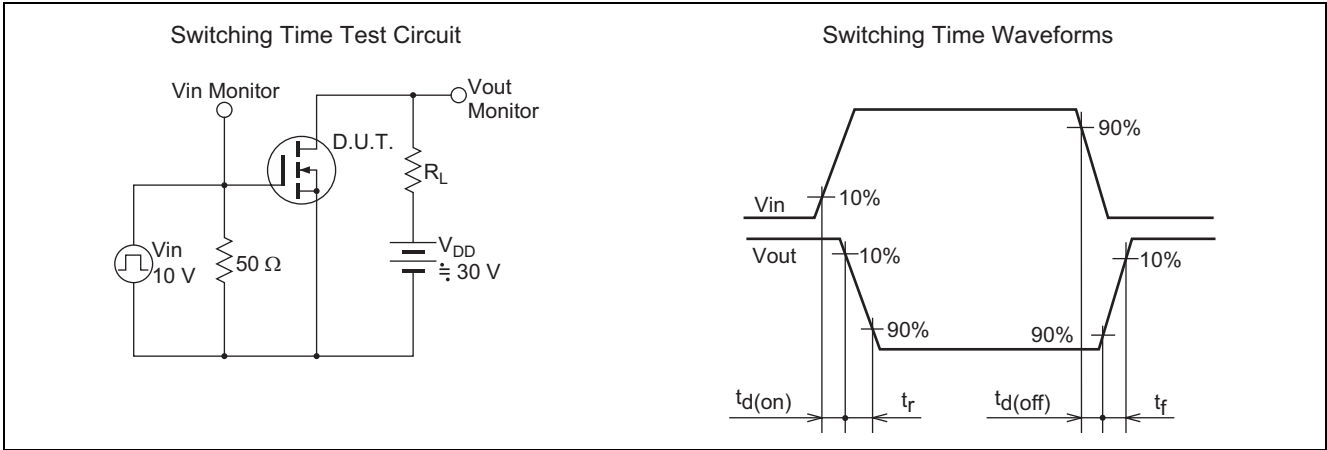


Avalanche Test Circuit

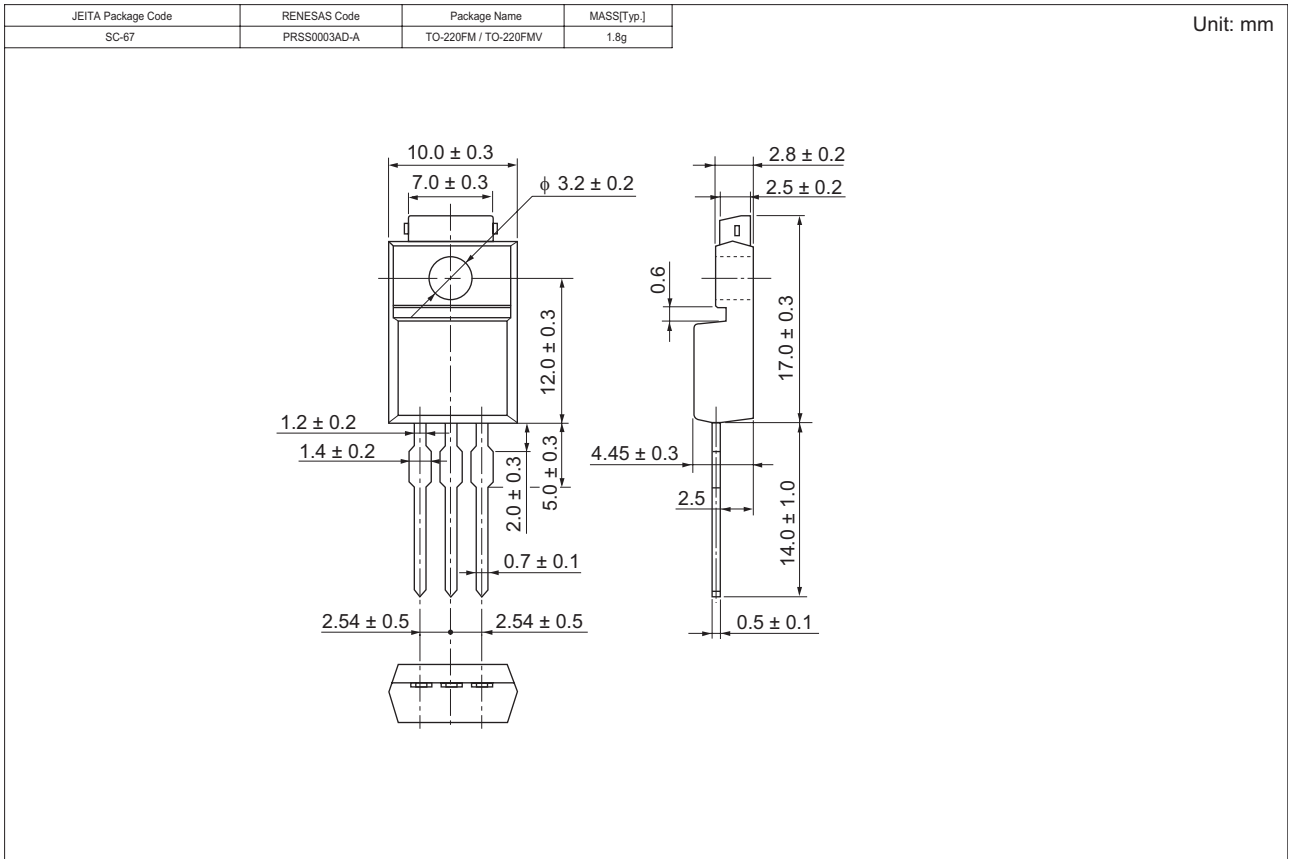


Avalanche Waveform





Package Dimensions



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Part Name	Quantity	Shipping Container
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