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April 1st, 2010 Renesas Electronics Corporation

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MOS FIELD EFFECT TRANSISTOR 2SK3663

N-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

DESCRIPTION

The 2SK3663 is a switching device which can be driven directly by a 2.5 V power source.

The 2SK3663 features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

FEATURES

- 2.5 V drive available
- · Low on-state resistance

RDS(on)1 = 0.57Ω MAX. (VGS = 4.5 V, ID = 0.30 A)

 $R_{DS(on)2} = 0.60 \Omega MAX. (V_{GS} = 4.0 V, I_{D} = 0.30 A)$

RDS(on)3 = 0.88Ω MAX. (Vgs = 2.5 V, ID = 0.15 A)

ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3663	SC-70 (SSP)

Remark Marking: G26

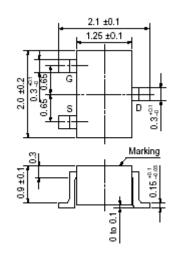
ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	20	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±12	V
Drain Current (DC)	ID (DC)	±0.5	Α
Drain Current (pulse) Note1	D (pulse)	±2.0	Α
Total Power Dissipation Note2	Рт	0.2	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C

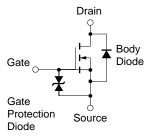
Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Mounted on FR-4 board of 2500 mm² x 1.1 mm

PACKAGE DRAWING (Unit: mm)



EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

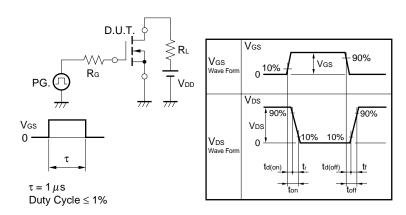
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ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	Ipss	V _{DS} = 20 V, V _{GS} = 0 V			1.0	μΑ
Gate Leakage Current	Igss	Vgs = ±12 V, Vps = 0 V			±10	μΑ
Gate Cut-off Voltage Note	V _{GS(off)}	V _{DS} = 10 V, I _D = 1.0 mA	0.5	1.0	1.5	V
Forward Transfer Admittance ^{Note}	yfs	V _{DS} = 10 V, I _D = 0.30 A	0.25	0.75		S
Drain to Source On-state Resistance Note	RDS(on)1	Vgs = 4.5 V, ID = 0.30 A		0.38	0.57	Ω
	RDS(on)2	Vgs = 4.0 V, ID = 0.30 A		0.41	0.60	Ω
	RDS(on)3	Vgs = 2.5 V, ID = 0.15 A		0.60	0.88	Ω
Input Capacitance	Ciss	V _{DS} = 10 V		28		pF
Output Capacitance	Coss	V _G S = 0 V		11		pF
Reverse Transfer Capacitance	Crss	f = 1.0 MHz		7		pF
Turn-on Delay Time	td(on)	V _{DD} = 10 V, I _D = 0.30 A		20		ns
Rise Time	tr	V _{GS} = 4.0 V		51		ns
Turn-off Delay Time	td off)	R _G = 10 Ω		94		ns
Fall Time	t f			87		ns
Body Diode Forward Voltage	VF(S-D)	IF = 0.5 A, VGS = 0 V		0.87		V

Note Pulsed : PW \leq 350 μ s, Duty Cycle \leq 2%

TEST CIRCUIT SWITCHING TIME



dT - Percentage of Rated Power - %

lo - Drain Current - A

Vesion -Gate Cut-off Voltage - V

20

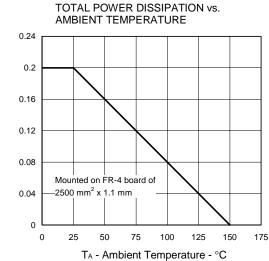
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SAFE OPERATING AREA 120 100 80 60 40

DERATING FACTOR OF FORWARD BIAS





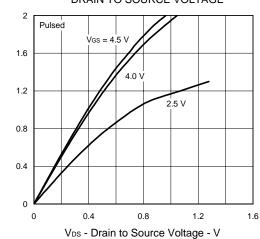
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE

TA - Ambient Temperature - °C

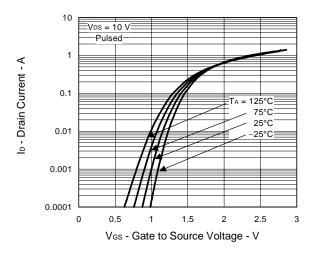
100

125

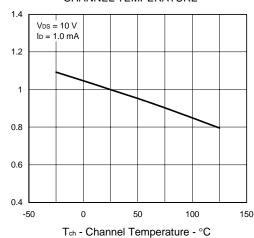
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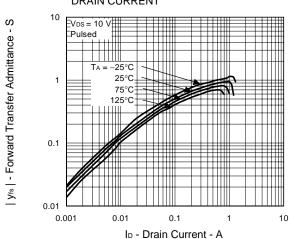
FORWARD TRANSFER CHARACTERISTICS



GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

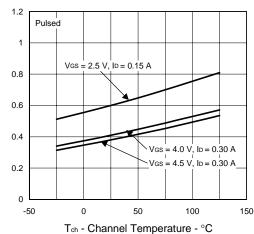


 $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$ - Drain to Source On-state Resistance - Ω

 $\mathsf{Rbs}_{(\varpi)}$ - Drain to Source On-state Resistance - Ω

Ciss, Coss, Cres - Capacitance - pF

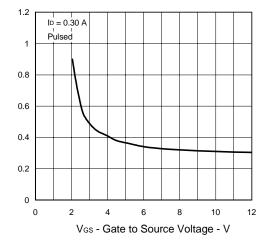
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



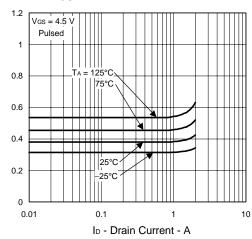
 $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$ - Drain to Source On-state Resistance - Ω

 $\mathsf{R}_{\mathsf{DS}(\varpi)}$ - Drain to Source On-state Resistance - Ω

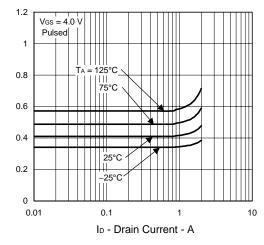
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



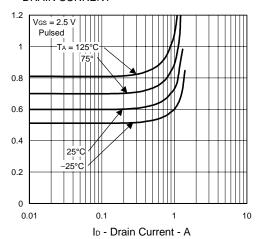
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



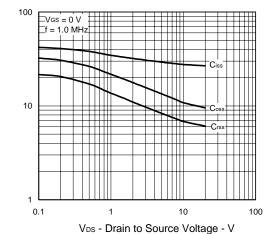
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



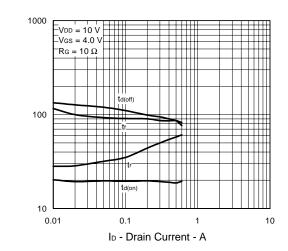
CAPACITANCE vs.
DRAIN TO SOURCE VOLTAGE



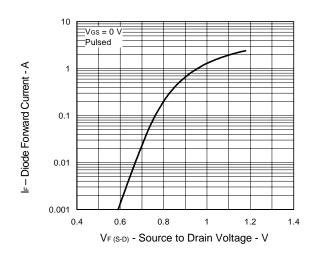
 $\mathsf{R}_{\mathsf{DS}(\varpi)}$ - Drain to Source On-state Resistance - Ω

td(on), tr, td(off), tr - Switching Time - ns

SWITCHING CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE



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