



(0.635 mm) .025"



DED GROUND PLANE SOCKET

SPECIFICATIONS

For complete specifications and recommended PCB layouts see www.samtec.com?QFSS

Insulator Material: Liquid Crystal Polymer Contact, Ground Plane & Shield Material: Phosphor Bronze Plating: Au over 50μ" (1.27 μm) Ni (Tin on Ground Plane tails)

Voltage Rating: 300 VAC mated with QMSS Operating Temp: -55°C to +125°C RoHS Compliant: Yes

Processing: Lead-Free Solderable: Yes SMT Lead Coplanarity: (0.10 mm) .004" max (026-078) **Board Stacking:** For applications requiring more than two connectors per board,

RECOGNITIONS

For complete scope of recognitions see www.samtec.com/quality

contact ipg@samtec.com

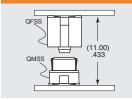


ALSO AVAILABLE (MOQ Required)

- Headers without Alignment Pins
- 8 Power Pins/End
- 4 or 8 Power Pins/End for (2.36 mm) .093" thick board
- Guide Holes
- 64 (-DP) and 104 pins per row

Contact Samtec.

APPLICATION



Note: Patented

Note: Some lengths, styles and options are non-standard, non-returnable.

Board Mates:



Integral guide post

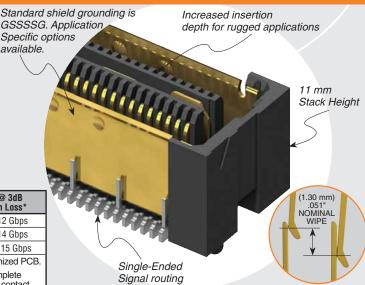
QMSS/QFSS 11 mm Stack Height	Туре	Rated @ 3dB Insertion Loss*
Single-Ended Signaling	-D	6 GHz / 12 Gbps
Differential Pair Signaling	-D	7 GHz / 14 Gbps
Differential Pair Signaling	-D-DP	7.5 GHz / 15 Gbps

*Performance data includes effects of a non-optimized PCB.

Performance data for other stack heights and complete test data available at www.samtec.com?QFSS or contact sig@samtec.com

PINS PER ROW

NO. OF PAIRS

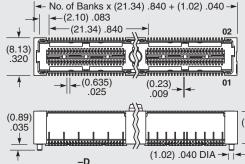


PLATING OPTION

OTHER OPTION

> -PC4 = 4 Power Pins/End (N/A with -A)

-026, -052, -078 (52 total pins per bank = 10µ" (0.25 µm) 40 signals + 12 grounds to shield = -D) **-016, -032, -048** (16 pairs per bank = -D-DP)



(7.06).278 (1.02) .040 DIA → -D-DP (2.54) .100 (2.00) .07874 No. of Banks x (21.34) .840 + (12.87) .507 -PC4 OPTION

Gold on Signal Pins, Shield and **Ground Plane** (Tin on Signal Pin tails, and Ground Plane tails)

(7.44)

(1.78)

= Single-Ended -D-DP = Differential Pair

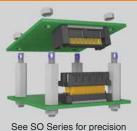
(0.10)

(2.54)

.100

–D

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