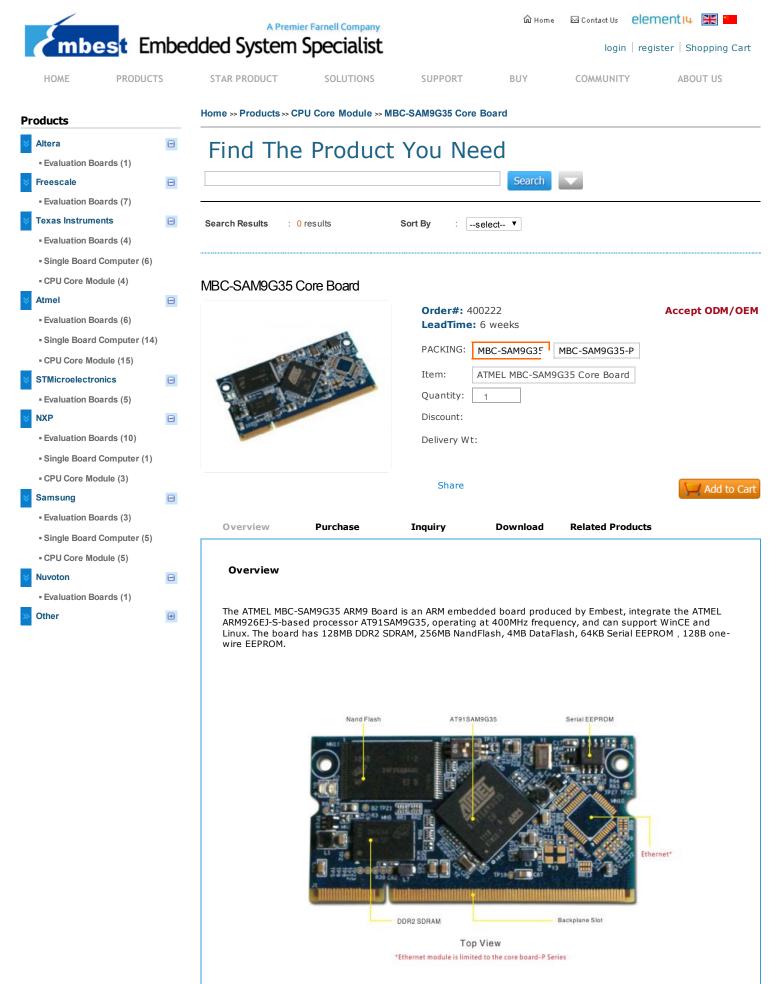
MBC-SAM9G35 Core Board - CPU Core Module - Embedded ARM Evaluation Boards and Tools



Serial Data Flash



1-wire EEPROM

Bottom View

Hardware Features

ARM926EJ-S^m ARM® Thumb® Processor running at up to 400 MHz @ 1.0V +/- 10%

16 Kbyte Data Cache, 16 Kbyte Instruction Cache, Memory Management Unit

One 64 KByte internal ROM embedding bootstrap routine: Boot on NandFlash, SDCard, Dataflash or serial dataflash

Programmable order

One 32 KByte internal SRAM, single-cycle access at system speed

External 128MB DDR2 SDRAM, 256MB NandFlash, 4MB DataFlash, 64KB Serial EEPROM , 128B one-wire EEPROM 10/100Mbps Ethernet MAC controller(only for MBC-SAM9G35-P)

Software Features(updating)

Test routines and resources based on MDK

Project	Description
audio	Audio test
nandflash	Get familiar with the Nandflash operation
fatfs	Get familiar with the basic operation of file system
filesystem	Base on FAT file system and Embedded file system lib implementation
datafalsh	Use SPI interface to access dataflash code efficiency.
twi-eeprom	Applying the twi host to access the eeprom chip
rtt	The application of the real-time/timer
rtc	The application of the real-time clock
twi	The application of twi with the Master and slave
emac	the application of the Ethernet
emac_uip_helloworld	The implementation of web-server and the telnet the device
emac_uip_telnetd	The application of remote access
emac_uip_webserver	The application of the web server
sdmmc	The application of sdcard interface
sdcard	The application of sdcard
fatfs_sdcard	The application of file system and the sdcard
usb_device_core	The application of USB UDPinterface
usb_device_hid_transfer	The application of USB Human Interface Device class
	The application of USB Communication

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usb_device_cdc_serial	The application of USB communication Device class
usb_device_hid_keyboard	Base on the USB HIDdriver, the button test
buzzer	The application of the buzzer
tsensor	The application of the temperature sensor
lcd	The application of the lcd
touchscreen	The application of the touchscreen
Button	Use the interruption to test the button

Linux-based operating system kernel driver source list

Туре	Function	Description	Source code
	AT91Bootstrap	Boot Uboot	\checkmark
		Version:UBoot1.3.4	
Bootloader	Uboot	Functions: 1.Support NandFlash erase, read and write 2.Support image download through NET 3.Support environment variables set and save 4.Support the memory content display, contrast, changes 5.Support bootm and bootargs set	\checkmark
	Kernel	Version: Linux-2.6.30	
	System Clock	System frequency: 400MHz	
	Display driver	Support a variety of different size LCD screen, resolution, adjustable	\checkmark
	Touchscreen	Touchscreen driver	\checkmark
	DM9000	DM9000 driver	\checkmark
	HSMMC	SD/MMC/SDIO driver	\checkmark
	IIC	I2C driver	\checkmark
Kernel and Drivers	SPI	SPI driver	\checkmark
Kerner and Drivers	NANDFLASH	Support 512 Byts small Page, 2K bytes large Page	\checkmark
	SERIAL	Serial driver	\checkmark
	WAVEDEV	Audio driver, support AC97 and IIS, default driver is IIS (WM8731)	\checkmark
	USB Host	Support USB keyboard, mouse and U disk	\checkmark
	DMA	DMA driver	\checkmark
	USB Gadget	USB Gadget driverFucntion: Support a 32MB NandFlash as U disk to use	\checkmark
File System	Jffs2 file system	Support jffs2 file system	
Cross Compiler	arm-none-linux- gnueabi	Cross tool chain	\checkmark
Download tools in	HyperTerminal	Serial debug terminal, USB download the image tool	
PC	SAM-BA1.13+USB	SAM-BA downloads Bootloader and kernel to NandFlash on the board through the USB line	

WinCE-based operating system kernel BSP source list

Туре	Function	Description	Source code
Bootloader	FirstBoot	Boot Eboot, provide source and the last image FIRSTBOOT.nb0	
	Eboot	Provide source and the last image Eboot.nb0 Eboot Function: 1.NET download: Can set Mac address, static IP, dynamic DHCP IP, and download WinCE kernel 2.FormatNandFlash 3.Set startup delay time 4.Set the kernel address in the NandFlash, the address in the RAM, and the size of the kernel	
	Kernel	Version: WinCE6.0	
	System Clock	System frequency: 400MHz	
	Display	LCD driver, Support the 480 * 272 display	\checkmark
odule/mbc-sam9q35-	core-board.html	FEDROM direct	_ /

http://www.embest-tech.com/product/cpu-core-module/mbc-sam9g35-core-board.html

	EEPKUM	EEPKUM airver			
	EMACB	NET driver			
	I2C	I2C bus driver			
	NandFlash	NandFlash driver			
Kernel and Drives	SDHC	Micro SD Card driver			
	Serial	Serial driver			
	Touchscreen	Touchscreen driver			
	USB Host	USB Host driver, support EHCI and OHCI			
	WAVEDEV	Audio, support WM8731			
	DMA	DMA driver			
	USB Device	USB Device driverFunction: Synchronization with PC			
Synchronization Software in PC	Microsoft Activesync	Use to transmit data between PC and board with USB line			
Download tools in	HyperTerminal	Serial debug terminal, USB download the image tool			
Download tools in PC	SAM-BA1.13+USB	SAM-BA downloads Bootloader and kernel to NandFlash on the board through the USB line			
Contact Us					
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