

R5F562TADDFP

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Product Spec

- Basic Information
- Memory
- CPU (Bit, Clock, DMA, External Bus etc)
- Timers
- Analog
- Interfaces
- Operating Conditions

Basic Information	
Product Status	Mass Production
Family	RX
Series	RX600
Group	RX62T

Memory	
Program memory	256 KB Flash memory
RAM	16 KB
Data Flash	32 KB

CPU (Bit, Clock, DMA, External Bus etc)	
CPU	RX (32-bit)
Max. Frequency	100 MHz
On-chip Oscillator Freq.	Low speed 125 kHz
PLL	YES
Power-On Reset	YES
Low Voltage Detection	YES
Floating Point Unit	YES
DMA Remarks	DTC
External Interrupt Pins	9
I/O Ports	76

Timers	
16-bit Timers	16 ch
Watchdog Timers	2 ch
PWM Output	32
3-Phase PWM Output Function	YES

Analog	
A/D Converters	10-bit x 12 ch, 12-bit x 8 ch

Interfaces	
CSIs	3 ch
SPIs	1 ch
UARTs	3 ch
I2Cs	1 ch
LIN	1 ch

Operating Conditions	
Operating Voltage	4 to 5.5 V
Operating Temperature	-40 to 85 °C (Ambient Temperature)

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Order Information

Orderable Part No.	Order	Production Status	Packing	EU RoHS	RoHS-pdf (China-RoHS)
R5F562TADDFP#V3	Buy	Mass Production	Tray	Compliant	English
R5F562TADDFP#V1	Contact	Mass Production	Tray	Compliant	English
R5F562TADDFP#V0	Contact	EOL	-	Compliant	English

Buy : Product can be purchased online.

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Package

Package	
Pins	100
Pin pitch	0.5 mm
Package Type	LFQFP
Body Size	14x14 mm ²
Package drawing	plqp0100kb_a
Mount pad drawing	fig0009e
Mounting height	1.7 mm
Mass	0.6 g
Renesas code	PLQP0100KB-A (old: FP-100UV)
JEITA code	P-LFQFP100-14x14-0.50

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