



**Spec No.: DS22-2013-0154** Effective Date: 09/26/2014

Revision: B

**LITE-ON DCC** 

**RELEASE** 

BNS-OD-FC001/A4

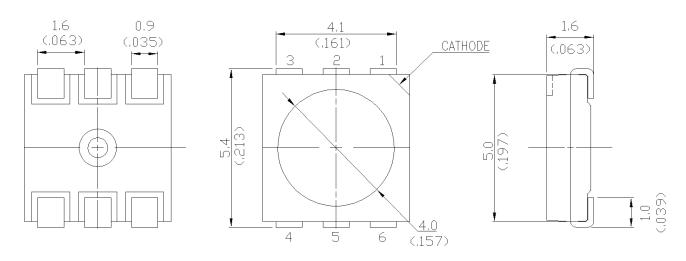


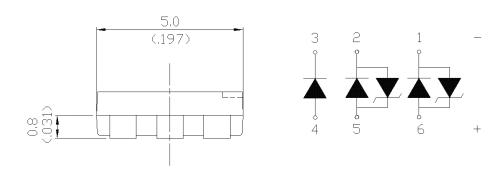
# Property of Lite-On Only

### **Features**

- \* Meet ROHS
- \* Package in 12mm tape on 7" diameter reels.
- \* EIA STD package.
- \* I.C. compatible.
- \* Compatible with automatic placement equipment.
- \* Compatible with infrared and reflow solder process.

# Package Dimensions





Part No.	Lens Color	Source Color	Pin Assignment
		InGaN Blue	1,6
LTST-G563ZEGBW	Diffused Lens	InGaN Green	2,5
		AlInGaP Red	3,4

#### Notes:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is  $\pm 0.2$  mm (.008") unless otherwise noted.

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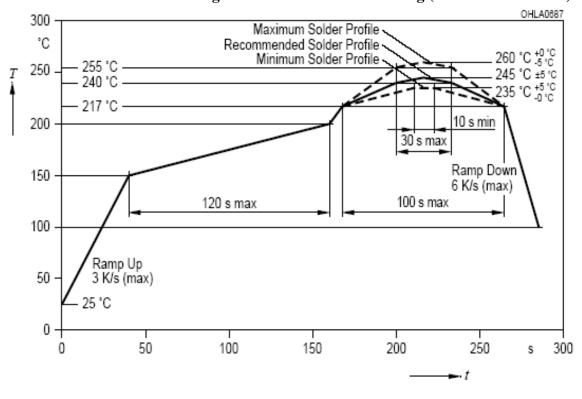
## Property of Lite-On Only

# Absolute Maximum Ratings at Ta=25℃

Parameter	LTS	Unit		
T arameter	Red	Green	Blue	Omt
Power Dissipation	75	105	105	mW
Peak Forward Current (1/10 Duty Cycle, 0.1ms Pulse Width)	80	100	100	mA
DC Forward Current	30	30	30	mA
Operating Temperature Range	-40°C to + 85°C			
Storage Temperature Range	-40°C to + 100°C			

Suggest IR Reflow Condition For Pb Free Process:

# **IR-Reflow Soldering Profile for lead free soldering** (Acc. to J-STD-020B)



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## Property of Lite-On Only

## Electrical / Optical Characteristics at Ta=25℃

Parameter	Symbol		LTS	T-G563ZE0	GBW	Unit	Test
1 arameter	Symbol		Red	Green	Blue		Condition
		MIN.	500	1400	240		
Luminous Intensity	IV	TYP.	-	-	-	mcd	IF = 20mA Note 1
		MAX.	1000	2200	480		
Viewing Angle	201/2	TYP.		120		deg	Note 2 (Fig.5)
Peak Emission Wavelength	λΡ	TYP.	630	523	458	nm	Measurement @Peak (Fig.1)
		MIN.	619	520	460		W 20 1
Dominant Wavelength	λd	TYP.	-	-	-	nm	IF = 20mA Note 3
		MAX.	628	530	470		
Spectral Line Half-Width	Δλ	TYP.	15	25	20	nm	IF = 20mA
		MIN.	1.9	2.9	2.9		
Forward Voltage	VF	TYP.	-	-	-	V	IF = 20mA
		MAX.	2.4	3.4	3.4		
Reverse Current	IR	MAX.	10	-	-	$\mu$ A	VR = 5V Note 4
		MIN.		-	-		
Reverse Voltage	VFz	TYP.		0.8	0.8	V	IFz = 5mA Note 4
		MAX.		-	-		

NOTE: 1. Luminous intensity is measured with a light sensor and filter combination that approximates the CIE eye-response curve.

- $2. \theta 1/2$  is the off-axis angle at which the luminous intensity is half the axial luminous intensity.
- 3. The dominant wavelength,  $\lambda$  d is derived from the CIE chromaticity diagram and represents the single wavelength which defines the color of the device.
- 4. Reverse voltage (VR) condition or Reverse Current (IFz) condition is applied to IR or VFz test only. The device is not designed for reverse operation.

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# Property of Lite-On Only

# **Bin Code List**

Forward Volta	orward Voltage Color: Red, Unit: V@20				
Bin Code	Min.	Max.			
V1	1.90	2.15			
V2	2.15	2.40			
Forward Voltag	ge Color : Green, U	Init: V@20mA			
Bin Code	Min.	Max.			
V5	2.90	3.15			
V6	3.15	3.40			
Forward Volta	Forward Voltage Color : Blue, Unit : V@2				
V5	2.90	3.15			
V6	3.15	3.40			

Tolerance on each Voltage Bin is +/-0.15V

Luminous Intens	ity Color: Red, Unit: mcd @20mA		
Bin Code	Min.	Max.	
1R	500.0	700.0	
2R	700.0	1000.0	
Luminous Intensi	ty Color: Green,	Unit: mcd @20mA	
Bin Code	Min.	Max.	
1G	1400.0	1800.0	
2G	1800.0	2200.0	
Luminous Inten	sity Color : Blue, U	Unit: mcd @20mA	
Bin Code	Min.	Max.	
0B	240.0	480.0	

Tolerance on each Intensity Bin is +/-15%

Dominant Waveler	ngth Color:Red, U	Jnit: mcd @20mA
Bin Code	Min.	Max.
R0	619.0	628.0
Dominant Waveleng	th Color: Green,	Unit: mcd @20mA
Bin Code	Bin Code	Bin Code
AP	520.0	525.0
AQ	525.0	530.0
Dominant Wavelen	gth Color: Blue,	Unit: mcd @20mA
Bin Code	Min.	Max.
AB	460.0	465.0
AC	465.0	470.0

Tolerance for each Dominate Wavelength Bin is +/- 1.5nm

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# Property of Lite-On Only

# **Bin Code List**

Forward Voltage Unit: V@20mA																					
Bin Code	R	ed	Gr	een	B	lue															
On Tag	Code	Range	Code	Range	Code	Range															
A1			V5 2.90-3.15		V5	2.90-3.15															
A2	V1	1.90-2.15	V 3	2.90-3.13	V6	3.15-3.40															
A3	V 1	1.90-2.13	1.90-2.13	1.90-2.13	1.90-2.15	1.90-2.13	1.90-2.13	1.90-2.13	1.90-2.13	1.90-2.13	1.90-2.13	1.90-2.13	1.90-2.13	1.90-2.15	1.90-2.13	1.90-2.13	1.90-2.13	V6	3.15-3.40	V5	2.90-3.15
A4			VO	3.13-3.40	V6	3.15-3.40															
A5			V5	2.90-3.15	V5	2.90-3.15															
A6	V2	2.15-2.40	V 3	2.90-3.13	V6	3.15-3.40															
A7	V Z	2.13-2.40	V/C	2 15 2 40	V5	2.90-3.15															
A8			V6	3.15-3.40	V6	3.15-3.40															

# Tolerance on each Voltage Bin is +/-0.15V

	Lumino	ous Intensity	Un	it: mcd @20	mA	
Bin Code	Red		Gr	een	Bl	ue
On Tag	Code	Range	Code	Range	Code	Range
B1	1D	500-700	1G	1400-1800	0B	240-480
B2	1R	300-700	2G	1800-2200	0B	240-480
В3	2R	700 1000	1G	1400-1800	0B	240-480
B4	ZΚ	700-1000	2G	1800-2200	0B	240-480

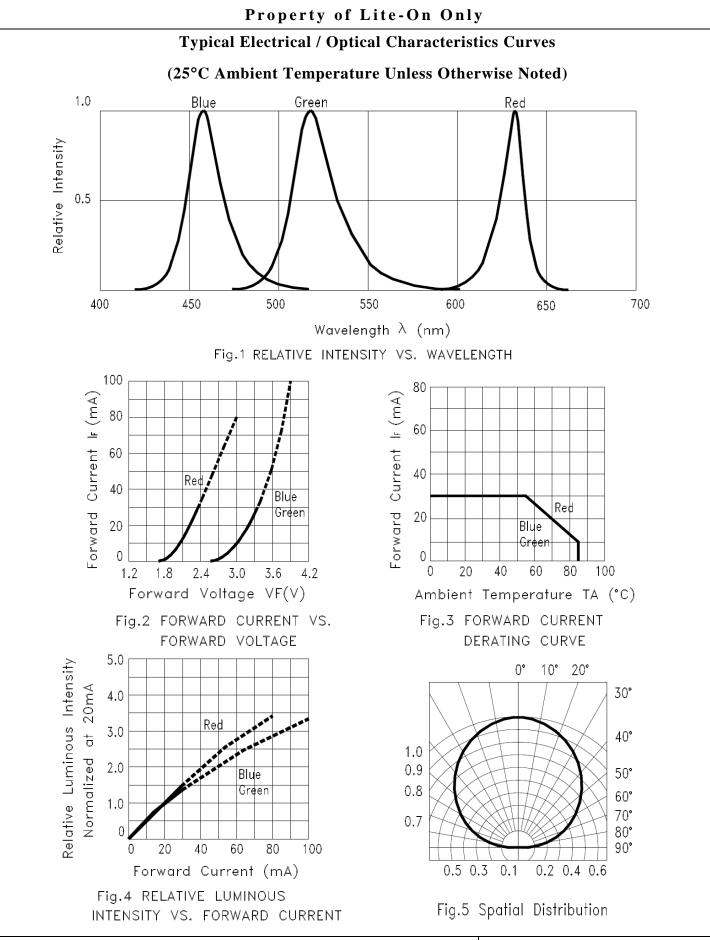
# Tolerance on each Intensity Bin is +/-15%

Dominant Wavelength Unit : nm @20mA										
Bin Code	Re	ed	Gre	een	Bl	ue				
on Tag	Code	Range	Code	Range	Code	Range				
C1			AP	520 525	AB	460-465				
C2	DΩ	610 6 <b>3</b> 0	AP	520-525	AC	465-470				
СЗ	R0	619-628	619-628	619-628	619-628	619-628	4.0	525 520	AB	460-465
C4			AQ	525-530	AC	465-470				

Tolerance for each Dominate Wavelength Bin is +/- 1.5nm

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# Property of Lite-On Only

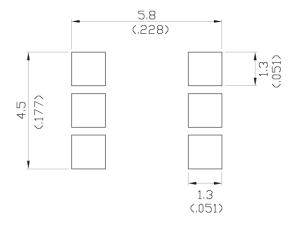
#### **User Guide**

## Cleaning

Do not use unspecified chemical liquid to clean LED they could harm the package. If cleaning is necessary, immerse the LED in ethyl alcohol or isopropyl alcohol at normal temperature for less one minute.

### **Recommend Printed Circuit Board Attachment Pad**

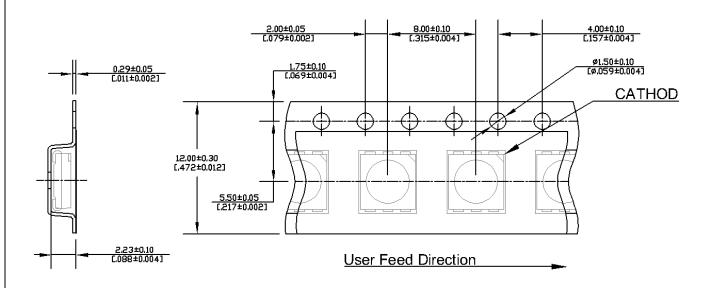
Infrared / vapor phase Reflow Soldering



Note:

1. All dimensions are in millimeters (inches).

## **Package Dimensions Of Tape And Reel**



Note:

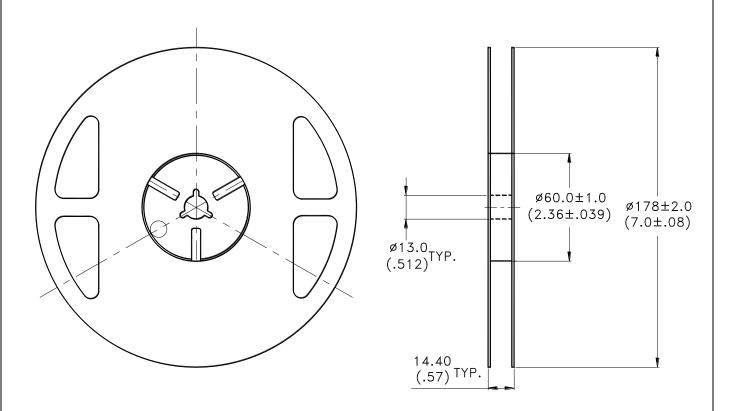
1. All dimensions are in millimeters (inches).

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# Property of Lite-On Only

# **Package Dimensions of Reel**



#### Notes:

- 1. Empty component pockets sealed with top cover tape.
- 2. 7 inch reel-1000 pieces per reel.
- 3. Minimum packing quantity is 500 pieces for remainders.
- 4. The maximum number of consecutive missing lamps is two.
- 5. In accordance with ANSI/EIA 481 specifications.

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## Property of Lite-On Only

### **CAUTIONS**

## 1. Application

The LEDs described here are intended to be used for ordinary electronic equipment (such as office equipment, communication equipment and household applications). Consult Liteon's Sales in advance for information on applications in which exceptional reliability is required, particularly when the failure or malfunction of the LEDs may directly jeopardize life or health (such as in aviation, transportation, traffic control equipment, medical and life support systems and safety devices).

## 2. Storage

The package is sealed:

The LEDs should be stored at 30°C or less and 70%RH or less. And the LEDs are limited to use within one year, while the LEDs is packed in moisture-proof package with the desiccants inside.

The package is opened:

The storage ambient for the LEDs should not exceed 30°C temperature and 60% relative humidity.

It is recommended that LEDs out of their original packaging are IR-reflowed within 168hrs.

For extended storage out of their original packaging, it is recommended that the LEDs be stored in a sealed container with appropriate desiccant, or in a desiccators with nitrogen ambient.

LEDs stored out of their original packaging for more than 168hrs should be baked at about 60 deg C for at least 48 hours before solder assembly.

#### 3. Cleaning

Use alcohol-based cleaning solvents such as isopropyl alcohol to clean the LED if necessary.

## 4. Soldering

Recommended soldering conditions:

Re	low soldering	Soldering iron		
Pre-heat	150~200°C	Temperature	300°C Max.	
Pre-heat time	120 sec. Max.	Soldering time	3 sec. Max.	
Peak temperature	260°C Max.		(one time only)	
Soldering time	10 sec. Max.(Max. two times)			

## **Soldering notes:**

Because different board designs use different number and types of devices, solder pastes, reflow ovens, and circuit boards, no single temperature profile works for all possible combinations.

However, you can successfully mount your packages to the PCB by following the proper guidelines and PCB-specific characterization.

LITE-ON Runs both component-level verification using in-house **KYRAMX98** reflow chambers and board-level assembly.

The results of this testing are verified through post-reflow reliability testing.

Profiles used at LITE-ON are based on JEDEC standards to ensure that all packages can be successfully and reliably surface mounted.

Figure on page3 shows a sample temperature profile compliant to JEDEC standards.

You can use this example as a generic target to set up your reflow process.

You should adhere to the JEDEC profile limits as well as specifications and recommendations from the solder paste manufacturer to avoid damaging the device and create a reliable solder joint.

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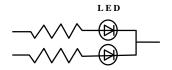
## Property of Lite-On Only

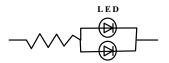
#### 5. Drive Method

An LED is a current-operated device. In order to ensure intensity uniformity on multiple LEDs connected in parallel in an application, it is recommended that a current limiting resistor be incorporated in the drive circuit, in series with each LED as shown in Circuit A below.

#### Circuit model A

#### Circuit model B





- (A) Recommended circuit.
- (B) The brightness of each LED might appear different due to the differences in the I-V characteristics of those LEDs.

# **6. ESD (Electrostatic Discharge)**

Static Electricity or power surge will damage the LED.

Suggestions to prevent ESD damage:

- Use of a conductive wrist band or anti-electrostatic glove when handling these LEDs.
- All devices, equipment, and machinery must be properly grounded.
- Work tables, storage racks, etc. should be properly grounded.
- Use ion blower to neutralize the static charge which might have built up on surface of the LED's plastic lens as a result of friction between LEDs during storage and handling.

ESD-damaged LEDs will exhibit abnormal characteristics such as high reverse leakage current, low forward voltage, or "no lightup" at low currents.

To verify for ESD damage, check for "lightup" and Vf of the suspect LEDs at low currents.

The Vf of "good " LEDs should be >2.0V@0.1mA for InGaN product and >1.4V@0.1mA for AlInGaP product.

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# Property of Lite-On Only

# 7. Reliability Test

No.	Test item	Test condition	Reference standard
1	Resistance to soldering heat	Tsld = 260°C, 10sec. 3 times	JEITA ED-4701 300 301
2	Solderability	Tsld=245± 5°C (Lead Free Solder, Coverage ≥ 95% of the dipped surface)	JEITA ED-4701 300 303
3	Thermal Shock	85 ± 5°C ~ -30°C ± 5°C 30min 30min 100cycles	JEITA ED-4701 300 307
4	Temperature Cycle	-55°C ~ 25°C ~ 100°C ~ 25°C 30min 5min 30min 5min 100cycles	JEITA ED-4701 100 105
5	High Temperature Storage	100°C 1000hrs	JEITA ED-4701 200 201
6	Low Temperature Storage	-55°C 1000hrs	JEITA ED-4701 200 202
7	Temperature Humidity Storage	60°C/90%RH 300hrs	JEITA ED-4701 100 103
8	Room temp life test	25°C, IF: Max current, 1000hrs	

## 8. Others

The appearance and specifications of the product may be modified for improvement without prior notice.

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## Property of Lite-On Only

## 9. Suggested Checking List

## Training and Certification

- 1. Everyone working in a static-safe area is ESD-certified?
- 2. Training records kept and re-certification dates monitored?

#### Static-Safe Workstation & Work Areas

- 1. Static-safe workstation or work-areas have ESD signs?
- 2. All surfaces and objects at all static-safe workstation and within 1 ft measure less than 100V?
- 3. All ionizer activated, positioned towards the units?
- 4. Each work surface mats grounding is good?

## Personnel Grounding

- 1. Every person (including visitors) handling ESD sensitive (ESDS) items wears wrist strap, heel strap or conductive shoes with conductive flooring?
- 2. If conductive footwear used, conductive flooring also present where operator stand or walk?
- 3. Garments, hairs or anything closer than 1 ft to ESD items measure less than 100V\*?
- 4. Every wrist strap or heel strap/conductive shoes checked daily and result recorded for all DLs?
- 5. All wrist strap or heel strap checkers calibration up to date? Note: \*50V for Blue LED.

## **Device Handling**

- 1. Every ESDS items identified by EIA-471 labels on item or packaging?
- 2. All ESDS items completely inside properly closed static-shielding containers when not at static-safe workstation?
- 3. No static charge generators (e.g. plastics) inside shielding containers with ESDS items?
- 4. All flexible conductive and dissipative package materials inspected before reuse or recycles?

#### Others

- 1. Audit result reported to entity ESD control coordinator?
- 2. Corrective action from previous audits completed?
- 3. Are audit records complete and on file?

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# **X-ON Electronics**

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