## MC10H141

## Four-Bit Universal Shift Register

## Description

The MC10H141 is a four-bit universal shift register. This device is a functional/pinout duplication of the standard MECL $10 \mathrm{~K}^{\mathrm{TM}}$ part with $100 \%$ improvement in propagation delay and operation frequency and no increase in power supply current.

## Features

- Shift frequency, 250 MHz Min
- Power Dissipation, 425 mW Typical
- Improved Noise Margin 150 mV
(Over Operating Voltage and Temperature Range)
- Voltage Compensated
- MECL 10K Compatible
- These Devices are Pb-Free, Halogen Free and are RoHS Compliant

Table 1. TRUTH TABLE

| SELECT |  | OPERATING MODE | OUTPUTS |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| S1 | S2 |  | $\mathrm{QO}_{\mathrm{n}+1}$ | Q1 ${ }_{n+1}$ | Q2 ${ }_{\text {n }+1}$ | Q3 ${ }_{n+1}$ |
| L | L | Parallel Entry | D0 | D1 | D2 | D3 |
| L | H | Shift Right* | Q1n | Q2n | Q3n | DR |
| H | L | Shift Left* | DL | Q0 ${ }_{\text {n }}$ | Q1n | Q2 ${ }_{\text {n }}$ |
| H | H | Stop Shift | Q0n | Q1n | Q2 ${ }^{\text {n }}$ | $32_{n}$ |

* Outputs as exist after pulse appears at "C" input with input conditions as shown (Pulse Positive transition of clock input).


Pin assignment is for Dual-in-Line Package.

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MARKING DIAGRAMS*


PLLC-20

$$
\begin{array}{ll}
\text { A } & =\text { Assembly Location } \\
\text { WL, L } & =\text { Wafer Lot } \\
\text { YY, Y } & =\text { Year } \\
\text { WW, W } & =\text { Work Week } \\
\text { G } & =\text { Pb-Free Package }
\end{array}
$$

*For additional marking information, refer to Application Note AND8002/D.

ORDERING INFORMATION

| Device | Package | Shipping $\dagger$ |
| :---: | :---: | :---: |
| MC10H141FNR2G | PLLC-20 <br> (Pb-Free) | 500 Tape \& Reel |
| MC10H141PG | PDIP-16 <br> (Pb-Free) | 25 Units / Tube |

$\dagger$ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Figure 1. Pin Assignment

## MC10H141

Table 2. MAXIMUM RATINGS

| Symbol | Characteristic | Rating | Unit |
| :---: | :--- | :---: | :---: |
| $\mathrm{V}_{\mathrm{EE}}$ | Power Supply $\left(\mathrm{V}_{\mathrm{CC}}=0\right)$ | -8.0 to 0 | Vdc |
| $\mathrm{V}_{\mathrm{I}}$ | Input Voltage $\left(\mathrm{V}_{\mathrm{CC}}=0\right)$ | 0 to $\mathrm{V}_{\mathrm{EE}}$ | Vdc |
| $\mathrm{I}_{\text {out }}$ | Output Current <br> - Continuous <br> - Surge | 50 | mA |
| $\mathrm{~T}_{\mathrm{A}}$ | Operating Temperature Range | 100 |  |
| $\mathrm{~T}_{\text {stg }}$ | Storage Temperature Range <br> - Plastic <br> - Ceramic | 0 to +75 | ${ }^{\circ} \mathrm{C}$ |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Table 3. ELECTRICAL CHARACTERISTICS $\left(\mathrm{V}_{\mathrm{EE}}=-5.2 \mathrm{~V} \pm 5 \%\right.$ (Note 1 )

| Symbol | Characteristic | $0^{\circ}$ |  | $25^{\circ}$ |  | $75^{\circ}$ |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min | Max | Min | Max | Min | Max |  |
| $\mathrm{I}_{\mathrm{E}}$ | Power Supply Current | - | 112 | - | 102 | - | 112 | mA |
| $\mathrm{I}_{\mathrm{inH}}$ | Input Current High <br> Pins 5,6,9,11,12,13 <br> Pins 7,10 <br> Pin 4 | - | $\begin{aligned} & 405 \\ & 416 \\ & 510 \end{aligned}$ | - - - | $\begin{aligned} & 255 \\ & 260 \\ & 320 \end{aligned}$ | - | $\begin{aligned} & 255 \\ & 260 \\ & 320 \end{aligned}$ | $\mu \mathrm{A}$ |
| $\mathrm{l}_{\text {inL }}$ | Input Current Low | 0.5 | - | 0.5 | - | 0.3 | - | $\mu \mathrm{A}$ |
| $\mathrm{V}_{\mathrm{OH}}$ | High Output Voltage | -1.02 | -0.84 | -0.98 | -0.81 | -0.92 | -0.735 | Vdc |
| $\mathrm{V}_{\mathrm{OL}}$ | Low Output Voltage | -1.95 | -1.63 | -1.95 | -1.63 | -1.95 | -1.60 | Vdc |
| $\mathrm{V}_{\mathrm{IH}}$ | High Input Voltage | -1.17 | -0.84 | -1.13 | -0.81 | -1.07 | -0.735 | Vdc |
| $\mathrm{V}_{\text {IL }}$ | Low Input Voltage | -1.95 | -1.48 | -1.95 | -1.48 | -1.95 | -1.45 | Vdc |

1. Each MECL $10 \mathrm{H}^{\text {TM }}$ series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 linear fpm is maintained. Outputs are terminated through a $50 \Omega$ resistor to -2.0 V .

Table 4. AC PARAMETERS

| $\mathrm{t}_{\mathrm{pd}}$ | Propagation Delay | 1.0 | 2.0 | 1.0 | 2.0 | 1.1 | 2.1 | ns |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $t_{\text {hold }}$ | Hold Time Data, Select | 1.0 | - | 1.0 | - | 1.0 | - | ns |
| $t_{\text {set }}$ | Set-up Time Data Select | $\begin{aligned} & 1.5 \\ & 3.0 \end{aligned}$ | - | $\begin{aligned} & 1.5 \\ & 3.0 \end{aligned}$ | - | $\begin{aligned} & 1.5 \\ & 3.0 \end{aligned}$ | - | ns |
| $t_{r}$ | Rise Time | 0.5 | 2.4 | 0.5 | 2.4 | 0.5 | 2.4 | ns |
| $\mathrm{t}_{\mathrm{f}}$ | Fall Time | 0.5 | 2.4 | 0.5 | 2.4 | 0.5 | 2.4 | ns |
| $\mathrm{f}_{\text {shift }}$ | Shift Frequency | 250 | - | 250 | - | 250 | - | MHz |

NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit board with maintained transverse airflow greater than 500 lfpm. Electrical parameters are guaranteed only over the declared operating temperature range. Functional operation of the device exceeding these conditions is not implied. Device specification limit values are applied individually under normal operating conditions and not valid simultaneously.

## MC10H141

## LOGIC DIAGRAM



## APPLICATION INFORMATION

The MC10H141 is a four-bit universal shift register which performs shift left, or shift right, serial/parallel in, and serial/parallel out operations with no external gating. Inputs S1 and S2 control the four possible operations of the register without external gating of the clock. The flip-flops shift
information on the positive edge of the clock. The four operations are stop shift, shift left, shift right, and parallel entry of data. The other six inputs are all data type inputs; four for parallel entry data, and one for shifting in from the left (DL) and one for shifting in from the right (DR).

## MC10H141

## PACKAGE DIMENSIONS



NOTES:

1. DIMENSIONS AND TOLERANCING PER ANSI Y14.5M, 1982.
2. DIMENSIONS IN INCHES
3. DATUMS -L-, -M-, AND -N- DETERMINED WHERE TOP OF LEAD SHOULDER EXITS PLASTIC BODY AT MOLD PARTING LINE.
4. DIMENSION G1, TRUE POSITION TO BE MEASURED AT DATUM -T-, SEATING PLANE
5. DIMENSIONS R AND U DO NOT INCLUDE MOLD FLASH ALLOWABLE MOLD FLASH IS 0.010 (0.250) PER SIDE.
6. DIMENSIONS IN THE PACKAGE TOP MAY BE SMALLER THAN THE PACKAGE BOTTOM BY UP TO 0.012 (0.300). THAN THE PACKAGE BOTTOM BY UP TO 0.012 (0.300)
DIMENSIONS R AND U ARE DETERMINED AT THE DIMENSIONS R AND U ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY
EXCLUSIVE OF MOLD FLASH, TIE BAR BURRS, GATE EXCLUSIVE OF MOLD FLASH, TIE BAR BURRS, GATE
BURRS AND INTERLEAD FLASH, BUT INCLUDING ANY BURRS AND INTERLEAD FLASH, BUT INCLUDING ANY
MISMATCH BETWEEN THE TOP AND BOTTOM OF THE PLASTIC BODY
7. DIMENSION H DOES NOT INCLUDE DAMBAR PROTRUSION OR INTRUSION. THE DAMBAR PROTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO BE GREATER THAN 0.037 ( 0.940 ). THE DAMBAR INTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO

|  | INCHES |  | MILLIMETERS |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| DIM | MIN | MAX | MIN | MAX |  |  |
| A | 0.385 | 0.395 | 9.78 | 10.03 |  |  |
| B | 0.385 | 0.395 | 9.78 | 10.03 |  |  |
| C | 0.165 | 0.180 | 4.20 | 4.57 |  |  |
| E | 0.090 | 0.110 | 2.29 | 2.79 |  |  |
| F | 0.013 | 0.021 | 0.33 | 0.53 |  |  |
| G | 0.050 |  | BSC | 1.27 |  | BSC |
| H | 0.026 | 0.032 | 0.66 | 0.81 |  |  |
| J | 0.020 | --- | 0.51 | --- |  |  |
| K | 0.025 | --- | 0.64 | --- |  |  |
| R | 0.350 | 0.356 | 8.89 | 9.04 |  |  |
| U | 0.350 | 0.356 | 8.89 | 9.04 |  |  |
| V | 0.042 | 0.048 | 1.07 | 1.21 |  |  |
| W | 0.042 | 0.048 | 1.07 | 1.21 |  |  |
| $\mathbf{X}$ | 0.042 | 0.056 | 1.07 | 1.42 |  |  |
| Y | --- | 0.020 | --- | 0.50 |  |  |
| $\mathbf{Z}$ | $2^{\circ}$ | $10^{\circ}$ | $2^{\circ}$ | $10^{\circ}$ |  |  |
| G1 | 0.310 | 0.330 | 7.88 | 8.38 |  |  |
| K1 | 0.040 | --- | 1.02 | --- |  |  | BE SMALLER THAN 0.025 (0.635).

## MC10H141

## PACKAGE DIMENSIONS

PDIP-16<br>P SUFFIX<br>CASE 648-08<br>ISSUE V

| STYLE 1: |  | STYLE 2: |
| ---: | ---: | :--- |
| PIN 1. |  |  | CATHODE | PIN 1. | COMMON DRAIN |
| ---: | :--- |
| 2. | CATHODE |
| 3. | CATHODE |

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TPIC6C595PWG4 74VHC164MTCX CD74HC195M96 CD4073BM96 CD4053BM96 MM74HC595MTCX 74HCT164T14-13
74HCT164S14-13 74HC4094D-Q100J NLV14014BFELG NLV74HC165ADR2G NLV74HC589ADTR2G NPIC6C595D-Q100, 11 NPIC6C595PW,118 NPIC6C596ADJ NPIC6C596APW-Q100J NPIC6C596D-Q100,11 BU4094BCF-E2 BU4094BCFV-E2 74HC164D14

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