TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VCX00FT, TC74VCX00FK

Low-Voltage Quad 2-Input NAND Gate with 3.6-V Tolerant Inputs and Outputs

The TC74VCX00FT/FK is a high-performance CMOS 2-input NAND gate which is guaranteed to operate from 1.2-V to 3.6-V. Designed for use in 1.5V, 1.8V, 2.5V or 3.3V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

It is also designed with overvoltage tolerant inputs and outputs up to $3.6\ V\!.$

All inputs are equipped with protection circuits against static discharge.

Features (Note)

- Low-voltage operation: VCC = 1.2 to 3.6 V
- High-speed operation: $t_{pd} = 2.8 \text{ ns (max)} (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$

: $t_{pd} = 3.7 \text{ ns (max) (V}_{CC} = 2.3 \text{ to } 2.7 \text{ V})$

 $t_{pd} = 7.4 \text{ ns (max) (V}_{CC} = 1.65 \text{ to } 1.95 \text{ V}$

 $t_{pd} = 14.8 \text{ ns (max) (VCC} = 1.4 \text{ to } 1.6 \text{ V})$

 $: t_{pd} = 37.0 \text{ ns (max) (V}_{CC} = 1.2 \text{ V})$

• Output current: $I_{OH}/I_{OL} = \pm 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$

 $: I_{OH}/I_{OL} = \pm 18 \text{ mA (min) (V}_{CC} = 2.3 \text{ V)}$

 $: I_{OH}/I_{OL} = \pm 6 \text{ mA (min) (V}_{CC} = 1.65 \text{ V)}$

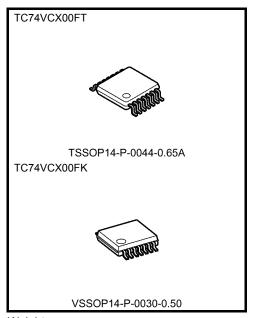
TOTIFIOE =0 IIII (IIIII) (VCC 1.00 V

: $I_{OH}/I_{OL} = \pm 2$ mA (min) ($V_{CC} = 1.4$ V)

- Latch-up performance: -300 mA
- ESD performance: Machine model ≥ ±200 V

Human body model $\geq \pm 2000 \text{ V}$

- Package: TSSOP and VSSOP (US)
- Power-down protection provided on all inputs and outputs

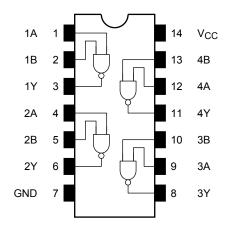


Weight

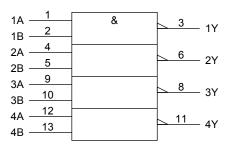
TSSOP14-P-0044-0.65A : 0.06 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

Note: Electrical Characteristics of V_{CC}=1.5±0.1V and 1.2V apply only to products whose Lot Code is over "3 12"

Pin Assignment (top view)



IEC Logic Symbol



Truth Table

Inp	uts	Outputs		
Α	В	Y		
L	L	Н		
L	Н	Н		
Н	L	Н		
Н	Н	L		

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5 to 4.6	V
DC input voltage	V _{IN}	-0.5 to 4.6	V
DC output voltage	Vour	-0.5 to 4.6 (Note 2)	V
DC output voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 3)	v
Input diode current	I _{IK}	-50	mA
Output diode current	lok	±50 (Note 4)	mA
DC output current	lout	±50	mA
Power dissipation	PD	180	mW
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: $V_{CC} = 0 V$

Note 3: High or low state. I_{OUT} absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND, V_{OUT} > V_{CC}$



Operating Ranges (Note 1)

Characteristics	Characteristics Symbol Rating		Unit	
Power supply voltage	V _{CC}	1.2 to 3.6	V	
Input voltage	V _{IN}	-0.3 to 3.6	V	
Output voltage	Vour	0 to 3.6 (Note 2)	V	
Output voltage	Vout	0 to V _{CC} (Note 3)	v	
		±24 (Note 4)	A	
Output current	IOH/IOI	±18 (Note 5)		
Output current	IOH/IOL	±6 (Note 6)	mA	
		±2 (Note 7)		
Operating temperature	T _{opr}	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 8)	ns/V	

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Note 2: $V_{CC} = 0 V$

Note 3: High or low state

Note 4: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ Note 5: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 6: $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$

Note 7: $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$

Note 8: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics (Ta = -40 to 85° C, 2.7 V < V_{CC} ≤ 3.6 V)

Characte	eristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
Input voltage	H-level	V _{IH}		_	2.7 to 3.6	2.0	_	V
Input voltage	L-level	V _{IL}		_	2.7 to 3.6	_	8.0	V
H-level Output voltage			$I_{OH} = -100 \mu A$	2.7 to 3.6	V _{CC} - 0.2	_		
	V _{OH}	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -12 \text{ mA}$	2.7	2.2	_		
			$I_{OH} = -18 \text{ mA}$	3.0	2.4	_		
				I _{OH} = -24 mA	3.0	2.2	_	V
			$I_{OL} = 100 \mu A$	2.7 to 3.6	_	0.2		
	L-level	Voi	$V_{IN} = V_{IH}$	I _{OL} = 12 mA	2.7		0.4	
	L-level	V _{OL}		I _{OL} = 18 mA	3.0	_	0.4	
				I _{OL} = 24 mA	3.0	_	0.55	
Input leakage curr	ent	I _{IN}	$V_{IN} = 0$ to 3.6 V		2.7 to 3.6		±5.0	μА
Power-off leakage	current	loff	V _{IN} , V _{OUT} = 0 to 3.6 V		0		10.0	μА
Quiescent supply current		laa	V _{IN} = V _{CC} or GND		2.7 to 3.6		20.0	
		Icc	V _{CC} ≤ V _{IN} ≤ 3.6 V		2.7 to 3.6		±20.0	μΑ
Increase in I _{CC} pe	er input	Δl _{CC}	$V_{IH} = V_{CC} - 0.6 V$		2.7 to 3.6	_	750	

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DC Characteristics (Ta = -40 to 85°C, 2.3 V \leq V_{CC} \leq 2.7 V)

Characte	Characteristics		Test (Test Condition			Max	Unit
Onaracio			1031	rest condition		Min	IVIAX	Offic
Input voltage	H-level	V _{IH}		_	2.3 to 2.7	1.6	_	V
input voltage	L-level	V _{IL}		_	2.3 to 2.7	_	0.7	v
H-level Output voltage			I _{OH} = -100 μA	2.3 to 2.7	V _{CC} - 0.2	_		
	V _{OH}	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -6 \text{ mA}$	2.3	2.0			
				$I_{OH} = -12 \text{ mA}$	2.3	1.8		V
				$I_{OH} = -18 \text{ mA}$	2.3	1.7		
			$V_{IN} = V_{IH}$	$I_{OL} = 100 \ \mu A$	2.3 to 2.7	_	0.2	
	L-level	V_{OL}		$I_{OL} = 12 \text{ mA}$	2.3	_	0.4	
				$I_{OL} = 18 \text{ mA}$	2.3	_	0.6	
Input leakage curi	Input leakage current		$V_{IN} = 0$ to 3.6 V		2.3 to 2.7	_	±5.0	μА
Power-off leakage	current	ent I _{OFF} V _{IN} , V _{OUT} = 0 to 3.6 V		0	_	10.0	μА	
Quioscont supply	Outroped supply supply	loo	V _{IN} = V _{CC} or GND		2.3 to 2.7	_	20.0	
Quiescent supply	Current	Icc	$V_{CC} \le V_{IN} \le 3.6 \text{ V}$		2.3 to 2.7	_	±20.0	μΑ

DC Characteristics (Ta = -40 to 85° C, 1.65 V \leq V_{CC} < 2.3 V)

Characte	ristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
Input voltage	H-level	V _{IH}	_	-	1.65 to 2.3	0.65 × V _{CC}	_	V
input voltage	L-level	V _{IL}	_	-	1.65 to 2.3	ı	0.2 × V _{CC}	V
	H-level	V _{OH}	$V_{IN} = V_{IH}$ or V_{IL}	$I_{OH} = -100 \mu A$	1.65	V _{CC} - 0.2	_	V
Output voltage				I _{OH} = -6 mA	1.65	1.25	_	
	L-level		$V_{IN} = V_{IH}$	$I_{OL} = 100 \mu A$	1.65	_	0.2	
	L-level	V _{OL}		I _{OL} = 6 mA	1.65	_	0.3	
Input leakage curr	ent	I _{IN}	V _{IN} = 0 to 3.6 V	V _{IN} = 0 to 3.6 V		_	±5.0	μА
Power-off leakage	current	loff	V_{IN} , $V_{OUT} = 0$ to 3.6 V		0	_	10.0	μА
Quiescent supply current		loo	V _{IN} = V _{CC} or GND		1.65 to 2.3	_	20.0	
Quiescent supply	Current	Icc	V _{CC} ≤ V _{IN} ≤ 3.6 V		1.65 to 2.3	_	±20.0	μА



DC Characteristics (Ta = -40 to 85° C, 1.4 V \leq V_{CC} < 1.65 V)

Characte	Characteristics		Test C	ondition		Min	Max	Unit
Onaraote			10010	rest condition		14	Wax	Onic
Input voltage	H-level	V _{IH}	-	_	1.4 to 1.65	0.65 × V _{CC}	_	V
input voitage	L-level	VIL	_		1.4 to 1.65		0.05 × V _{CC}	•
H-level	Voh	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -100 \mu A$	1.4 to 1.65	V _{CC} - 0.2	_		
Output voltage	Output voltage			$I_{OH} = -2 \text{ mA}$	1.4	1.05	_	V
	L-level	V _{OL}	$V_{IN} = V_{IH}$	$I_{OL} = 100 \ \mu A$	1.4 to 1.65	_	0.05	
	L-level	VOL		I _{OL} = 2 mA	1.4	_	0.35	
Input leakage curi	rent	I _{IN}	V _{IN} = 0 to 3.6 V	V _{IN} = 0 to 3.6 V		_	±5.0	μА
Power-off leakage	current	l _{OFF}	V_{IN} , $V_{OUT} = 0$ to 3.6 V		0	_	10.0	μΑ
Quiescent supply current		loo	V _{IN} = V _{CC} or GND		1.4 to 1.65	_	20.0	^
Quiescent supply	Current	Icc	V _{CC} ≤ V _{IN} ≤ 3.6 V		1.4 to 1.65	_	±20.0	μΑ

DC Characteristics (Ta = -40 to 85° C, 1.2 V \leq V_{CC} < 1.4 V)

Characte	ristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
Input voltage	H-level	V _{IH}	_	_		0.8 × V _{CC}	_	V
input voitage	L-level	V _{IL}	_		1.2 to 1.4		0.05 × V _{CC}	V
Output voltage	H-level	V _{OH}	$V_{IN} = V_{IH}$ or V_{IL}	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $I_{OH} = -100 \mu A$		V _{CC} - 0.1	_	V
	L-level	V _{OL}	$V_{IN} = V_{IH}$	I _{OL} = 100 μA	1.2	_	0.05	
Input leakage curr	ent	I _{IN}	$V_{IN} = 0$ to 3.6 V	V _{IN} = 0 to 3.6 V			±5.0	μΑ
Power-off leakage	current	I _{OFF}	V_{IN} , $V_{OUT} = 0$ to 3.6 V		0	_	10.0	μΑ
Quiescent supply current		loo	V _{IN} = V _{CC} or GND		1.2		20.0	μА
Quiescent suppry	Julielit	Icc	$V_{CC} \le V_{IN} \le 3.6 \text{ V}$		1.2		±20.0	μΑ

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AC Characteristics (Ta = -40 to 85°C, input: $t_r = t_f = 2.0$ ns) (Note 1)

Characteristics	Symbol	Test	Test Condition V _{CC} (V)			Max	Unit
			$C_{\parallel} = 15 \text{ pF}, R_{\parallel} = 2 \text{ k}\Omega$	1.2	1.5	37.0	
Propagation delay time	.		Ο[- 13 β1 , Ν[- 2 κΩ2	1.5 ± 0.1	1.0	14.8	
		Figure 1, Figure 2		1.8 ± 0.15	1.5	7.4	ns
	үнс		$C_L = 30 \text{ pF}, R_L = 500 \Omega$	2.5 ± 0.2	0.8	3.7	
				3.3 ± 0.3	0.6	2.8	
			C _L = 15 pF, R _L = 2 kΩ	1.2		1.5	
	•			1.5 ± 0.1		1.5	
Output to output skew	t _{osLH} t _{osHL}	(Note 2)	$C_L = 30 \text{ pF}, R_L = 500 \Omega$	1.8 ± 0.15		0.5	ns
				2.5 ± 0.2		0.5	
				3.3 ± 0.3		0.5	

Note 1: For $C_L = 50 \ pF$, add approximately 300 ps to the AC maximum specification.

Note 2: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{pLHm} - t_{pLHn}|, t_{OSHL} = |t_{pHLm} - t_{pHLn}|)$

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
		V _{IH} = 1.8 V, V _{IL} = 0 V (No	e) 1.8	0.25	
Quiet output maximum dynamic V _{OL}	V _{OLP}	V _{IH} = 2.5 V, V _{IL} = 0 V (No	e) 2.5	0.6	V
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 3.3	8.0	
	V _{OLV}	$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 1.8	-0.25	
Quiet output minimum dynamic $V_{\hbox{\scriptsize OL}}$		$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 2.5	-0.6	V
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 3.3	-0.8	
		$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 1.8	1.5	
Quiet output minimum dynamic V _{OH}	V _{OHV}	$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 2.5	1.9	V
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 3.3	2.2	

Note: Parameter guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol		Test Condition		V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}		_		1.8, 2.5, 3.3	6	pF
Power dissipation capacitance	C _{PD}	$f_{IN} = 10 \text{ MHz}$		(Note)	1.8, 2.5, 3.3	20	pF

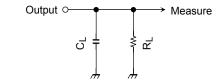
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 \text{ (per gate)}$

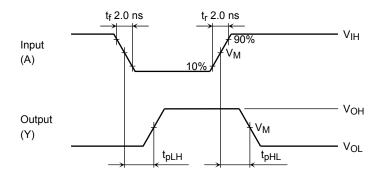
AC Test Circuit



	V _{CC}	
Symbol	$\begin{array}{c} 3.3 \pm 0.3 \text{ V} \\ 2.5 \pm 0.2 \text{ V} \\ 1.8 \pm 0.15 \text{ V} \end{array}$	1.5 ± 0.1 V 1.2V
R_L	500 Ω	2 kΩ
CL	30 pF	15 pF

Figure 1

AC Waveform



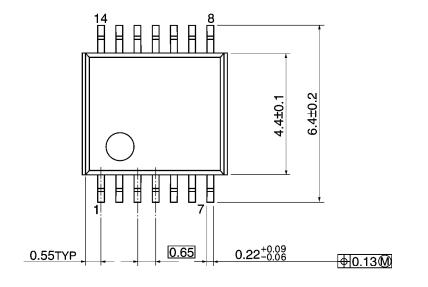
Symbol	Vcc				
	$3.3\pm0.3~\textrm{V}$	$2.5\pm0.2~\textrm{V}$	1.8 ± 0.15 V	$1.5\pm0.1~\textrm{V}$	1.2 V
V _{IH}	2.7 V	V _{CC}	V _{CC}	V _{CC}	V _{CC}
V_{M}	1.5 V	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2

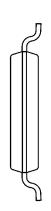
Figure 2 t_{pLH}, t_{pHL}

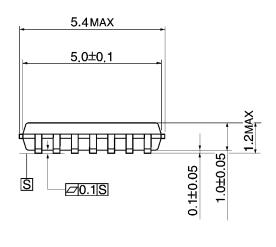
Package Dimensions

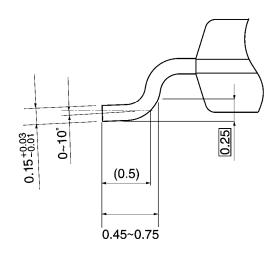
TSSOP14-P-0044-0.65A

Unit: mm





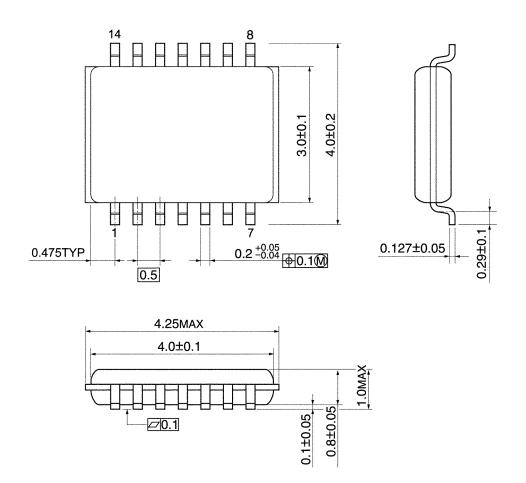




Weight: 0.06 g (typ.)

Package Dimensions

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

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