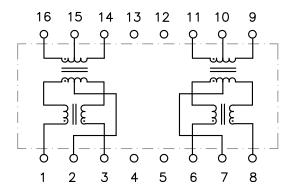
PART NO.: TG110-ESV01N6RL



16PIN FAST ETHERNET ISOLATION MODULE
RoHS COMPLIANT
COMPATIBLE TO LEAD—FREE SOLDERING PROCESS
CONDITION PER IPC/JEDEC J—STD—020C
UL/EU60950 AND DEMKO RECOGNIZED
ENHANCED SHOCK/VIBRATION PERFORMANCE
EXTENDED OPERATING TEMPERATURE —40/+85°C
STORAGE TEMPERATURE —57/+125°C



ELECTRICAL SPECIFICATIONS @25° C

TURNS RATIO P1-3:P16-14 P6-8:P11-9	1CT:1CT ±3% 1CT:1CT ±3%
OCL (100KHz,0.1Vrms,8mA) P16-14,P11-9	350µH min*
DCR P16-14,P11-9	1.0Ω max
LL P16-14,P11-9	0.4µH max
Cw/w	40pF max
INSERTION LOSS	
0.1-100MHz	-1.1dB max
RETURN LOSS	10.10
0.5-30MHz 40MHz	-18dB min -15.5dB min
50MHz	-13.6dB min
60-80MHz	-12dB min
TYPICAL CROSSTALK	
0.1-100MHz	-40dB min
TYPICAL CMR	
0.1-30MHz 40-100MHz	-60dB min -50dB min
ISOLATION	1,500Vrms

^{*} MINIMUM OCL OVER OPERATING TEMPERATURE

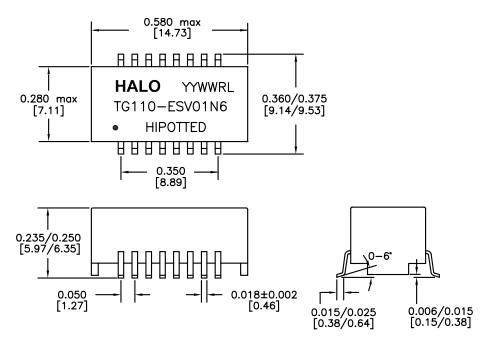


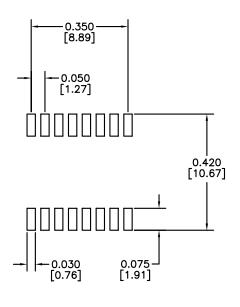
H	A	L(P	B	

CALIFORNIA, USA KOWLOON, HONG KONG SINGAPORE

TITLE ISOLATION MODULE				SIGNATURE	ES	DATE	REV.	DESC.	DATE	
H	FOR	FAST ETHERNET			DRAWN	PETER LU	11/5/04	Α	FIRST ISSUE	11/5/04
F					CHECKED	LEI KEONG	1/31/08	В	PROD. RELEASE	1/31/08
1	PART NO. TG110-ESV01N6RL				APPROVED	PETER LU	1/31/08			
	SCALE	NONE	PAGE	1 of 2	FILE	ESV01N6RL.DWG				

US PATENT NO. 5,656,985 6,297,720B1 6,297,721B1 6,320,489B1 6,344,785B1 6,662,431B1





CO-PLANARITY: 0.004 [0.10]

DIMENSIONS: Inch [mm]

TOLERANCES: ±0.005 INCH IF NOT SPECIFIED

RECOMMANDED SOLDER PAD DIMENSIONS

\square		DDI
ПА	LU	/ PDL

CALIFORNIA, USA KOWLOON, HONG KONG SINGAPORE

TITLE ISOLATION MODULE				SIGNATURES		DATE	REV.	DESC.	DATE
FOR					PETER LU	11/5/04	Α	FIRST ISSUE	11/5/04
TASI LITILINILI			CHECKED	LEI KEONG	1/31/08	В	PROD. RELEASE	1/31/08	
PART NO.	PART NO. TG110-ESV01N6RL				PETER LU	1/31/08			
SCALE	NONE	PAGE	2 of 2	FILE	ESV01N6RL.DWG				

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