# TLP290-4 Technical Information

This material is technological examination material to aim at the product introduction. The change in the content of the characteristic might be accompanied at the final specification process. The final specification will be able to be gotten in the brokerage department when the product is designed and to get the confirmation.

2011/03/15

Toshiba Corporation Semiconductor Company Optoelectronics Device Marketing & Engineering Group 1 TOSHIBA PHOTOCOUPLER GaAs IRED & PHOTO-TRANSISTOR

# TLP290-4

Programmable Controllers Power Supplies Hybrid ICs

The Toshiba TLP290-4 consists of photo transistor, optically coupled to a gallium arsenide infrared emitting diode. TLP290-4 is housed in the SO16 package, very small and thin coupler.

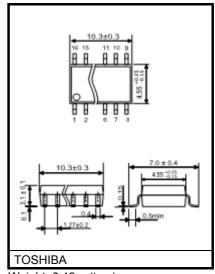
Since TLP290-4 are guaranteed wide operating temperature (Ta=-55 to 110 °C), it's suitable for high-density surface mounting applications such as programmable controllers and hybrid ICs.

- Collector-Emitter Voltage : 80 V (min
- Current Transfer Ratio : 50% (min) Rank GB : 100% (min)
  - Isolation Voltage : 2500 Vrms (min)
- Guaranteed performance over -55 to 110 °C
- UL (under preparation) cUL (under preparation) No.5A
- : UL1577 , File No. E67349
- ation) : CSA Component Acceptance Service
- BSI (under preparation)

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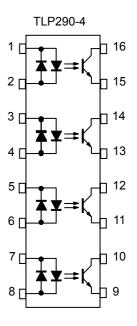
- : BS EN 60065: 2002,
- : BS EN 60950-1: 2006

Construction Mechanical Rating



Weight: 0.19 g (typ.)

### **Pin Configuration**



 1,3,5,7
 :ANODE, CATHODE

 2,4,6,8
 :CATHODE, ANODE

 9,11,13,15
 :EMITTER

 10,12,14,16
 :COLLECTOR

/oltage : 80 V (min) atio : 50% (min) Unit in mm



## **Current Transfer Ratio**

	Classification	Current Transfer Ratio (%) (I <sub>C</sub> / I <sub>F</sub> )		
TYPE	(Note1)	I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V, Ta = 25		Marking of Classification
		Min	Max	
TLP290-4	Blank	50	400	Blank
TLF 290-4	Rank GB	100	400	GB

Note1: ex. Rank GB: TLP290-4 (GB)

Application type name for certification test, please use standard product type name, i.e. TLP290–4 (GB,E: TLP290-4

Absolute Maximum Ratings (Ta = 25 )

		•	-	
CHARACTERISTIC		SYMBOL	RATING	UNIT
	Forward Current	I <sub>F(RMS)</sub>	±50	mA
Q	Forward Current Derating	∆I <sub>F</sub> /°C	−0.67 (Ta≥50°C)	mA /°C
LED	Pulse Forward Current (Note2)	I <sub>FP</sub>	±1	А
	Junction Temperature	Tj	125	°C
	Collector-Emitter Voltage	V <sub>CEO</sub>	80	V
	Emitter-Collector Voltage	V <sub>ECO</sub>	7	V
NOR.	Collector Current	IC	50	mA
DETECTOR	Collector Power Dissipation (1 Circuit)	P <sub>C</sub>	100	mW
Ω	Collector Power Dissipation Derating(Ta≥25°C) (1 Circuit)	∆P <sub>C</sub> /°C	-1.0	mW /°C
	Junction Temperature	Tj	125	°C
Ope	erating Temperature Range	T <sub>opr</sub>	−55 to 110	°C
Stor	age Temperature Range	T <sub>stg</sub>	-55 to 125	°C
Lea	d Soldering Temperature	T <sub>sol</sub>	260 (10s)	°C
Total Package Power Dissipation (1 Circuit)		PT	170	mW
Total Package Power Dissipation Derating (Ta≥25°C) (1 Circuit)		$\Delta P_T /°C$	-1.7	mW /°C
Isola	ation Voltage (Note3)	BVS	2500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note2: Pulse width  $\leq$  100  $\mu s,$  frequency 100 Hz

Note3: AC, 1 minute, R.H.≤60%, Device considered a two terminal device : LED side pins shorted together and DETECTOR side pins shorted together.

### Individual Electrical Characteristics (Ta = 25)

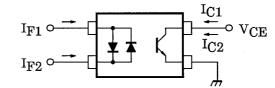
	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
LED	Forward Voltage	VF	I <sub>F</sub> = 10 mA	1.1	1.20	1.4	V
Ш	Capacitance	CT	V = 0, f = 1 MHz	_	30		pF
	Collector-Emitter Breakdown Voltage	V <sub>(BR)</sub> CEO	I <sub>C</sub> = 0.5 mA	80	_	-	V
OR	Emitter-Collector Breakdown Voltage	V <sub>(BR)</sub> ECO	I <sub>E</sub> = 0.1 mA	7	_	-	V
DETECTOR	Collector Dark Current	lama	V <sub>CE</sub> = 48 V,	_	0.01	0.1	μA
DET	(Note5)	ICEO	V <sub>CE</sub> = 48 V, Ta = 85°C	_	2	50	μA
	Capacitance (Collector to Emitter)	C <sub>CE</sub>	V = 0, f = 1 MHz		10		pF

### Coupled Electrical Characteristics (Ta = 25)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Current Transfer Datia		I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V	50	_	400	%
Current Transfer Ratio	I <sub>C</sub> / I <sub>F</sub>	Rank GB	100	_	400	70
Saturated CTD	I <sub>C</sub> / I <sub>F (sat)</sub>	I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 0.4 V	_	60	_	%
Saturated CTR		Rank GB	30	_	_	70
		I <sub>C</sub> = 2.4 mA, I <sub>F</sub> = 8 mA	_	_	0.4	
Collector-Emitter Saturation Voltage	V <sub>CE (sat)</sub>	I <sub>C</sub> = 0.2 mA, I <sub>F</sub> = 1 mA	_	0.2	_	V
		Rank GB	_	_	0.4	
Off-State Collector Current	I <sub>C (off)</sub>	V <sub>F</sub> = 0.7 V, V <sub>CE</sub> = 48 V	_	_	10	μA
CTR symmetry	I <sub>C</sub> (ratio)	$I_{C}$ (I <sub>F</sub> = -5 mA) / $I_{C}$ (I <sub>F</sub> = 5 mA) Note5	0.33	_	3	_

Note5:

$$I_{C(ratio)} = \frac{I_{C2}(I_{F} = I_{F2}, V_{CE} = 5V)}{I_{C1}(I_{F} = I_{F1}, V_{CE} = 5V)}$$



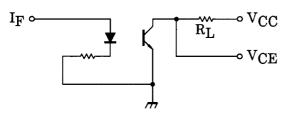
### Isolation Characteristics (Ta = 25 )

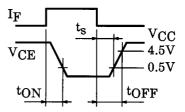
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Capacitance (Input to Output)	CS	V <sub>S</sub> = 0 V, f = 1 MHz	_	0.8		pF
Isolation Resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H.≤60%	1×10 <sup>12</sup>	10 <sup>14</sup>		Ω
		AC , 1 minute	2500		_	Vrmo
Isolation Voltage	BVS	AC , 1 second, in OIL	_	5000	_	Vrms
		DC , 1 minute, in OIL	_	5000		Vdc

## Switching Characteristics (Ta = 25)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Rise Time	tr		_	2	—	
Fall Time	t <sub>f</sub>	V <sub>CC</sub> = 10 V, I <sub>C</sub> = 2 mA	_	3	—	
Turn-On Time	t <sub>on</sub>	$R_L = 100\Omega$	_	3	—	μs
Turn-Off Time	t <sub>off</sub>		_	3	—	
Turn-On Time	t <sub>ON</sub>		_	2	—	
Storage Time	ts	$R_L = 1.9 kΩ$ (Fig.1) V <sub>CC</sub> = 5 V, I <sub>F</sub> = 16 mA	_	25	—	μs
Turn-Off Time	tOFF			40	_	

(Fig.1) Switchin Time Test Circuit





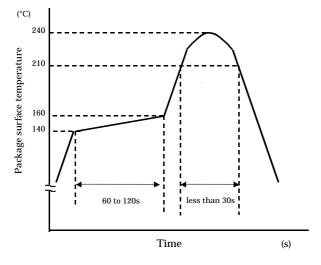
## **Soldering and Storage**

### 1. Soldering

1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as much as possible by observing the following conditions.

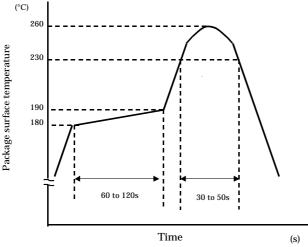
- 1) Using solder reflow
  - ·Temperature profile example of lead (Pb) solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

·Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.

2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)

- Please preheat it at 150°C between 60 and 120 seconds.
- · Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.
- 3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

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## 2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.

3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.

- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

# Option:Specification for Embossed-Tape Packing (TP) for Mini-Flat Coupler

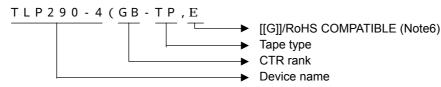
### 1. Applicable Package

Package Name	Product Type
SO16	Mini-Flat Coupler

### 2. Product Naming System

Type of package used for shipment is denoted by a symbol suffix after a product number. The method of classification is as below.

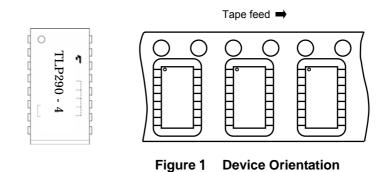
#### (Example)



### 3. Tape Dimensions

# 3.1 Orientation of Device in Relation to Direction of Tape Movement

Device orientation in the recesses is as shown in Figure 1.



### 3.2 Tape Packing Quantity: 2000 devices per reel

3.3 Empty Device Recesses are as Shown in Table 1.

Table 1 **Empty Device Recesses** 

	Standard	Remarks
Occurrences of 2 or more successive empty device recesses	0	Within any given 40-mm section of tape, not including leader and trailer
Single empty device recesses	6 device (max) per reel	Not including leader and trailer

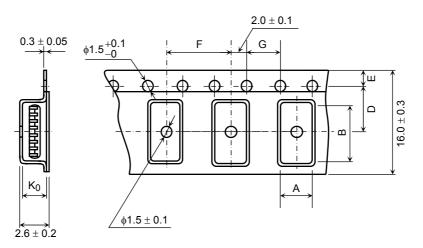
#### 3.4 Start and End of Tape

The start of the tape has 50 or more empty holes. The end of tape has 50 or more empty holes and two empty turns only for a cover tape.

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### 3.5 Tape Specification

- (1) Tape material: Plastic (protection against electrostatics)
- (2) Dimensions: The tape dimensions are as shown in Figure 2 and table 2.



### Figure2 Tape Forms

### Table2Tape Dimensions

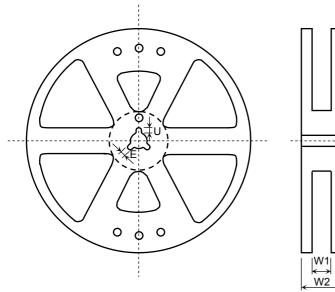
Unit: mm Unless otherwise specified: ±0.1

		Unless otherwise specified. ±0.1
Symbol	Dimension	Remark
A	7.5	
В	10.5	
D	7.5	Center line of indented square hole and sprocket hole
E	1.75	Distance between tape edge and hole center
F	12.0	Cumulative error $^{+0.1}_{-0.3}$ (max) per 10 feed holes
G	4.0	Cumulative error $^{+0.1}_{-0.3}$ (max) per 10 feed holes
K <sub>0</sub>	2.2	Internal space

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### 3.6 Reel

- (1) Material: Plastic
- (2) Dimensions: The reel dimensions are as shown in Figure 3 and Table 3.



	A
/1 → /2	 <u> </u>

#### Table 3 Reel Dimensions

	Unit: mm
Symbol	Dimension
А	$\phi 330\pm 2$
В	$\phi 80 \pm 1$
С	$\phi 13 \pm 0.5$
Е	$2.0\pm0.5$
U	$4.0\pm0.5$
W1	$17.5\pm0.5$
W2	$21.5 \pm 1.0$

Figure 3 Reel Forms

### 4. Packing

Either one reel or five reels of photocouplers are packed in a shipping carton.

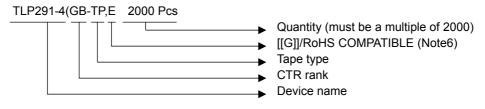
### 5. Label Indication

The carton bears a label indicating the product number, the symbol representing classification of standard, the quantity, the lot number and the Toshiba company name.

### 6. Ordering Method

When placing an order, please specify the product number, the CTR rank, the tape type and the quantity as shown in the following example.

(Example)



Note6:Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.

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