Photocouplers InGaAs Infrared LED & Photo Transistor

TLP383

1. Applications

- Programmable Logic Controllers (PLCs)
- AC Adapters
- I/O Interface Boards

2. General

TLP383 is a photocoupler of low input and high isolation type that consists of phototransistor optically coupled to InGaAs infrared emitting diode in a 4-pin SO6L package.TLP383 is guaranteed high isolation voltage (5000 Vrms) and wide operating temperature range ($T_a = -55$ to 125 °C).Since TLP383 has a small and thin package compared with a standard DIP package, it is suitable for high-density surface mounting applications such as programmable controllers.

3. Features

- (1) Collector-emitter voltage: 80 V (min)
- (2) Current transfer ratio: 50 % (min) GB Rank: 100 % (min)
- (3) Isolation voltage: 5000 Vrms (min)
- (4) Operating temperature: -55 to 125 °C
- (5) Safety standards

UL-approved: UL1577, File No.E67349

cUL-approved: CSA Component Acceptance Service No.5A File No.E67349

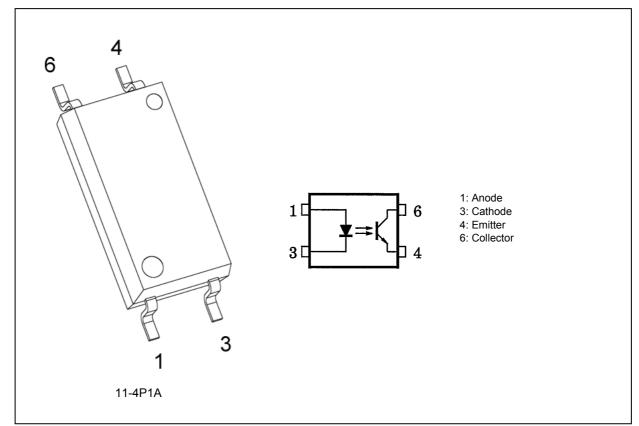
VDE-approved: EN60747-5-5, EN60065 or EN60950-1 (Note 1)

: EN62368-1 (Pending) **(Note 1)**

CQC-approved: GB4943.1, GB8898 Thailand Factory

Note 1: When a VDE approved type is needed, please designate the Option (D4).

4. Packaging and Pin Assignment



5. Mechanical Parameters

Characteristics	Min	Unit
Creepage distances	8.0	mm
Clearance	8.0	
Internal isolation thickness	0.4	

6. Absolute Maximum Ratings (Note) (Unless otherwise specified, T_a = 25 °C)

	Characteristics		Symbol	Note	Rating	Unit
LED	Input forward current		١ _F		50	mA
	Input forward current derating	(T _a ≥ 90 °C)	$\Delta I_F / \Delta T_a$		-1.11	mA/°C
	Input forward current (pulsed)		I _{FP}	(Note 1)	1	Α
	Input power dissipation		PD		100	mW
	Input power dissipation derating	$(T_a \ge 90 \ ^\circ C)$	$\Delta P_D / \Delta T_a$		-2.22	mW/°C
	Input reverse voltage		V _R		5	V
Detector	Collector-emitter voltage		V _{CEO}		80	
	Emitter-collector voltage		V _{ECO}		7	
	Collector current		Ι _C		50	mA
	Collector power dissipation		Pc		150	mW
	Collector power dissipation derating	$(T_a \ge 25 \ ^\circ C)$	$\Delta P_{C} / \Delta T_{a}$		-1.36	mW/°C
	Junction temperature		Tj		125	°C
Common	Operating temperature		T _{opr}		-55 to 125	
	Storage temperature		T _{stg}		-55 to 125	
	Lead soldering temperature	(10 s)	T _{sol}		260	
	Total power dissipation		PT		250	mW
	Total power dissipation derating	$(T_a \ge 25 \ ^\circ C)$	$\Delta P_T / \Delta T_a$		-2.5	mW/°C
	Isolation voltage	AC, 60 s, R.H. ≤ 60 %	BVS	(Note 2)	5000	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width (PW) \leq 0.1 ms, f = 100 Hz

Note 2: This device is considered as a two-terminal device: Pins 1 and 3 are shorted together, and pins 4 and 6 are shorted together.

7. Electrical Characteristics (Unless otherwise specified, Ta = 25 °C)

	Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
LED	Input forward voltage	V _F		I _F = 10 mA	1.1	1.25	1.4	V
	Input reverse current	I _R		V _R = 5 V	_	_	5	μA
	Input capacitance	Ct		V = 0 V, f = 1 MHz	_	30	—	pF
Detector	Collector-emitter breakdown voltage	V _{(BR)CEO}		I _C = 0.5 mA	80		—	V
	Emitter-collector breakdown voltage	V _{(BR)ECO}		I _E = 0.1 mA	7		—	
	Dark Current	IDARK		V _{CE} = 48 V	_	0.01	0.08	μA
				V _{CE} = 48 V, T _a = 85 °C	_	2	50	
	Collector-emitter capacitance	C _{CE}		V = 0 V, f = 1 MHz	_	10	_	pF

8. Coupled Electrical Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Current transfer ratio	I_C/I_F	(Note 1)	I _F = 5 mA, V _{CE} = 5 V	50	_	600	%
			I _F = 5 mA, V _{CE} = 5 V, Rank GB	100	_	600	
			I _F = 0.5 mA, V _{CE} = 5 V	50	_	600	
			I_F = 0.5 mA, V_{CE} = 5 V, Rank GB	100	_	600	
Saturated current transfer ratio	I _C /I _{F(sat)}		I _F = 1 mA, V _{CE} = 0.4 V	_	60	_	
			I_F = 1 mA, V_{CE} = 0.4 V, Rank GB	30	_	_	
Collector-emitter saturation voltage	V _{CE(sat)}		I _F = 8 mA, I _C = 2.4 mA	_	_	0.3	V
			I _F = 1 mA, I _C = 0.2 mA	_	0.09	—	
			I _F = 1 mA, I _C = 0.2 mA, Rank GB			0.3	
OFF-state collector current	I _{C(off)}		V _{CE} = 48 V, V _F = 0.7 V		_	10	μA

Note 1: See Table 8.1 for current transfer ratio.

Table 8.1 Current Transfer Ratio (CTR) Rank (Note) (Unless otherwise specified, Ta = 25 °C)

Rank	Rank short code	Note	Test Condition	Current transfer ratio I _C /I _F Min	Current transfer ratio I _C /I _F Max	Marking of classification	Unit
Blank	_		I _F = 5 mA, V _{CE} = 5 V I _F = 0.5 mA, V _{CE} = 5 V	50	600	Blank, YE, GR, GB, Y+, G, G+, BL, B	%
Y	_		$I_F = 5 \text{ mA}, V_{CE} = 5 \text{ V}$ $I_F = 0.5 \text{ mA}, V_{CE} = 5 \text{ V}$	50	150	YE	
GR			$I_F = 5 \text{ mA}, V_{CE} = 5 \text{ V}$ $I_F = 0.5 \text{ mA}, V_{CE} = 5 \text{ V}$	100	300	GR	
GB	_		$I_F = 5 \text{ mA}, V_{CE} = 5 \text{ V}$ $I_F = 0.5 \text{ mA}, V_{CE} = 5 \text{ V}$	100	600	GB	
BL			I _F = 5 mA, V _{CE} = 5 V I _F = 0.5 mA, V _{CE} = 5 V	200	600	BL	
YH	—		I _F = 0.5 mA, V _{CE} = 5 V	75	150	Y+	
GRL	GL	(Note 1)	I _F = 0.5 mA, V _{CE} = 5 V	100	200	G]
GRH	GH	(Note 1)	I _F = 0.5 mA, V _{CE} = 5 V	150	300	G+]
BLL	В	(Note 1)	I_{F} = 0.5 mA, V_{CE} = 5 V	200	400	В	

Note: Specify both the part number and a rank in this format when ordering. Example: TLP383(GB,E For safety standard certification, however, specify the part number alone. Example: TLP383(GB,E: TLP383

Note 1: Rank code in order name may be shorten as shown above "Rank short code".

9. Isolation Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Total capacitance (input to output)	C _S	(Note 1)	V _S = 0 V, f = 1 MHz	—	0.8	_	pF
Isolation resistance	R _S	(Note 1)	V_S = 500 V, RH \leq 60%	1 × 10 ¹²	1014	_	Ω
Isolation voltage	BVS	(Note 1)	AC, 60 s	5000	—	—	Vrms
			AC, 1 s in oil	_	10000	_	
			DC, 60 s in oil	_	10000	_	Vdc

Note 1: This device is considered as a two-terminal device: Pins 1 and 3 are shorted together, and pins 4 and 6 are shorted together.

10. Switching Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Rise time	tr		V _{CC} = 10 V, I _C = 2 mA,		2	_	μS
Fall time	t _f		$R_{L} = 100 \Omega$		3	_	
Turn-on time	t _{on}				3	_	
Turn-off time	t _{off}				3	_	1
Turn-on time	t _{on}		See Fig. 10.1	_	0.95	_	
Storage time	ts		$R_{L} = 1.9 \text{ k}\Omega, \text{ V}_{CC} = 5 \text{ V},$ $I_{F} = 16 \text{ mA}$		14	_	1
Turn-off time	t _{off}				28	_	1
Turn-on time	t _{on}		See Fig. 10.1		7.5	_	
Storage time	ts		$R_L = 4.7 k\Omega, V_{CC} = 5 V,$ $I_F = 1.6 mA$		7	_	
Turn-off time	t _{off}		$r_F = 1.0 \text{ mA}$		30	_	

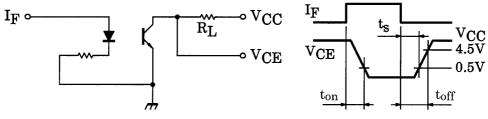
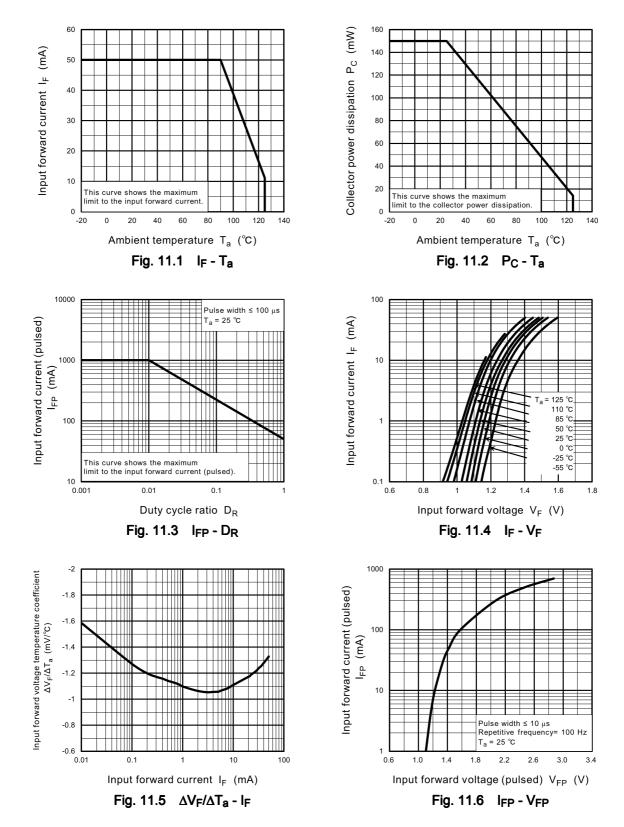
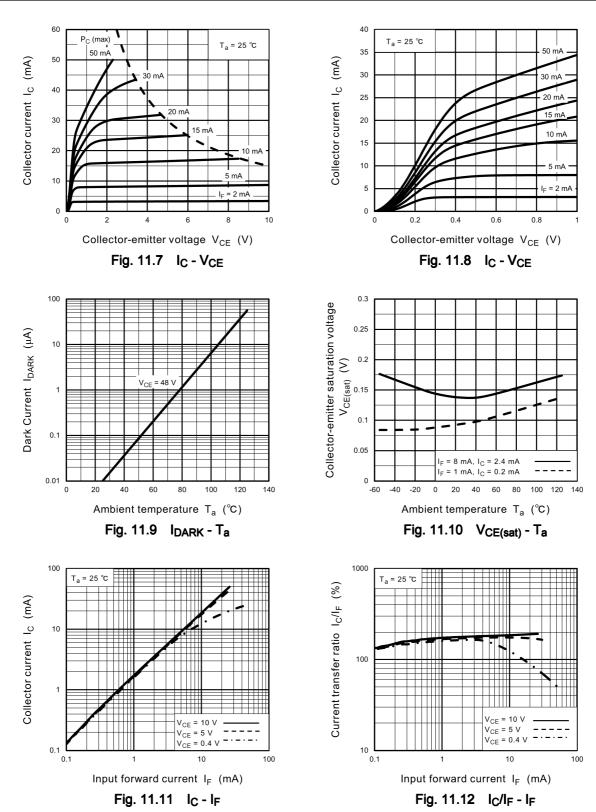
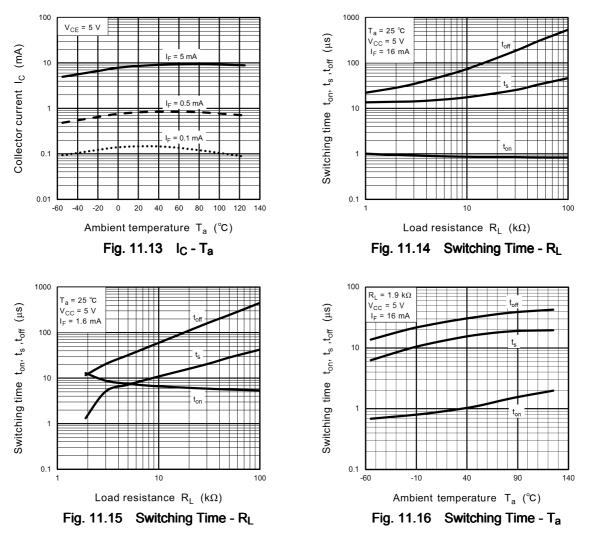


Fig. 10.1 Switching Time Test Circuit

11. Characteristics Curves (Note)







Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

12. Soldering and Storage

12.1. Precautions for Soldering

The soldering temperature should be controlled as closely as possible to the conditions shown below, irrespective of whether a soldering iron or a reflow soldering method is used.

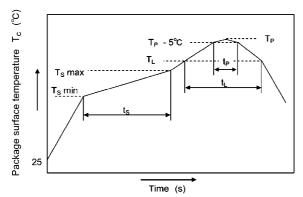
• When using soldering reflow.

The soldering temperature profile is based on the package surface temperature.

(See the figure shown below, which is based on the package surface temperature.)

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.



	Symbol	Min	Max	Unit
Preheat temperature	Ts	150	200	°C
Preheat time	ts	60	120	S
Ramp-up rate $(T_L \text{ to } T_P)$			3	°C/s
Liquidus temperature	TL	2	17	°C
Time above T_L	tL	60	150	s
Peak temperature	Τ _Ρ		260	°C
Time during which T_c is between (T _P – 5) and T_P	t₽		30	s
Ramp-down rate $(T_P \text{ to } T_L)$			6	°C/s

Fig. 12.1.1 An Example of a Temperature Profile When Lead(Pb)-Free Solder Is Used

• When using soldering flow Preheat the device at a temperature of 150 °C (package surface temperature) for 60 to 120 seconds.

Mounting condition of 260 °C within 10 seconds is recommended.

Flow soldering must be performed once.

When using soldering Iron

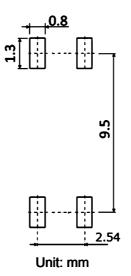
Complete soldering within 10 seconds for lead temperature not exceeding 260 °C or within 3 seconds not exceeding 350 °C

Heating by soldering iron must be done only once per lead.

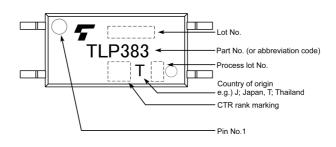
12.2. Precautions for General Storage

- Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- Follow the precautions printed on the packing label of the device for transportation and storage.
- Keep the storage location temperature and humidity within a range of 5 °C to 35 °C and 45 % to 75 %, respectively.
- Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- When restoring devices after removal from their packing, use anti-static containers.
- Do not allow loads to be applied directly to devices while they are in storage.
- If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

13. Land Pattern Dimensions (for reference only)



14. Marking



15. EN60747-5-5 Option (D4) Specification

• Part number: TLP383 (Note 1)

The following part naming conventions are used for the devices that have been qualified according to option (D4) of EN60747.

Example: TLP383(D4GR-TL,E

D4: EN60747 option

GR: CTR rank

TL: Tape type

E: [[G]]/RoHS COMPATIBLE (Note 2)

Note 1: Use TOSHIBA standard type number for safety standard application.

e.g., TLP383(D4GR-TL,E \rightarrow TLP383

Note 2: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.

Description	Symbol	Rating	Unit
Application classification for rated mains voltage ≤ 600 Vrms		I-IV	_
for rated mains voltage \leq 1000 Vrms		1-111	
Climatic classification		55 / 125 / 21	_
Pollution degree		2	_
Maximum operating insulation voltage	VIORM	1230	Vpeak
Input to output test voltage, Method A $V_{pr} = 1.6 \times V_{IORM}$, type and sample test $t_p = 10$ s, partial discharge < 5 pC	Vpr	1970	Vpeak
Input to output test voltage, Method B V_{pr} =1.875 × V _{IORM} , 100 % production test t_p = 1 s, partial discharge < 5 pC	Vpr	2310	Vpeak
Highest permissible overvoltage (transient overvoltage, t _{pr} = 60 s)	V _{TR}	8000	Vpeak
Safety limiting values (max. permissible ratings in case of fault, also refer to thermal derating curve)			
current (input current I_F , $P_{SO} = 0$)	l _{si}	250	mA
power (output or total power dissipation) temperature	P _{so} T _s	400 150	mW °C
Insulation resistance $V_{IO} = 500 \text{ V}, \text{ T}_a = 25 \text{ °C}$ $V_{IO} = 500 \text{ V}, \text{ T}_a = 100 \text{ °C}$ $V_{IO} = 500 \text{ V}, \text{ T}_a = \text{ T}_s$	R _{si}	$\geq 10^{12}$ $\geq 10^{11}$ $\geq 10^{9}$	Ω

Fig. 15.1 EN60747 Insulation Characteristics

Minimum creepage distance	Cr	8.0 mm
Minimum clearance	CI	8.0 mm
Minimum insulation thickness	ti	0.4 mm
Comparative tracking index	CTI	175

Fig. 15.2 Insulation Related Specifications (Note)

Note: This photocoupler is suitable for **safe electrical isolation** only within the safety limit data. Maintenance of the safety data shall be ensured by means of protective circuits.

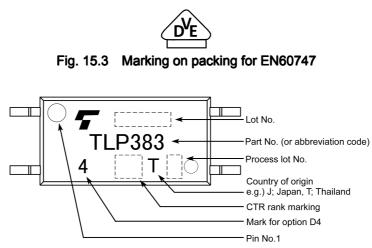
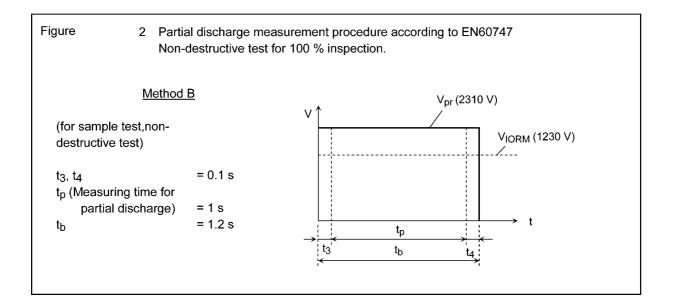
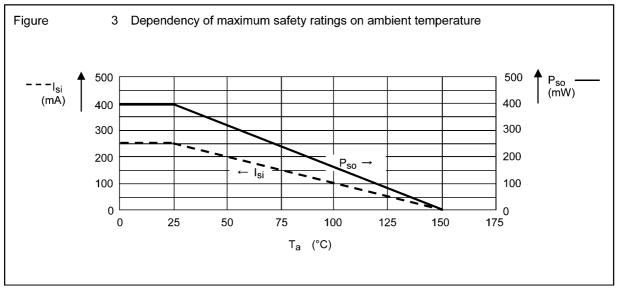


Fig. 15.4 Marking Example (Note)

Note: The above marking is applied to the photocouplers that have been qualified according to option (D4) of EN60747.

Figure	-	neasurement procedure according to EN60747 r qualification and sampling tests.
	Method A	V _{INITIAL} (8 kV)
(for type and s destructive tes		V Vpr (1970 V)
t ₁ , t ₂	= 1 to 10 s	VIORM (1230 V)
t3, t4	= 1 s	
t _p (Measuring f	time for	
partial disc	charge) = 10 s	$0 \xrightarrow{\mu} t$
t _b	= 12 s	t_3 t_p t_4
t _{ini}	= 60 s	t ₁ t _{ini} t ₂ t _b







16. Embossed-Tape Packing (TPL),(TPR) Specification for Mini-Flat Photocouplers

16.1. Applicable Package

Package Name	Product Type
SO6L	Long creepage mini flat coupler

16.2. Product Naming Conventions

Type of package used for shipment is denoted by a symbol suffix after a part number. The method of classification is as below.

Example) TLP383(GR-TPL,E

Part number: TLP383 CTR rank: GR Tape type: TPL [[G]]/RoHS COMPATIBLE: E **(Note)**

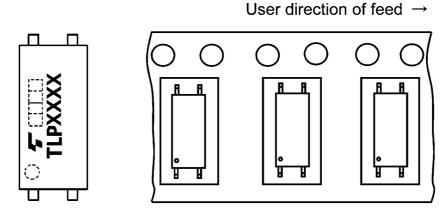
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RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.

16.3. Tape Dimensions Specification

TPL:L direction, TPR:R direction

16.3.1. Orientation of Device in Relation to Direction of Feed

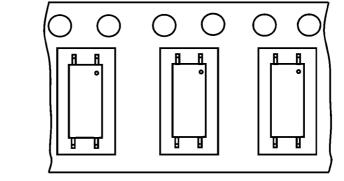
Device orientation in the carrier cavities as shown in Fig. 16.3.1.1



L direction

User direction of feed \rightarrow





R direction

Fig. 16.3.1.1 Device Orientation

16.3.2. Packing Quantity

 $3000 \mathrm{\ pcs} \mathrm{\ per} \mathrm{\ reel}$

16.3.3. Empty Cavities

Characteristics	Criterion	Remarks
Occurrences of 2 or more successive empty cavities	0 device	Within any given 40-mm section of tape, not including leader and trailer
Single empty cavity	6 devices (max) per reel	Not including leader and trailer

16.3.4. Tape Leader and Trailer

The start end of the tape has 50 or more empty cavities. The hub end of the tape has 50 or more empty cavities and two empty turns only for a cover tape.

16.3.5. Tape Dimensions

Tape material: Plastic (for protection against static electricity)

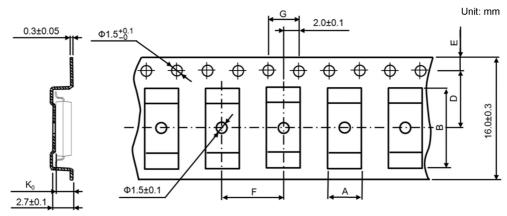


Fig. 16.3.5.1 Tape Dimensions

Table 16.3.5.1	Tape Dimensions	(unit: mm, unless otherwise	specified: ±0.1)
----------------	-----------------	-----------------------------	------------------

Symbol	Dimension	Remark
A	4.24	—
В	10.4	_
D	7.5	Center line of embossed cavity and sprocket hole
E	1.75	Distance between tape edge and sprocket hole center
F	8.0	—
G	4.0	Cumulative error \pm 0.2 (max) per 10 sprocket holes
K ₀	2.4	Internal space

16.3.6. Reel Specification

Material: Plastic

TOSHIBA

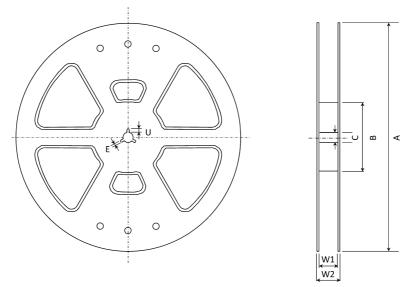


Fig. 16.3.6.1 Reel Dimensions

	()
Symbol	Dimension
А	$\phi 330 \pm 2$
В	$\varphi 100 \pm 1$
С	$\varphi 13 \pm 0.5$
E	2.0 ± 0.5
U	4.0 ± 0.5
W1	17.5 ± 0.5
W2	21.4 ± 1.0

Table 16.3.6.1 Reel Dimensions (unit: mm)

16.4. Packing

Either one reel or ten reels (maximum) of photocouplers are packed in a shipping carton.

16.5. Label Format

- (1) Carton: The label provides the part number, quantity, lot number, the Toshiba logo, etc.
- (2) Reel: The label provides the part number, the taping name, quantity, lot number, etc.

16.6. Ordering Information

When placing an order, please specify the part number, CTR rank, tape type and quantity as shown in the following example.

Example) TLP383(GR-TPL,E 3000 pcs

Part number: TLP383 CTR rank: GR Tape type: TPL(L direction) [[G]]/RoHS COMPATIBLE: E **(Note)** Quantity (must be a multiple of 3000 pcs): 3000 pcs

Note: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

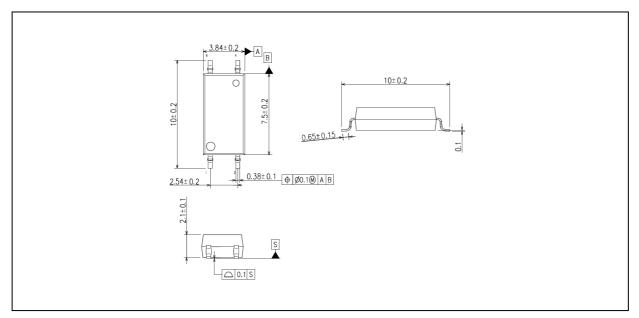
RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.



TLP383

Package Dimensions

Unit: mm



Weight: 0.128 g (typ.)

Package Name(s)

TOSHIBA: 11-4P1A

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