



# High Voltage NPN Transistor





**TO-220 ITO-220** 



#### Pin Definition:

- 1. Base
- 2. Collector
- 3. Emitter

#### **PRODUCT SUMMARY**

BV <sub>CEO</sub>	400V
BV <sub>CBO</sub>	700V
Ic	4A
V <sub>CE(SAT)</sub>	1V @ I <sub>C</sub> =4A, I <sub>B</sub> =1A

### **Features**

- High Voltage
- High Speed Switching

## **Structure**

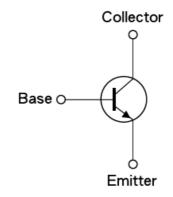
- Silicon Triple Diffused Type
- NPN Silicon Transistor

### **Ordering Information**

Part No.	Package	Packing
TS13005CZ C0G	TO-220	50pcs / Tube
TS13005CI C0G	ITO-220	50pcs / Tube

Note: "G" denote for Halogen Free Product

## **Block Diagram**



**Absolute Maximum Rating** (Ta = 25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit	
Collector-Base Voltage		$V_{CBO}$	700	V	
Collector-Emitter Voltage		$V_{CEO}$	400	V	
Emitter-Base Voltage		$V_{EBO}$	9	V	
Collector Current	DC		4	_	
	Pulse	lc	8	A	
Base Current	DC		2	А	
	Pulse	I <sub>B</sub>	4		
Total Power Dissipation	TO-220	_	75	W	
	ITO-220	P <sub>tot</sub>	30		
Operating Junction Temperature		TJ	+150	°C	
Operating Junction and Storage Temperature Range		T <sub>STG</sub>	T <sub>STG</sub> - 55 to +150		

Note: Single Pulse. P<sub>W</sub> = 300uS, Duty ≤ 2%

1

# TS13005

# High Voltage NPN Transistor



**Electrical Specifications** (Ta = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static						
Collector-Base Voltage	$I_C = 1 \text{mA}, I_B = 0$	BV <sub>CBO</sub>	700			V
Collector-Emitter Breakdown Voltage	$I_{C} = 10 \text{mA}, I_{E} = 0$	BV <sub>CEO</sub>	400			V
Emitter-Base Breakdown Voltage	$I_E = 0.1 \text{mA}, I_C = 0$	BV <sub>EBO</sub>	9			V
Collector Cutoff Current	$V_{CE} = 400 \text{V}, I_{B} = 0$	I <sub>CEO</sub>			250	uA
Collector Cutoff Current	$V_{CB} = 700 V, I_{E} = 0$	I <sub>CBO</sub>			1	mA
Emitter Cutoff Current	$V_{EB} = 9V, I_{C} = 0$	I <sub>EBO</sub>			1	mA
Collector-Emitter Saturation Voltage	I <sub>C</sub> =1A, I <sub>B</sub> =0.2A	V <sub>CE(SAT)1</sub>			0.5	
	I <sub>C</sub> =2A, I <sub>B</sub> =0.5A	V <sub>CE(SAT)2</sub>			0.6	V
	I <sub>C</sub> =4A, I <sub>B</sub> =1A	V <sub>CE(SAT)3</sub>			1	
Base-Emitter Saturation Voltage	I <sub>C</sub> =1A, I <sub>B</sub> =0.2A	V <sub>BE(SAT)1</sub>			1.2	V
	I <sub>C</sub> =2A, I <sub>B</sub> =0.5A	V <sub>BE(SAT)2</sub>			1.6	
DC Current Gain	$V_{CE} = 5V$ , $I_C = 1A$	h <sub>FE</sub>	15		32	
	$V_{CE} = 5V$ , $I_C = 2A$		8		40	
Dynamic						
Frequency	$V_{CE} = 10V, I_{C} = 0.5A$	f <sub>T</sub>	4			MHz
Output Capacitance	V <sub>CB</sub> =10V, f =0.1MHz	Cob		65		рF
Resistive Load						
Turn On Time	V <sub>CC</sub> =125V, I <sub>C</sub> =2A,	t <sub>ON</sub>		0.3	0.7	uS
Storage Time	$I_{B1} = I_{B2} = 0.4A, t_P = 25uS$	t <sub>STG</sub>		2.2	3	uS
Fall Time	Duty Cycle ≤1%	t <sub>f</sub>		0.3	0.5	uS

**Note:** pulse test: pulse width  $\leq$  300uS, duty cycle  $\leq$  2%







## High Voltage NPN Transistor

#### **Electrical Characteristics Curve** (Ta = 25°C, unless otherwise noted)

Figure 3. V<sub>CE(SAT)</sub> v.s. V<sub>BE(SAT)</sub>

6

Vce[V], Collector-Emitter Voltage

8

10

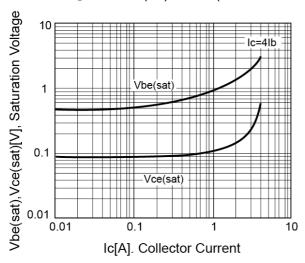


Figure 5. Reverse Bias SOA

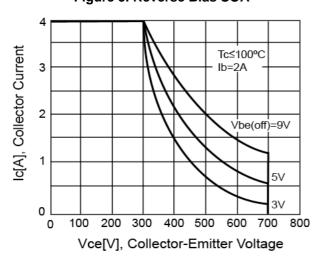


Figure 2. DC Current Gain

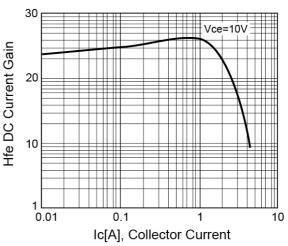


Figure 4. Power Derating

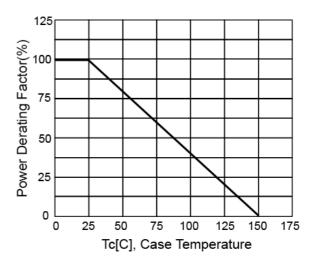
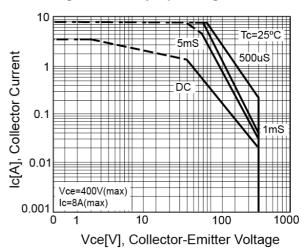


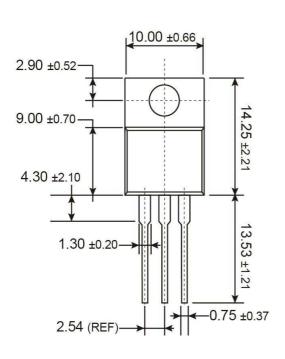
Figure 6. Safety Operating Area

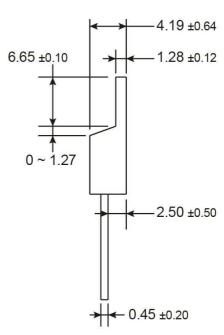






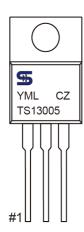
# **TO-220 Mechanical Drawing**





Unit: Millimeters

## **Marking Diagram**



Y = Year Code

**M** = Month Code for Halogen Free Product

O =Jan P =Feb Q =Mar R =Apr S =May T =Jun U =Jul V =Aug

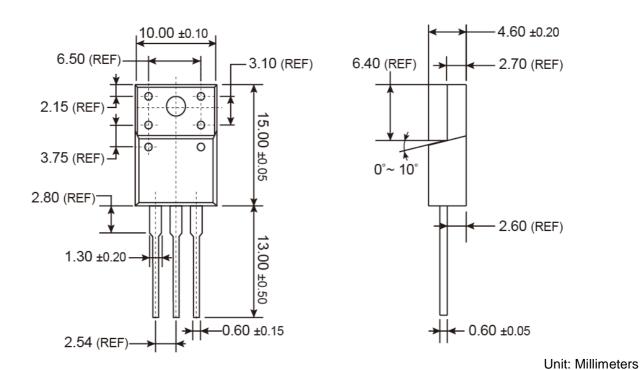
W = Sep X = Oct Y = Nov Z = Dec

L = Lot Code

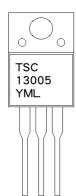




## **ITO-220 Mechanical Drawing**



# **Marking Diagram**



Y = Year Code

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 $oldsymbol{O}$  =Jan  $oldsymbol{P}$  =Feb  $oldsymbol{Q}$  =Mar  $oldsymbol{R}$  =Apr

S =May T =Jun U =Jul V =Aug W =Sep X =Oct Y =Nov Z =Dec

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