# 250mA Low Quiescent Current CMOS LDO

#### DESCRIPTION

TS9011 is a positive voltage regulator developed utilizing CMOS technology featured very low power consumption, low dropout voltage and high output voltage accuracy. Built in low on-resistor provides low dropout voltage and large output current. A 1µF or greater can be used as an output capacitor. TS9011 are prevented device failure under the worst operation condition with both thermal shutdown and current fold-back. These series are recommended for configuring portable devices and large current application, respectively.

#### **FEATURES**

- Dropout Voltage 0.4V (typ.) @ lo=250mA •
- Output Current up to 250mA
- Low Power Consumption, 2µA (typ.) •
- Output Voltage ± 2% •
- Internal Current Limit .
- **Thermal Shutdown Protection** •
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC.
- Halogen-free according to IEC 61249-2-21 •

#### **APPLICATION**

- Battery-operated systems
- Microprocessor reset circuitry .
- Memory battery back-up circuits
- Power-on reset circuits
- Power failure detection
- System battery life and charge voltage monitors





**Pin Definition:** 1. Ground

Input

2

3.



**SOT-89** 

#### Pin Definition:

1. Ground Input 2.

Output 3.

#### Notes: Moisture sensitivity level: level 3. Per J-STD-020

#### **TYPICAL APPLICATION CIRCUIT**



\*Tantalum capacitor for Input & Output capacitor are recommended.



Taiwan Semiconductor

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise specified) (Note 1)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Input Supply Voltage		V <sub>IN</sub>	12	V	
Output Current		Ι <sub>ουτ</sub>	250	mA	
Dower Dissipation	SOT-23	D	0.30	\\/	
	SOT-89	PD	0.50	٧V	
Operating Ambient Temperature		T <sub>OPR</sub>	-40 ~ +85	°C	
Junction Temperature Range		TJ	-40 ~ +150	°C	
Storage Temperature Range		T <sub>STG</sub>	-65 ~ +150	°C	

THERMAL PERFORMANCE					
PARAMETER		SYMBOL	LIMIT	UNIT	
Thermal Resistance Junction to Ambient	SOT-23	R <sub>θJA</sub>	333	°C/W	
	SOT-89		200		

Note: Measured with FR4 4-layer board having thermal via holes

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise specified)						
PARAMETER	CONDITION		MIN	TYP	MAX	UNIT
	V <sub>IN</sub> =V <sub>O</sub> + 1V, I <sub>O</sub> =40mA,	TS90115	4.90	5.0	5.10	V
		TS9011S	3.23	3.3	3.36	
Output Voltage		TS9011P	2.94	3.0	3.06	
		TS9011K	2.45	2.5	2.55	
		TS9011D	1.76	1.8	1.83	
Maximum Output Current	V <sub>IN</sub> =V <sub>O</sub> +1V,	V <sub>IN</sub> =V <sub>O</sub> +1V,				mA
Input Stability	$V_0$ +1V $\leq V_{IN} \leq V_0$ +2V, $I_0$ =1mA			0.2	0.3	%
	$V_{IN}=V_O+1V,$ 1mA $\leq$ I <sub>L</sub> $\leq$ 100mA	TS90115		40	80	mV
		TS9011S				
Load Regulation (Note1)	V <sub>IN</sub> =V <sub>O</sub> +1V, 1mA≤I <sub>L</sub> ≤80mA	TS9011P		40	90	
		TS9011K				
		TS9011D				
	I <sub>0</sub> =250mA	TS90115		400	600	mV
	I <sub>0</sub> =200mA	TS9011S		400	650	
Dropout Voltage (Note 2)	I <sub>0</sub> =160mA	TS9011P		400	700	
	I <sub>0</sub> =160mA	TS9011K		400	700	
	I <sub>o</sub> =120mA	TS9011D		400	750	
Quiescent Current	V <sub>IN</sub> =V <sub>O</sub> +1V, I <sub>O</sub> =0A			2	5	μA
Output Current Limit	V <sub>OUT</sub> < 0.4V			400		mA
Power Supply Rejection Ratio	At f=100kHz, I <sub>O</sub> =10mA,			30		dB
Output Voltage Temperature Coefficient <sup>(Note 3)</sup>				100		ppm/°C

#### Note:

1.

Regulation is measured at constant junction temperature, using pulsed ON time. Dropout is measured at constant junction temperature, using pulsed ON time, and the criterion is  $V_{OUT}$  inside 2.

target value +/-2%. Guaranteed by design. 3.



### **ORDERING INFORMATION**

OUTPUT VOLTAGE	PART NO.	PACKAGE	PACKING
1.8V	TS9011DCX RFG	SOT-23	3,000pcs / 7" Reel
	TS9011DCY RMG	SOT-89	1,000pcs / 7" Reel
2.5V	TS9011KCX RFG	SOT-23	3,000pcs / 7" Reel
	TS9011KCY RMG	SOT-89	1,000pcs / 7" Reel
3.0V	TS9011PCY RMG	SOT-89	1,000pcs / 7" Reel
2 2\/	TS9011SCX RFG	SOT-23	3,000pcs / 7" Reel
3.3V	TS9011SCY RMG	SOT-89	1,000pcs / 7" Reel
5)/	TS90115CX RFG	SOT-23	3,000pcs / 7" Reel
37	TS90115CY RMG	SOT-89	1,000pcs / 7" Reel

# FUNCTION BLOCK DIAGRAM





## ELECTRICAL CHARACTERICS CURVES (T<sub>A</sub>=25°C, unless otherwise noted)







Figure 3. Quiescent Current vs. Input Voltage



Figure 2. Output Voltage vs. Input Voltage



Figure 4. Short Circuit Current vs. Input Voltage



## PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

0.95 (REF)



### SUGGESTED PAD LAYOUT (Unit: Millimeters)



### **MARKING DIAGRAM**



E = Product CodeY = Year Code

**M** = Month Code for Halogen Free Product

- L = Lot Code (1~9, A~Z)
- **X** = Fixed Output Voltage Code
  **A**=1.5V, **D**=1.8V, **K**=2.5V, **P**=3.0V, **S**=3.3V, **5**=5.0V.



# PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)



SUGGESTED PAD LAYOUT (Unit: Millimeters)



#### **MARKING DIAGRAM**

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- Y = Year Code
- **M** = Month Code for Halogen Free Product

<b>O</b> =Jan	P =Feb	<b>Q</b> =Mar	R =Apr
<b>S</b> =May	<b>T</b> =Jun	U =Jul	V =Aug
W =Sep	X =Oct	Y =Nov	Z =Dec

- **L** = Lot Code  $(1 \sim 9, A \sim Z)$
- **X** = Fixed Output Voltage Code
  **A**=1.5V, **D**=1.8V, **K**=2.5V, **P**=3.0V, **S**=3.3V, **5**=5.0V.



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