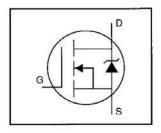
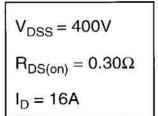
International TOR Rectifier

IRFP350PbF

HEXFET® Power MOSFET

- Dynamic dv/dt Rating
- Repetitive Avalanche Rated
- Isolated Central Mounting Hole
- Fast Switching
- · Ease of Paralleling
- Simple Drive Requirements
- Lead-Free

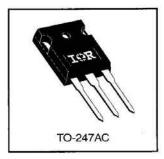




Description

Third Generation HEXFETs from International Rectifier provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-247 package is preferred for commercial–industrial applications where higher power levels preclude the use of TO-220 devices. The TO-247 is similar but superior to the earlier TO-218 package because of its isolated mounting hole. It also provides greater creepage distance between pins to meet the requirements of most safety specifications.



Absolute Maximum Ratings

	Parameter	Max.	Units	
I _D @ T _C = 25°C	Continuous Drain Current, VGS @ 10 V	16	-24567-01	
I _D @ T _C = 100°C	Continuous Drain Current, VGS @ 10 V	10	A	
I _{DM}	Pulsed Drain Current ①	64		
P _D @ T _C = 25°C	Power Dissipation	190	W	
	Linear Derating Factor	1.5	W/°C	
V _G s	Gate-to-Source Voltage	±20	V	
Eas	Single Pulse Avalanche Energy ②	390	mJ	
IAR	Avalanche Current ①	16	A	
EAR	Repetitive Avalanche Energy ①	19	mJ	
dv/dt	Peak Diode Recovery dv/dt ③	4.0	V/ns	
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C	
	Soldering Temperature, for 10 seconds	300 (1.6mm from case)		
	Mounting Torque, 6-32 or M3 screw	10 lbf•in (1.1 N•m)		

Thermal Resistance

	Parameter	Min.	Тур.	Max.	Units	
Resc	Junction-to-Case		_	0.65		
Recs	Case-to-Sink, Flat, Greased Surface	. –	0.24	-	°C/W	
Raja	Junction-to-Ambient	—	_	40		

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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Test Conditions		
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	400	_	-	٧	V _{GS} =0V, I _D = 250μA		
ΔV _{(BR)DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient	-	0.51	-	V/°C	Reference to 25°C, ID= 1mA		
Ros(on)	Static Drain-to-Source On-Resistance	 	1 2-1 4	0.30	Ω	V _{GS} =10V, I _D =9.6A ④		
V _{GS(th)}	Gate Threshold Voltage	2.0	_	4.0	٧	V _{DS} =V _{GS} , I _D = 250μA		
g/s	Forward Transconductance	10	_	_	s	V _{DS} =50V, I _D =9.6A @		
T	Drain-to-Source Leakage Current	s s	-	25		V _{DS} =400V, V _{GS} =0V		
loss	Drain-to-Source Leakage Current	-	=	250	μА	V _{DS} =320V, V _{GS} =0V, T _J =125°		
lass	Gate-to-Source Forward Leakage	-		100	nA	V _{GS} =20V		
IGSS	Gate-to-Source Reverse Leakage		=	-100	IIA.	V _{GS} =-20V		
Qg	Total Gate Charge	_	1000	150		I _D =16A		
Q _{gs}	Gate-to-Source Charge	-		23	nC	V _{DS} =320V V _{GS} =10V See Fig. 6 and 13 G		
Q _{gd}	Gate-to-Drain ("Miller") Charge	_	_	80				
t _{d(on)}	Turn-On Delay Time	_	16	*		V _{DD} =200V		
tr	Rise Time	_	49	-	ns	I _D =16A		
t _{d(off)}	Turn-Off Delay Time	_	87	-	113	R _G =6.2Ω		
t _f	Fall Time	-	47	-		R _D =12Ω See Figure 10 ®		
LD	Internal Drain Inductance	-	5.0	==0	nН	Between lead, 6 mm (0.25in.)		
Ls	Internal Source Inductance	=	13		11161	from package and center of die contact		
Ciss	Input Capacitance	(2600	5728		V _{GS} =0V		
Coss	Output Capacitance	5-2	660		pF	V _{DS} = 25V		
Crss	Reverse Transfer Capacitance	_	250	_	ļ	f=1.0MHz See Figure 5		

Source-Drain Ratings and Characteristics

	Parameter	Min.	Тур.	Max.	Units	Test Conditions	
Is	Continuous Source Current (Body Diode)	_	_	16	A	MOSFET symbol showing the	
Ism	Pulsed Source Current (Body Diode) ①	-	=	64	^	integral reverse p-n junction diode.	
Vsb	Diode Forward Voltage		-	1.6	V	T _J =25°C, I _S =16A, V _{GS} =0V @	
t _{rr}	Reverse Recovery Time		380	570	ns	T _J =25°C, I _F =16A	
Qrr	Reverse Recovery Charge		4.7	7.1	μC	di/dt=100A/μs ④	
ton	Forward Turn-On Time	Intrinsic turn-on time is neglegible (turn-on is dominated by L _S +L _D)					

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature (See Figure 11)
- ③ I_{SD}≤16A, di/dt≤200A/ μ s, V_{DD}≤V(BR)DSS, T_J≤150°C
- $^{\circ}$ V_{DD}=50V, starting T_J=25°C, L=2.7mH R_G=25Ω, I_{AS}=16A (See Figure 12)
- ④ Pulse width ≤ 300 μs; duty cycle ≤2%.

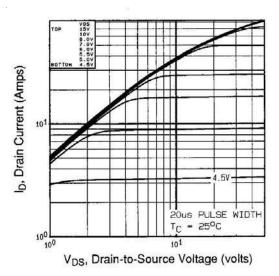


Fig 1. Typical Output Characteristics, T_C=25°C

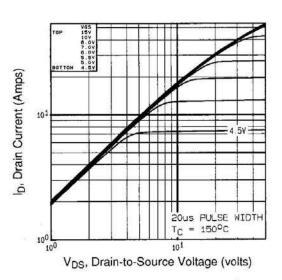


Fig 2. Typical Output Characteristics, Tc=150°C

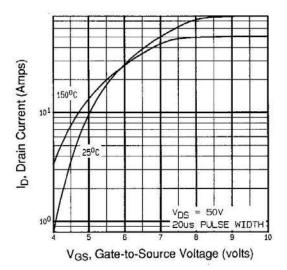


Fig 3. Typical Transfer Characteristics

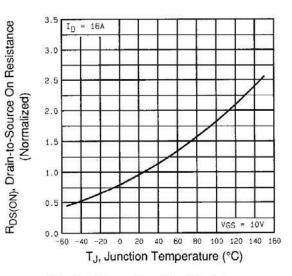


Fig 4. Normalized On-Resistance Vs. Temperature

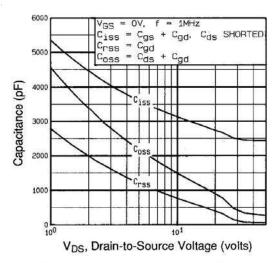
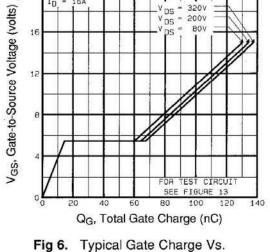


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage



ID. - 16A

Gate-to-Source Voltage

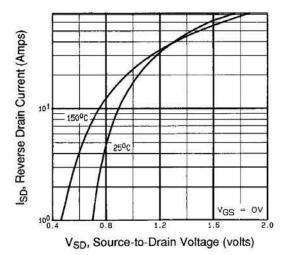


Fig 7. Typical Source-Drain Diode Forward Voltage

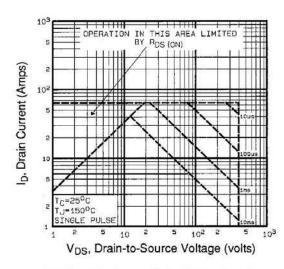


Fig 8. Maximum Safe Operating Area

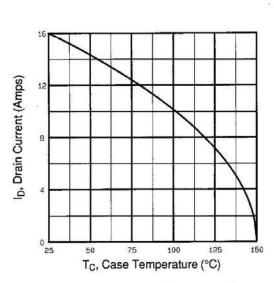


Fig 9. Maximum Drain Current Vs. Case Temperature

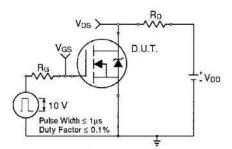


Fig 10a. Switching Time Test Circuit

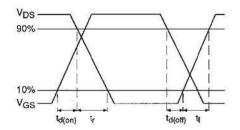


Fig 10b. Switching Time Waveforms

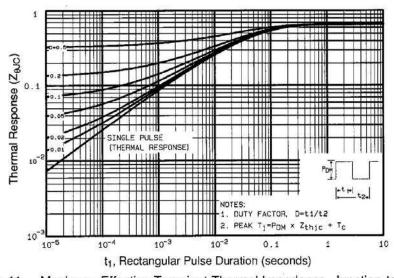


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

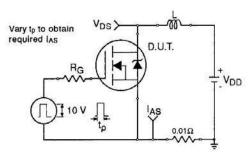


Fig 12a. Unclamped Inductive Test Circuit

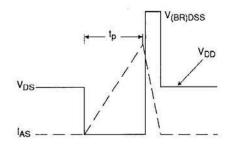


Fig 12b. Unclamped Inductive Waveforms

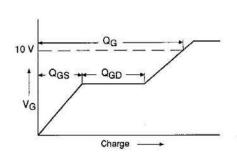


Fig 13a. Basic Gate Charge Waveform

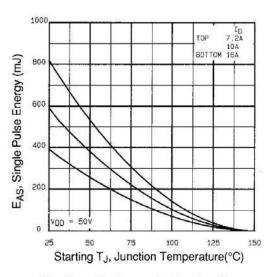


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

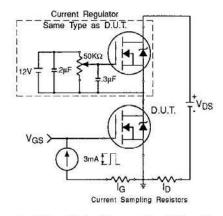


Fig 13b. Gate Charge Test Circuit

Appendix A: Figure 14, Peak Diode Recovery dv/dt Test Circuit - See page 1505

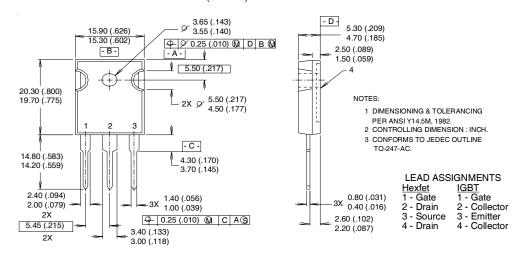
Appendix B: Package Outline Mechanical Drawing - See page 1511



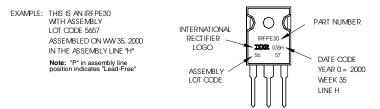
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TO-247AC Package Outline

Dimensions are shown in millimeters (inches)



TO-247AC Part Marking Information



Data and specifications subject to change without notice.



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