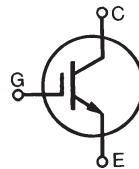


High Voltage IGBT For Capacitor Discharge Applications

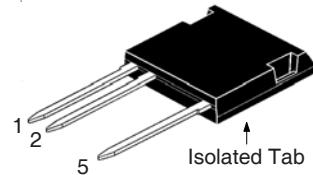
IXGF30N400

(Electrically Isolated Tab)



V_{CES} = 4000V
 I_{C25} = 30A
 $V_{CE(sat)}$ ≤ 3.1V

ISOPLUS i4-Pak™



1 = Gate 5 = Collector
 2 = Emitter

Symbol	Test Conditions	Maximum Ratings	
V_{CES}	T_J = 25°C to 150°C	4000	V
V_{GES}	Continuous	± 20	V
V_{GEM}	Transient	± 30	V
I_{C25}	T_c = 25°C	30	A
I_{C110}	T_c = 110°C	15	A
I_{CM}	T_c = 25°C, V_{GE} = 20V, 1ms	360	A
SSOA	V_{GE} = 20V, T_{VJ} = 125°C, R_G = 2Ω	I_{CM} = 300	A
(RBSOA)	Clamped Inductive Load	$V_{CE} \leq 0.8 \cdot V_{CES}$	
P_c	T_c = 25°C	160	W
T_j		-55 ... +150	°C
T_{JM}		150	°C
T_{stg}		-55 ... +150	°C
T_L	1.6 mm (0.062 in.) from case for 10s	300	°C
T_{SOLD}	Plastic body for 10s	260	°C
F_c	Mounting Force	20..120 / 4.5..27	Nm/lb.in.
V_{ISOL}	50/60Hz, 1 minute	4000	V~
Weight		5	g

Symbol	Test Conditions (T_j = 25°C, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
BV_{CES}	I_c = 250µA, V_{GE} = 0V	4000		V
$V_{GE(th)}$	I_c = 250µA, V_{CE} = V_{GE}	3.0		V
I_{CES}	$V_{CE} = 0.8 \cdot V_{CES}$, V_{GE} = 0V Note 2, T_j = 100°C		50 µA 3 mA	
I_{GES}	V_{CE} = 0V, V_{GE} = ±20V		±200 nA	
$V_{CE(sat)}$	I_c = 30A, V_{GE} = 15V, Note 1 I_c = 90A		3.1 V 5.2 V	

Features

- Silicon Chip on Direct-Copper Bond (DCB) Substrate
- Isolated Mounting Surface
- 4000V Electrical Isolation
- High Peak Current Capability
- Low Saturation Voltage
- Molding Epoxies Meet UL 94 V-0 Flammability Classification

Advantages

- High Power Density
- Easy to Mount

Applications

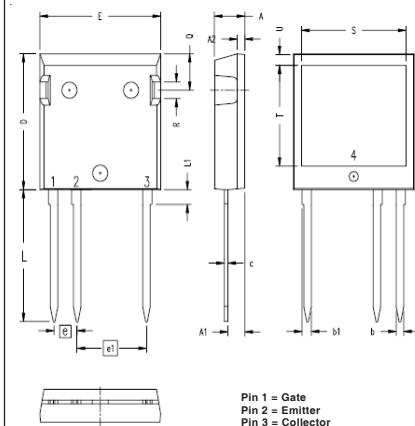
- Capacitor Discharge
- Pulser Circuits

Symbol	Test Conditions (T _J = 25°C, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
g _{fs}	I _C = 30A, V _{CE} = 10V, Note 1	14	23	S
I _{C(ON)}	V _{GE} = 15V, V _{CE} = 20V, Note 1	360		A
C _{ies}	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz	3040		pF
C _{oes}		95		pF
C _{res}		30		pF
Q _g	I _C = 30A, V _{GE} = 15V, V _{CE} = 600V	135		nC
Q _{ge}		22		nC
Q _{gc}		50		nC
t _{d(on)}	Resistive Switching Times I _C = 30A, V _{GE} = 15V, V _{CE} = 1250V, R _G = 2Ω	55		ns
t _r		146		ns
t _{d(off)}		210		ns
t _f		514		ns
R _{thJC}		0.78 °C/W °C/W °C/W		
R _{thCS}			0.15	
R _{thJA}			30	

Notes:

1. Pulse test, t < 300μs, duty cycle, d < 2%.
2. Device must be heatsunk for high-temperature leakage current measurements to avoid thermal runaway.

ISOPLUS i4-Pak™ (HV) Outline



SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.190	.205	4.83	5.21
A1	.102	.118	2.59	3.00
A2	.046	.085	1.17	2.16
b	.045	.055	1.14	1.40
b1	.058	.068	1.47	1.73
C	.020	.029	0.51	0.74
D	.819	.840	20.80	21.34
E	.770	.799	19.56	20.29
e	.150 BSC		3.81 BSC	
e1	.450BSC		11.43 BSC	
L	.780	.840	19.81	21.34
L1	.083	.102	2.11	2.59
Q	.210	.244	5.33	6.20
R	.100	.180	2.54	4.57
S	.660	.690	16.76	17.53
T	.590	.620	14.99	15.75
U	.065	.080	1.65	2.03

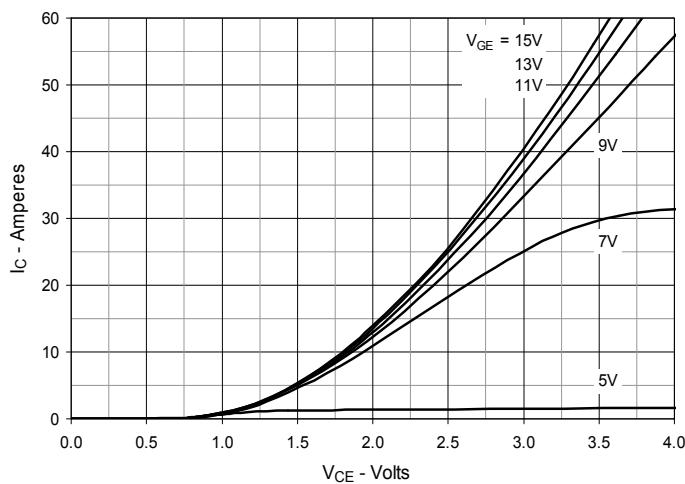
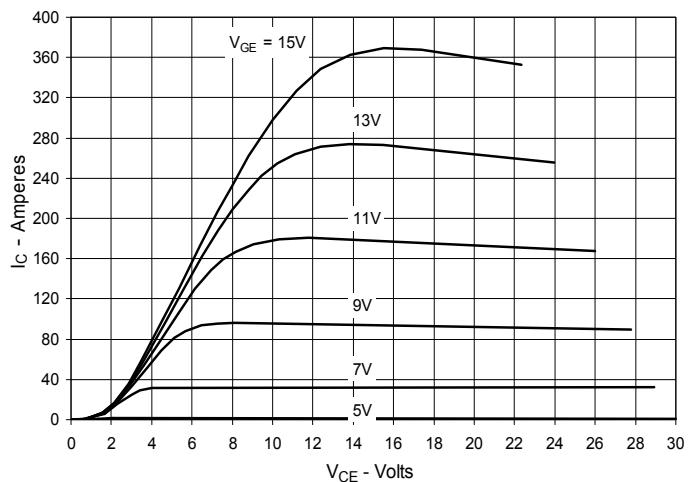
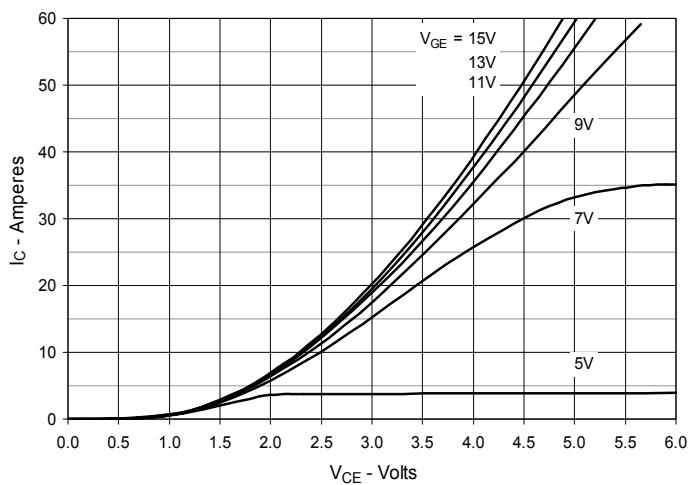
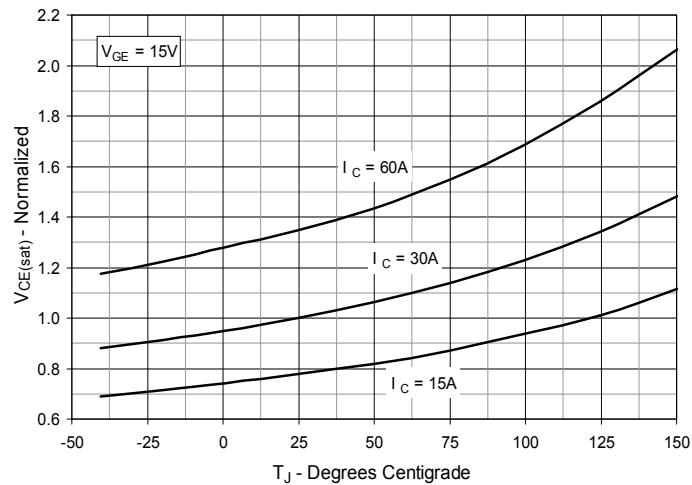
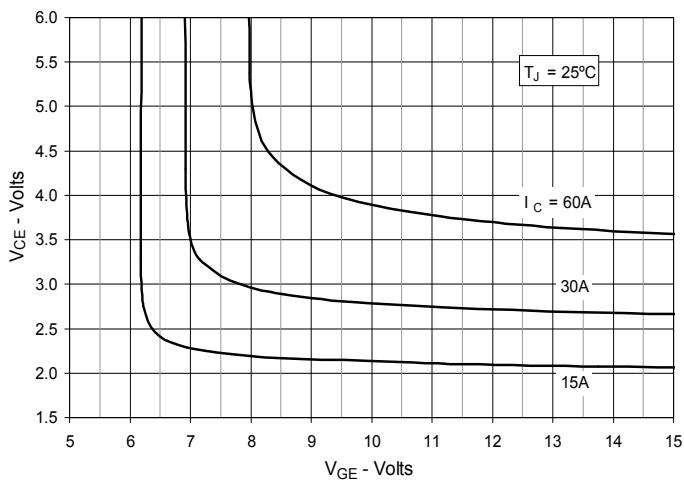
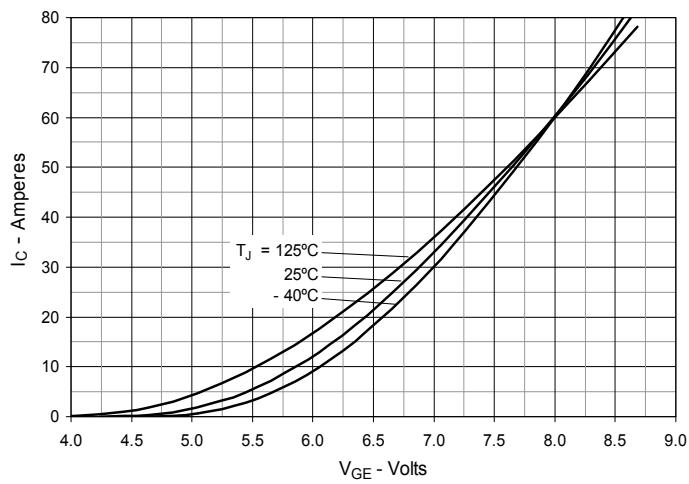
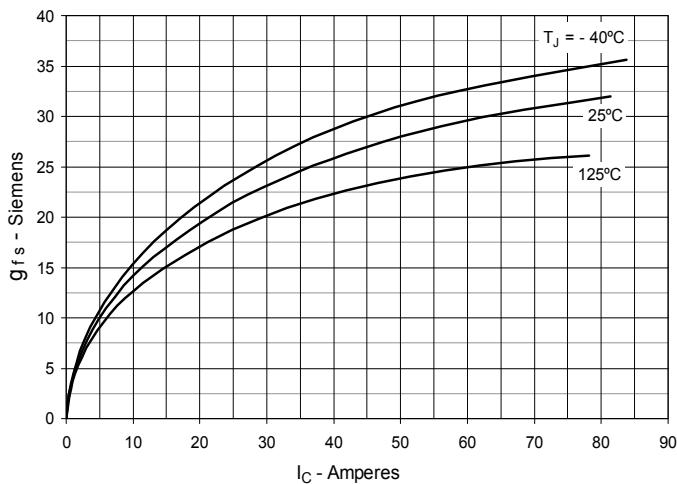
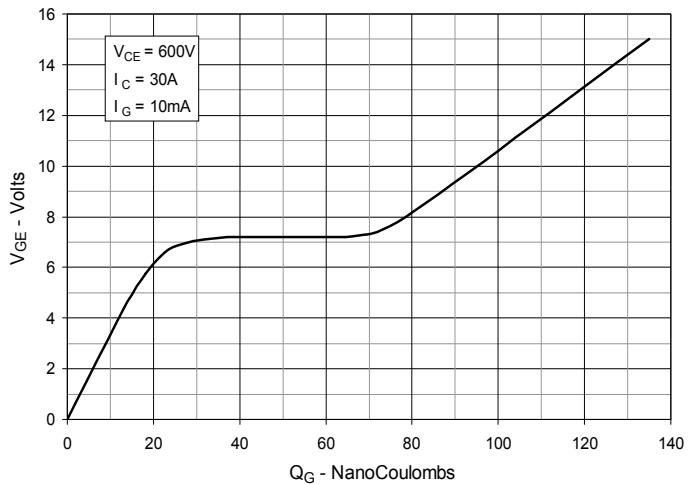
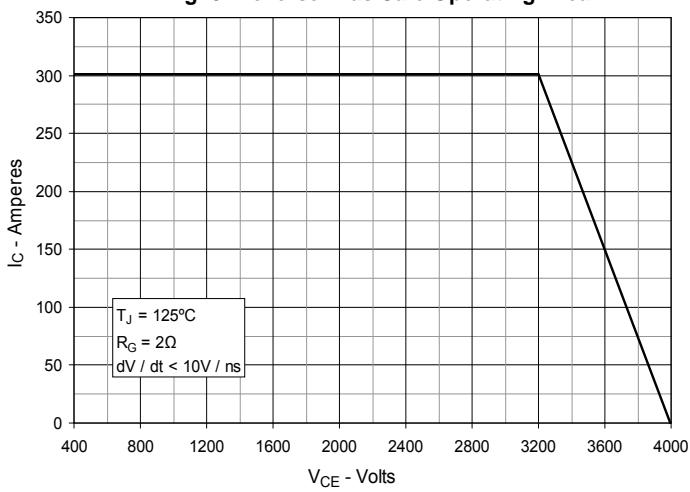
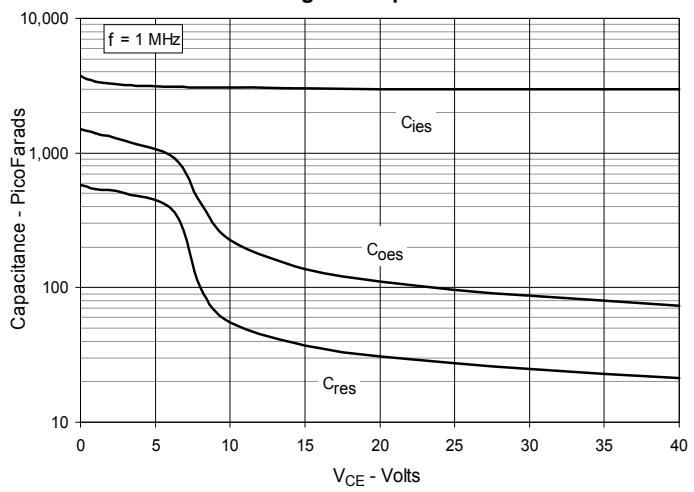
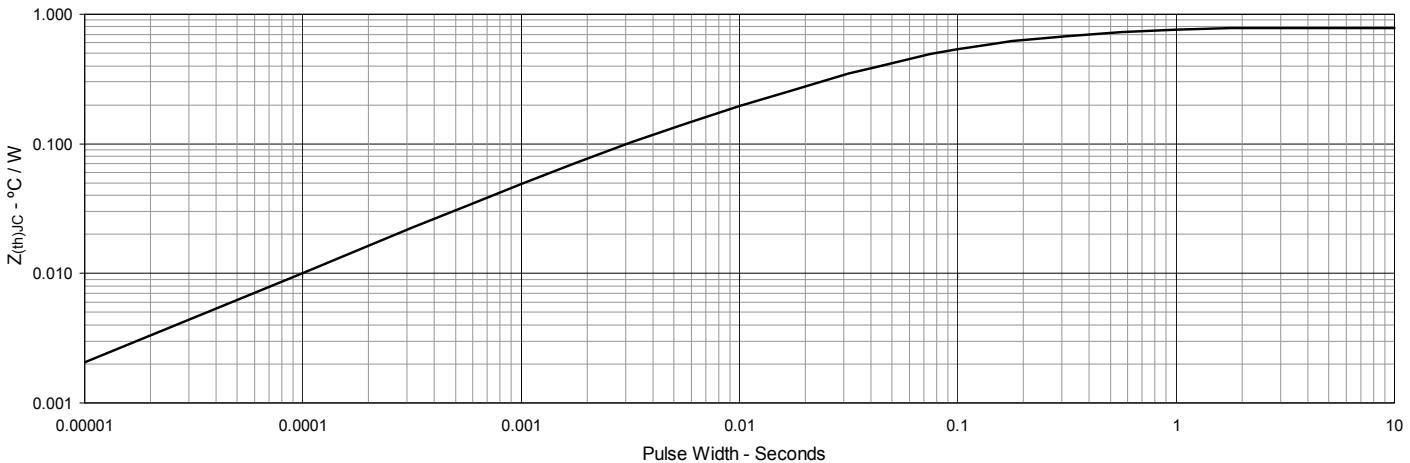
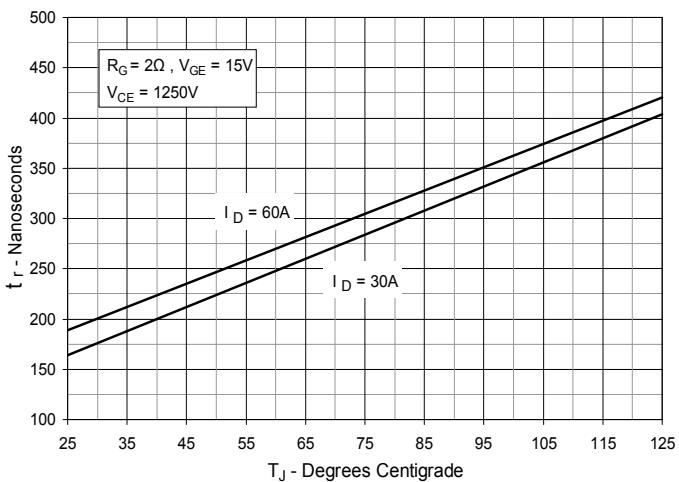
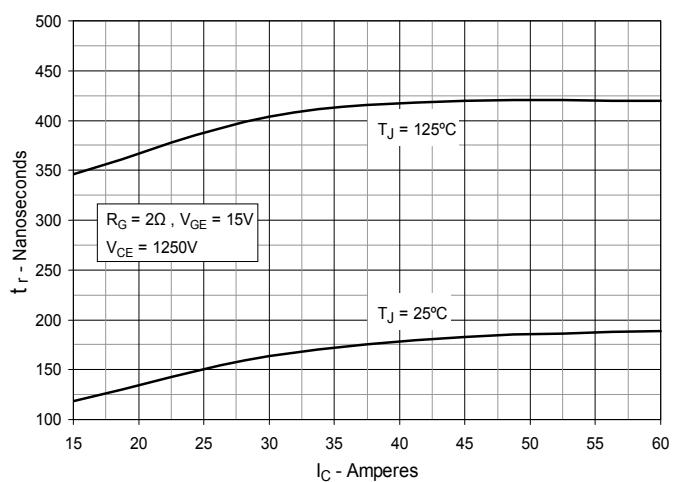
Fig. 1. Output Characteristics @ $T_J = 25^\circ\text{C}$ **Fig. 2. Extended Output Characteristics @ $T_J = 25^\circ\text{C}$** **Fig. 3. Output Characteristics @ $T_J = 125^\circ\text{C}$** **Fig. 4. Dependence of $V_{CE(\text{sat})}$ on Junction Temperature****Fig. 5. Collector-to-Emitter Voltage vs. Gate-to-Emitter Voltage****Fig. 6. Input Admittance**

Fig. 7. Transconductance**Fig. 8. Gate Charge****Fig. 9. Reverse-Bias Safe Operating Area****Fig. 10. Capacitance****Fig. 11. Maximum Transient Thermal Impedance**

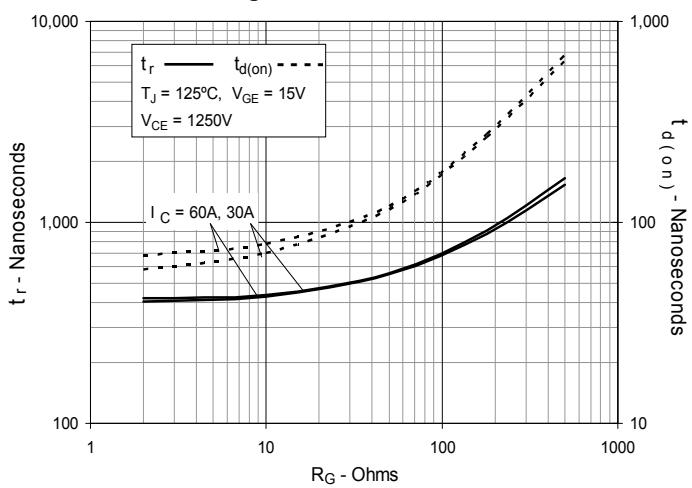
**Fig. 12. Resistive Turn-on
Rise Time vs. Junction Temperature**



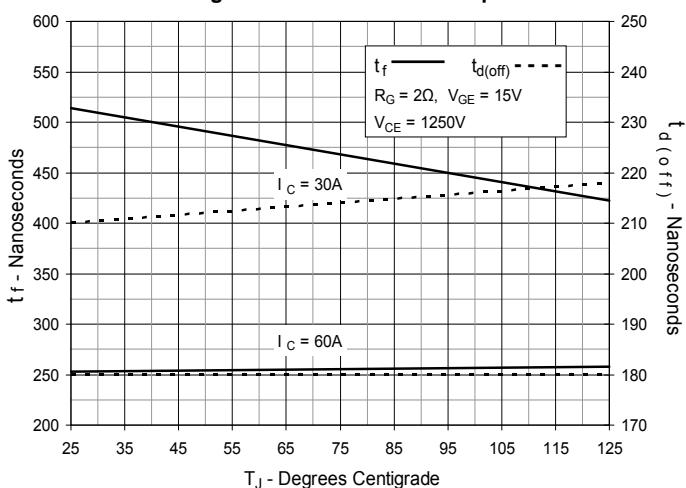
**Fig. 13. Resistive Turn-on
Rise Time vs. Drain Current**



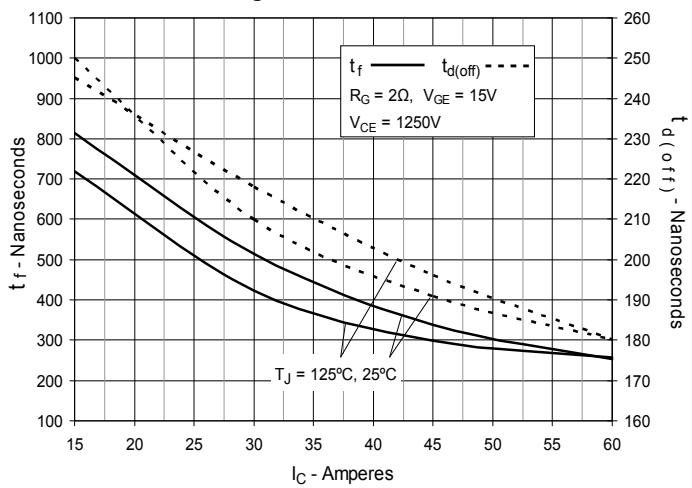
**Fig. 14. Resistive Turn-on
Switching Times vs. Gate Resistance**



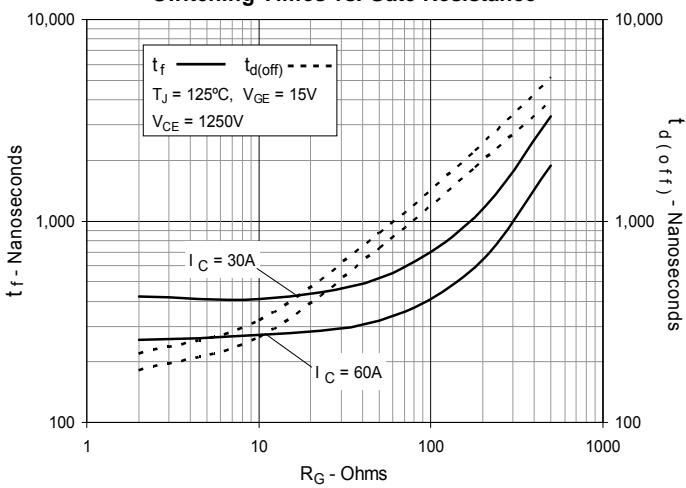
**Fig. 15. Resistive Turn-off
Switching Times vs. Junction Temperature**



**Fig. 16. Resistive Turn-off
Switching Times vs. Drain Current**



**Fig. 17. Resistive Turn-off
Switching Times vs. Gate Resistance**



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