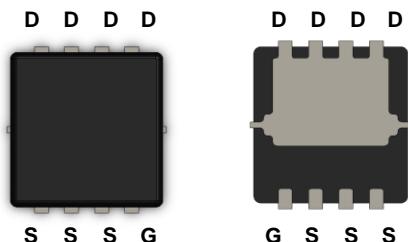


## General Description

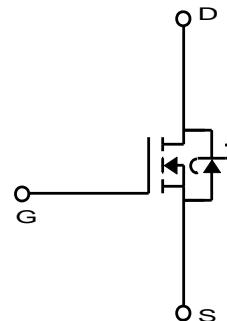
The MDV1595S uses advanced MagnaChip's MOSFET Technology, which provides high performance in on-state resistance, fast switching performance and excellent quality. MDV1595S is suitable for DC/DC converter and general purpose applications.

## Features

- $V_{DS} = 30V$
- $I_D = 36.1A @ V_{GS} = 10V$
- $R_{DS(ON)}$   
 $< 10.7m\Omega @ V_{GS} = 10V$   
 $< 13.0m\Omega @ V_{GS} = 4.5V$
- 100% UIL Tested
- 100% Rg Tested
- SBD Built In



PDFN33



## Absolute Maximum Ratings ( $T_a = 25^\circ C$ )

Characteristics	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DSS}$	30	V
Gate-Source Voltage	$V_{GSS}$	$\pm 12$	V
Continuous Drain Current <sup>(1)</sup>	$I_D$	36.1	A
		22.8	
		13.4 <sup>(3)</sup>	
		10.8 <sup>(3)</sup>	
Pulsed Drain Current	$I_{DM}$	80	A
Power Dissipation	$P_D$	24.5	W
		9.8	
		3.4 <sup>(3)</sup>	
		2.2 <sup>(3)</sup>	
Single Pulse Avalanche Energy <sup>(2)</sup>	$E_{AS}$	48	mJ
Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~150	°C

## Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient <sup>(1)</sup>	$R_{\theta JA}$	36	°C/W
Thermal Resistance, Junction-to-Case		5.1	

## Ordering Information

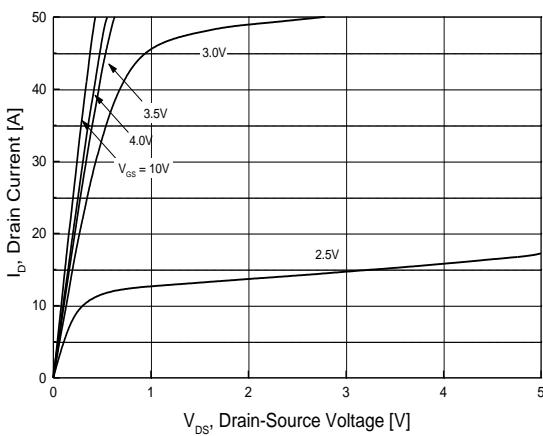
Part Number	Temp. Range	Package	Packing	RoHS Status
MDV1595SURH	-55–150°C	PDFN33	Tape & Reel	Halogen Free

## Electrical Characteristics ( $T_J = 25^\circ\text{C}$ )

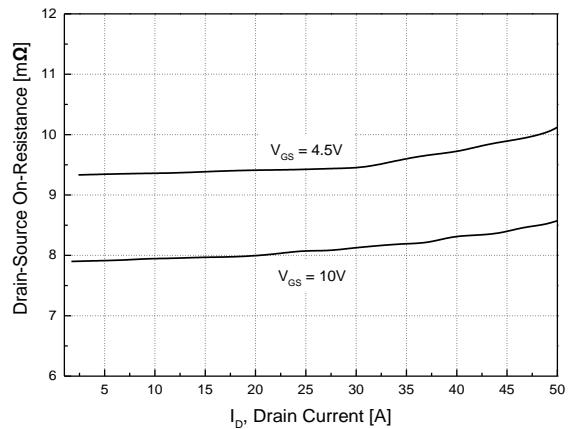
Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	30	-	-	V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.0	1.5	2.0	
Drain Cut-Off Current	$I_{\text{DSS}}$	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$ $T_J=125^\circ\text{C}$	-	-	0.5	mA
Gate Leakage Current	$I_{GSS}$	$V_{GS} = \pm 12\text{V}, V_{DS} = 0\text{V}$	-	-	$\pm 100$	nA
Drain-Source ON Resistance	$R_{DS(\text{ON})}$	$V_{GS} = 10\text{V}, I_D = 13\text{A}$ $T_J=125^\circ\text{C}$	-	8.2	10.7	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 11\text{A}$	-	14.9	13.5	
Forward Transconductance	$g_{fs}$	$V_{DS} = 5\text{V}, I_D = 13\text{A}$	-	10.0	13.0	
<b>Dynamic Characteristics</b>						
Total Gate Charge	$Q_{g(10\text{V})}$	$V_{DS} = 15.0\text{V}, I_D = 13\text{A}, V_{GS} = 10\text{V}$	15.6	22.3	29.0	nC
Total Gate Charge	$Q_{g(4.5\text{V})}$		6.9	9.9	12.9	
Gate-Source Charge	$Q_{gs}$		-	3.0	-	
Gate-Drain Charge	$Q_{gd}$		-	2.7	-	
Input Capacitance	$C_{iss}$	$V_{DS} = 15.0\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$	-	1426	1853	pF
Reverse Transfer Capacitance	$C_{rss}$		-	75.4	98	
Output Capacitance	$C_{oss}$		-	198	257	
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = 10\text{V}, V_{DS} = 15.0\text{V}, I_D = 13\text{A}, R_G = 3.0\Omega$	-	7.8	-	ns
Rise Time	$t_r$		-	3.1	-	
Turn-Off Delay Time	$t_{d(off)}$		-	33.5	-	
Fall Time	$t_f$		-	4.3	-	
Gate Resistance	$R_g$	f=1 MHz	0.5	1.0	2.0	$\Omega$
<b>Drain-Source Body Diode Characteristics</b>						
Source-Drain Diode Forward Voltage	$V_{SD}$	$I_S = 1\text{A}, V_{GS} = 0\text{V}$	-	0.45	0.7	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 13\text{A}, dI/dt = 100\text{A}/\mu\text{s}$	-	24.2	36.3	ns
Body Diode Reverse Recovery Charge	$Q_{rr}$		-	16.4	24.6	nC

Note :

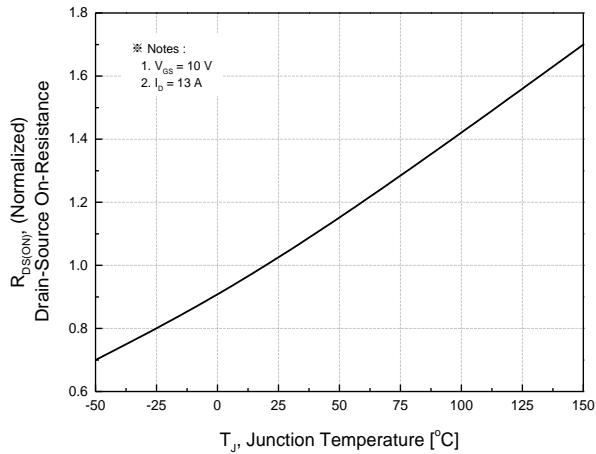
1. Surface mounted FR4 board with 2oz. Copper. Continuous current at  $T_C=25^\circ\text{C}$  is silicon limited.
2.  $E_{AS}$  is tested at starting  $T_J = 25^\circ\text{C}$ ,  $L = 0.1\text{mH}$ ,  $I_{AS} = 16.8\text{A}$ ,  $V_{DD} = 27\text{V}$ ,  $V_{GS} = 10\text{V}$ .
3.  $T < 10\text{sec}$



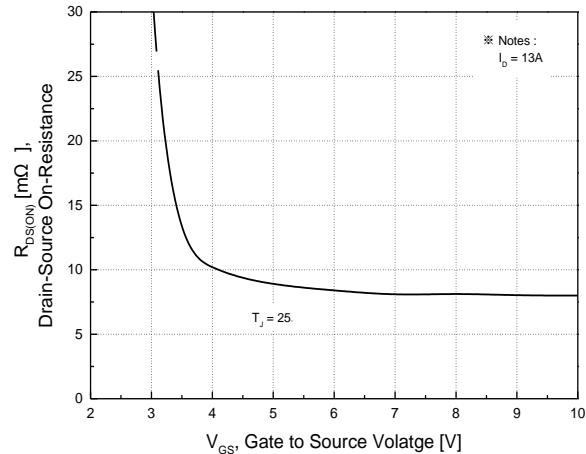
**Fig.1 On-Region Characteristics**



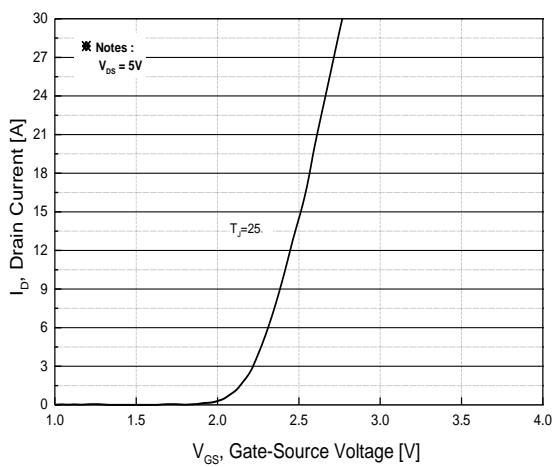
**Fig.2 On-Resistance Variation with Drain Current and Gate Voltage**



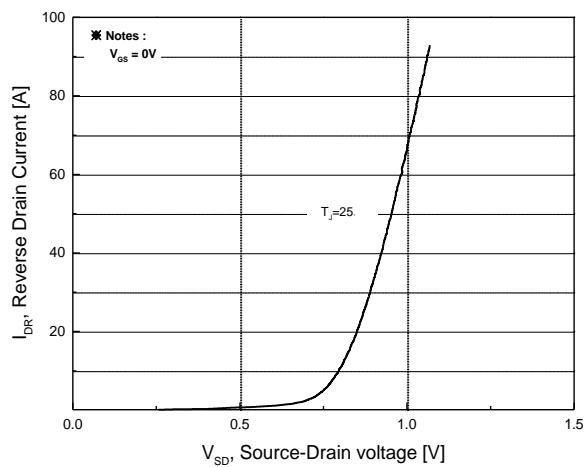
**Fig.3 On-Resistance Variation with**



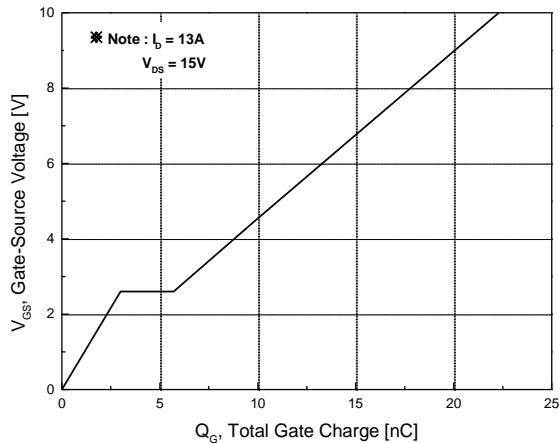
**Fig.4 On-Resistance Variation with Gate to Source Voltage**



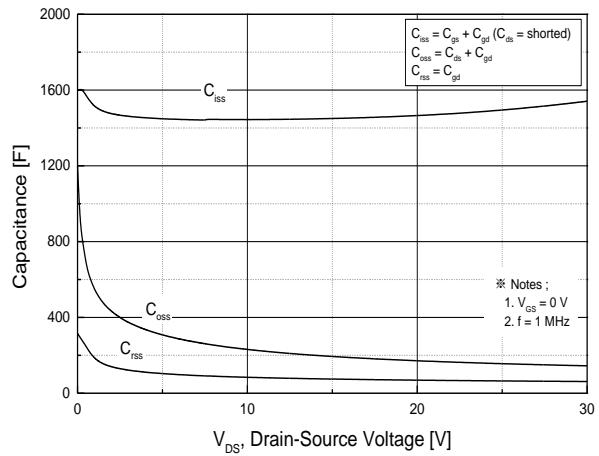
**Fig.5 Transfer Characteristics**



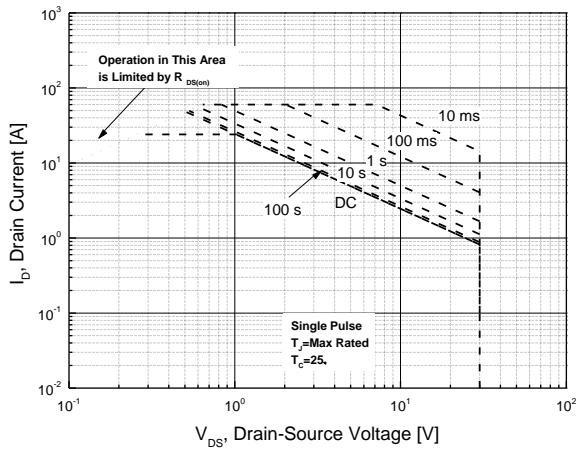
**Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature**



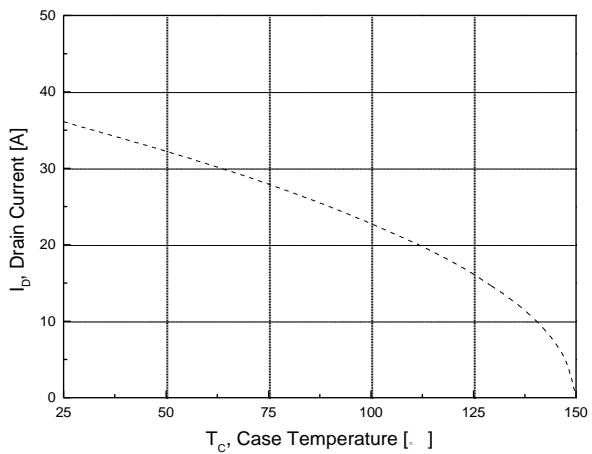
**Fig.7 Gate Charge Characteristics**



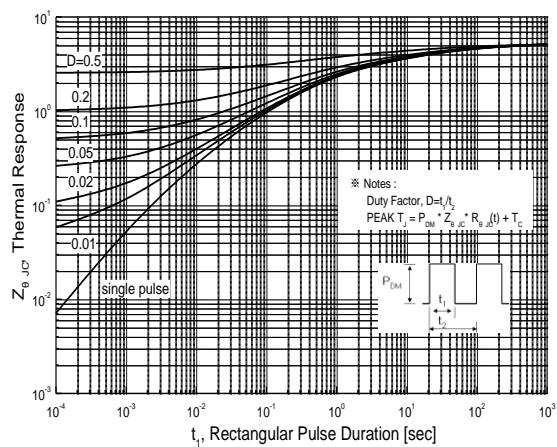
**Fig.8 Capacitance Characteristics**



**Fig.9 Maximum Safe Operating Area**



**Fig.10 Maximum Drain Current vs. Case Temperature**

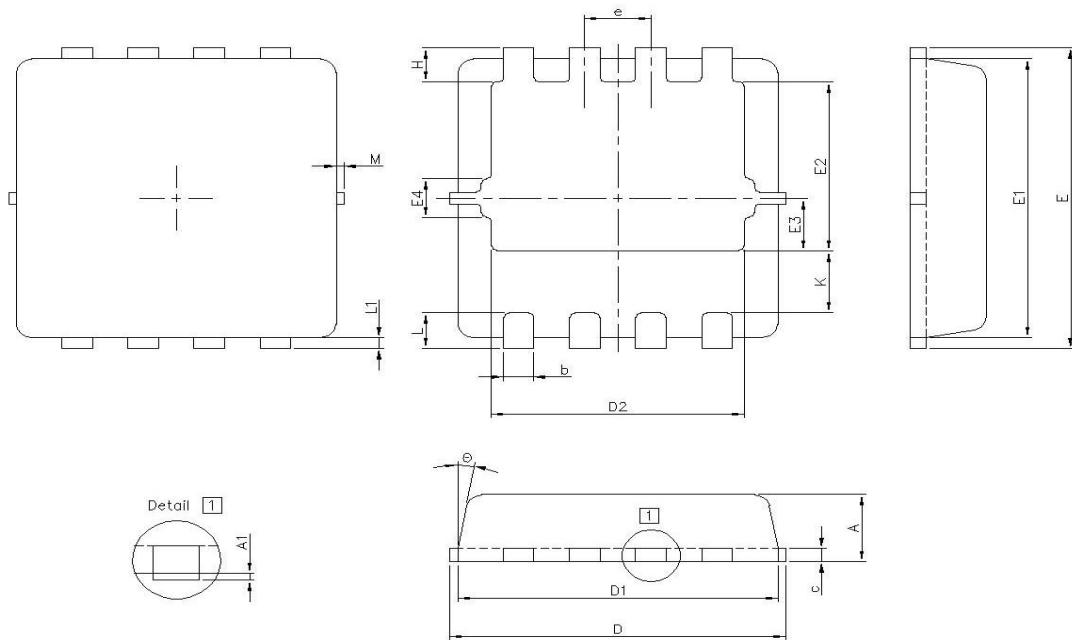


**Fig.11 Transient Thermal Response Curve**

## Package Dimension

### PDFN33 (3.3x3.3mm)

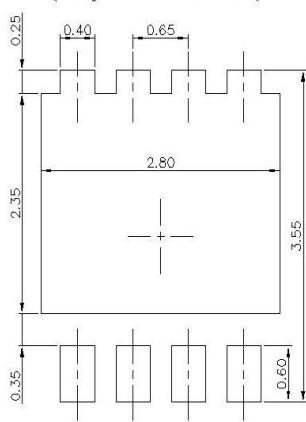
Dimensions are in millimeters, unless otherwise specified



(Unit:  
mm)

DIM	Min	Max	DIM	Min	Max
A	0.70	0.80	E2	1.78	1.98
A1	0.00	0.05	E3	0.49	0.69
b	0.25	0.35	E4	0.35	TYP.
c	0.10	0.25	e	0.65	BSC
D	3.20	3.40	K	0.70	TYP.
D1	3.00	3.20	L	0.30	0.50
D2	2.39	2.59	L1	0.13	TYP.
E	3.25	3.45	H	0.27	0.47
E1	3.00	3.20	Θ	0	12

Land Pattern  
(Only for Reference)



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