



# DDTA (R1 = R2 SERIES)

PNP PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSIS

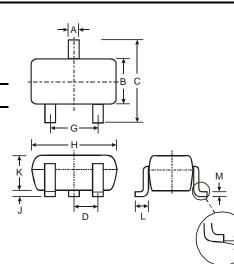
#### Features

- Epitaxial Planar Die Construction •
- Complementary NPN Types Available (DDTC) .
- Built-In Biasing Resistors, R1 = R2
- Lead Free/RoHS Compliant (Note 2)
- "Green" Device, Note 3 and 4

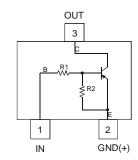
#### **Mechanical Data**

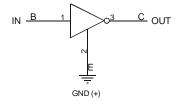
- Case: SC-59 •
- Case Material: Molded Plastic, "Green" Molding Compound, Note 4. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Copper leadframe).
- Terminal Connections: See Diagram
- Marking Information: See Table Below & Page 4
- Ordering Information: See Page 4
- Weight: 0.008 grams (approximate)

P/N	R1, R2 (NOM)	Type Code
DDTA123EKA	2.2KΩ	P04
DDTA143EKA	4.7KΩ	P08
DDTA114EKA	10KΩ	P13
DDTA124EKA	<b>22K</b> Ω	P17
DDTA144EKA	47ΚΩ	P20
DDTA115EKA	100KΩ	P24



	SC-59									
Dim	Min	Max								
Α	0.35	0.50								
в	1.50	1.70								
С	2.70	3.00								
D	0.95									
G	1.90									
н	2.90	3.10								
J	0.013	0.10								
К	1.00	1.30								
L	0.35	0.55								
М	0.10	0.20								
α	0°	8°								
All Dir	All Dimensions in mm									





Equivalent Inverter Circuit

Schematic and Pin Configuration

#### **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit	
Supply Voltage, (3) to (2)		V <sub>CC</sub>	-50	V	
Input Voltage, (1) to (2)	DDTA123EKA DDTA143EKA DDTA114EKA DDTA124EKA DDTA124EKA DDTA144EKA DDTA115EKA	V <sub>IN</sub>	+10 to -12 +10 to -30 +10 to -40 +10 to -40 +10 to -40 +10 to -40	V	
Output Current	DDTA123EKA DDTA143EKA DDTA114EKA DDTA124EKA DDTA124EKA DDTA144EKA DDTA115EKA	lo	-100 -100 -50 -30 -100 -20	mA	
Output Current	All	I <sub>C</sub> (Max)	-100	mA	
Power Dissipation		Pd	200	mW	
Thermal Resistance, Junction to Ambient	$R_{ ext{ heta}}JA$	625	°C/W		
Operating and Storage Temperature Rang	e	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C	

Notes: 1 Mounted on FR4 PC Board with recommended pad layout at http://www.diodes.com/datasheets/ap02001.pdf.

2. No purposefully added lead.

3.

Product manufactured with Date Code 0627 (week 27, 2006) and newer are built with Green Molding Compound. Product manufactured prior to 4. Date Code 0627 are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.



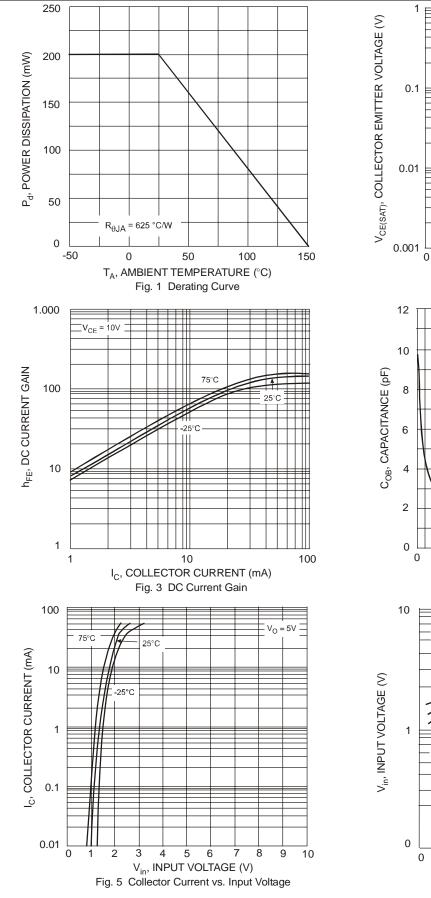
### **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

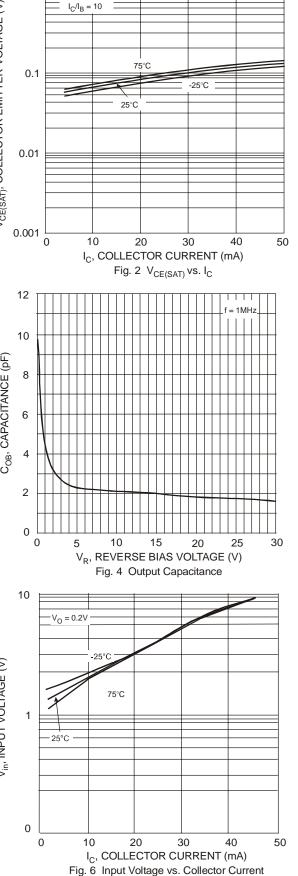
Characteristic	Symbol	Min Typ		Max	Unit	Test Condition	
	V <sub>l(off)</sub>	-0.5	-1.1	_		V <sub>CC</sub> = -5V, I <sub>O</sub> = -100μA	
Input Voltage	V <sub>I(on)</sub>		-1.9	-3	V	$\label{eq:VO} \begin{array}{l} V_{O} = -0.3V, \ I_{O} = -20mA, \ DDTA123EKA \\ V_{O} = -0.3V, \ I_{O} = -20mA, \ DDTA143EKA \\ V_{O} = -0.3V, \ I_{O} = -10mA, \ DDTA114EKA \\ V_{O} = -0.3V, \ I_{O} = -5mA, \ DDTA124EKA \\ V_{O} = -0.3V, \ I_{O} = -2mA, \ DDTA144EKA \\ V_{O} = -0.3V, \ I_{O} = -1mA, \ DDTA115EKA \end{array}$	
Output Voltage		V <sub>O(on)</sub>	_	-0.1	-0.3	V	I <sub>O</sub> /I <sub>I</sub> = -10mA/-0.5mA, DDTA123EKA I <sub>O</sub> /I <sub>I</sub> = -10mA/-0.5mA, DDTA143EKA I <sub>O</sub> /I <sub>I</sub> = -10mA/-0.5mA, DDTA114EKA I <sub>O</sub> /I <sub>I</sub> = -10mA/-0.5mA, DDTA124EKA I <sub>O</sub> /I <sub>I</sub> = -10mA/-0.5mA, DDTA144EKA I <sub>O</sub> /I <sub>I</sub> = -5mA/-0.25mA, DDTA115EKA
Input Current	DDTA123EKA DDTA143EKA DDTA114EKA DDTA124EKA DDTA124EKA DDTA114EKA DDTA115EKA	I			-3.8 -1.8 -0.88 -0.36 -0.18 -0.15	mA	V <sub>1</sub> = -5V
Output Current		I <sub>O(off)</sub>			-0.5	μΑ	$V_{CC} = -50V, V_I = 0V$
DDTA123EKA DDTA143EKA DDTA143EKA DDTA114EKA DDTA124EKA DDTA124EKA DDTA144EKA DDTA115EKA		Gı	20 20 30 56 68 82				$V_{O} = -5V, I_{O} = -20mA$ $V_{O} = -5V, I_{O} = -10mA$ $V_{O} = -5V, I_{O} = -5mA$ $V_{O} = -5V, I_{O} = -5mA$ $V_{O} = -5V, I_{O} = -5mA$ $V_{O} = -5V, I_{O} = -5mA$
Input Resistor (R <sub>1</sub> ) Tolerance		$\Delta R_1$	-30	_	+30	%	_
Resistance Ratio		$R_2/R_1$	0.8	1	1.2	—	—
Gain-Bandwidth Product*		f <sub>T</sub>		250		MHz	V <sub>CE</sub> = -10V, I <sub>E</sub> = 5mA, f = 100MHz

\* Transistor - For Reference Only



#### **Typical Curves – DDTA143EKA**





3 of 4 www.diodes.com DDTA (R1 = R2 SERIES) KA © Diodes Incorporated

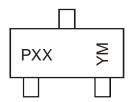


#### Ordering Information (Note 4 & 5)

Device	Packaging	Shipping		
DDTA123EKA-7-F	SC-59	3000/Tape & Reel		
DDTA143EKA-7-F	SC-59	3000/Tape & Reel		
DDTA114EKA-7-F	SC-59	3000/Tape & Reel		
DDTA124EKA-7-F	SC-59	3000/Tape & Reel		
DDTA144EKA-7-F	SC-59	3000/Tape & Reel		
DDTA115EKA-7-F	SC-59	3000/Tape & Reel		

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

#### **Marking Information**



 $\begin{array}{l} \mathsf{PXX}=\mathsf{Product}\ \mathsf{Type}\ \mathsf{Marking}\ \mathsf{Code},\ \mathsf{See}\ \mathsf{Table}\ \mathsf{on}\ \mathsf{Page}\ \mathsf{1}\\ \mathsf{YM}=\mathsf{Date}\ \mathsf{Code}\ \mathsf{Marking}\\ \mathsf{Y}=\mathsf{Year}\ \mathsf{ex:}\ \mathsf{T}=2006\\ \mathsf{M}=\mathsf{Month}\ \mathsf{ex:}\ \mathsf{9}=\mathsf{September} \end{array}$ 

Date Code Key

Year	2002	2003	2004	2005	5 200	)6 2	007	20	800	2009	2010	2011	2012
Code	Ν	Р	R	S	Т	U			V	W	Х	Y	Z
Month	Jan	Feb	Mar	Apr	Мау	Jun	J	ul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	7	8	9	0	N	D

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