

APPROVAL SHEET

WLPN404010 Series Shielded SMD Power Inductors

*Contents in this sheet are subject to change without prior notice.

Features

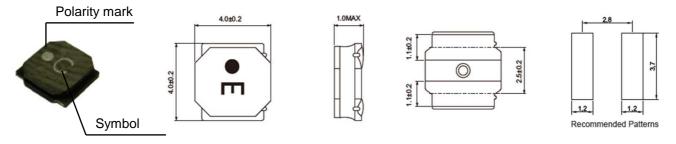
- 1. Close magnetic loop with magnetic resin shielded.
- 2. Low profile, High inductance.

Applications

- 1. General propose power inductor in DC power system.
- 2. Inductor in DC/DC converter.
- 3. Low profile for portable and wearable device.
- 4. LC filter in Audio D class Amplifier.

Shape and Dimension

Unit: mm



Ordering Information

WL	PN	4040	10	N	1R2	L	В
Product Code	Series	Dimensions	Thickness	Tolerance	Value	Packing Code	
WL: Inductor	Shielded SMD Power Inductors	4.0 * 4.0 mm	1.0 mm	M: ± 20% N: ± 30%	1R2 = 1.2uH 100 = 10uH	L=13" Reeled (Embossed Tape)	B:STD

Electrical Characteristics

		Symbol	Inductance	Test Freg	DCR		Rated Current	
WLPN404010	L Symbol Freg					SRF	(mA) Max	
Series		(Ω±20%)	(MHz)Min	Saturation Current Idc1	Temperature Rise Current Idc2			
WLPN404010N1R0LB	1.0	Α	±30%	100	0.056	116	2000	1900
WLPN404010M2R2LB	2.2	С	±20%	100	0.085	73	1200	1500
WLPN404010M3R3LB	3.3	E	±20%	100	0.100	58	1100	1400
WLPN404010M4R7LB	4.7	Н	±20%	100	0.140	47	950	1200
WLPN404010M6R8LB	6.8	I	±20%	100	0.200	38	800	1000
WLPN404010M100LB	10	К	±20%	100	0.300	31	620	750
WLPN404010M150LB	15	М	±20%	100	0.430	24	540	600
WLPN404010M220LB	22	Ν	±20%	100	0.570	19	450	500

1. Test Frequency: 100KHz

2. Test Equipment:

Inductance: Chroma3302+1320+16502 or equivalent. DCR: Chroma16502 or equivalent.

SRF: HP4291B or equivalent.

3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.

4. Temperature rise current ldc2: The value of current causes a 40 $^\circ\!\mathrm{C}$ temperature rise.

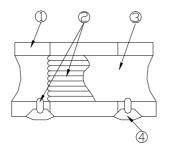
5. Rated Current: Either Idc1 or Idc2 whichever is smaller.

6. Operating Temperature Range:-25℃ to +120℃ (Including self-temperature rise)

7. Storage Temp. Range : -40° C to $+85^{\circ}$ C.

8. MSL : Level 1

Structural Drawing

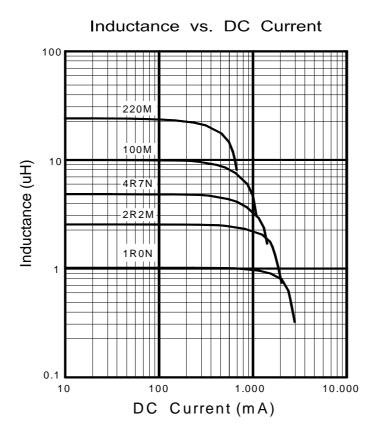


- ① Ferrite core : Ni-Zn ferrite
- ② Winding wire : Polyurethane-copper wire
- ③ Over-coating resin : Epoxy resin, containing ferrite powder
- ④ Electrode : External electrode (substrate) Cu

External electrode (top surface solder coating) Sn-Ag-Cu

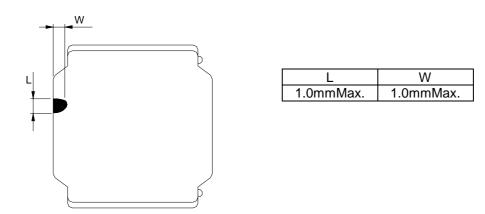


Characteristic Curve



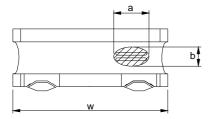
Core Chipping:

The appearance standard of the chipping size in top side, of bottom side ferrite Core is following dimension.



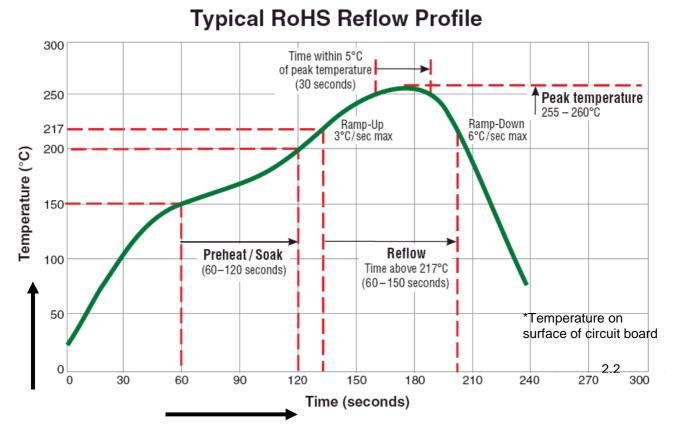


Exposed wire tolerance limit of coating resin part on product side Size of exposed wire occurring to coating resin is specified below.



- Width direction (dimension a): Acceptable when a<=w/2 Nonconforming when a>w/2
 Length direction (dimension b): Dimension b is not specified.
- When total area of exposed wire occurring to each sides is
 - not greater than 50% of coating resin area, that is acceptable.

Reflow Profile Chart (Reference):



(Table 1)

The products may be exposed to reflow soldering process of above profile up to two times.

Mechanical Performance /Environmental Test Performance Specifications: (WLPN404010 series)

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No.	ltem	Test condition	Requirements					
	Resistance to Deflection	No damage.	The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1. As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm. 20					
			Force R230					
1			R5 45 ± 2 45 ± 2 0.8 1.4 0.8					
			Land dimensionsTest board size :100×40×10Unit: mmTest board material I: glass epoxy-resinSolder cream thickness:0.1					
	Adhesion of Terminal Electrode	Shall not come off PC board	The test samples shall be soldered to the test board By the reflow soldering conditions shown in Table 1. 10 N, 5 s					
2			Applied force: 10 N to X and Y directions Duration: 5 s. Solder cream thickness:0.1 mm (Refer to recommended Land Pattern Dimensions Defined in "Precaution")					
	Body strength	No damage	Applied force :20 N Duration :10 s					
3								
	Resistance to Vibration	△L/L:within±10% No abnormality observed In	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1.Then It shall be submitted to below test conditions					
		appearance	Frequency range 10Hz~55Hz					
4			Total Amplitude 1.5mm(May not exceed acceleration 196 m/S2)					
			Sweeping Method 10Hz to 55Hz to 10 Hz for 1 min.					
			Time For 2 hours on each X, Y, and Z axis.					
5	Resistance to Soldering heat (Reflow)	△L/L:within±10% No abnormality observed In appearance	The test sample shall be exposed to reflow oven at 230±5 deg C for 40 seconds, with peak temperature at 260±5 deg C for 5 seconds, 2 times. Test board thickness:1.0 mm					
			Test board thickness: 1.0 mm Test board material :glass epoxy-resin					

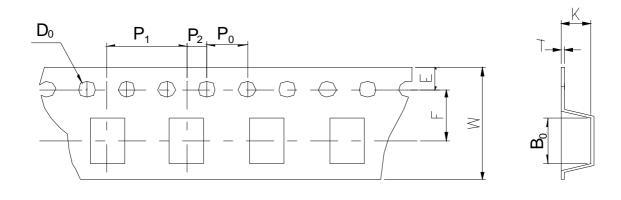
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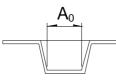
	Solder ability	At least 90% of surface of terminal electrode is	molten s	t samples shall solder as shown	in below	table.	hen Immers	ed in		
c		covered by new		ethanol solution r Temperature		5±deg C	7			
6		solder.	Time			5±009 0 5±1.0 S.				
			Immersing Speed			5 mm/s				
						0 1111/3				
7	Temperature Characteristics	△L/L:within±20% No abnormality observed In appearance	Measurement of inductance shall be taken at temperature Range within -25 deg C to +85 deg C. With reference to inductance value at +20 deg C, change Rate shall be calculated.							
	Thermal shock	△L/L:within±10% No abnormality observed In appearance	The test samples shall be soldered to test board By the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified Shown in below table in sequence. The temperature cycle shall be repeated 100 cycles.							
8				ns of steps for 1	-					
			Step	Temperat		Time(r				
			1	-40±3 deg	-		30±3			
				2 Room Temp		3 maximum				
			3 85±2 deg C			30±3				
	Lew		4 Room Temp				3 maximum			
9	Low Temperature life Test	△L/L:within±10% No abnormality observed In appearance	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1. After that, the test samples shall be placed at test Conditions as shown in below table. Temperature -40±2 deg C Time 500 +24/-0 h							
10	Loading at high temperature life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and applied the rated current continuously as shown in below table. Temperature 85±2 deg C Applied current Rated current (Refer to Page 2) Time 500+24/-0 h							
	Damp heat life	△L/L:within±10%		samples shall b	be soldere	ed to the test b	board by the	reflow		
11	test	No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflest soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at spatial temperature and humidity as shown in below table. Temperature 60±2 deg C Humidity 90~95%RH Time 500+24/-0 h							
	Loading under	\triangle L/L:within±10%					board by the	reflow		
12	Damp heat life test	No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at spectomerature and humidity and applied the rated current continuou as shown in below table. Temperature $60\pm 2 \deg C$ Humidity $90\sim95\%$ RH Applied current Rated current (Refer to Page 2) Time $500+24/-0 h$							



Tape & Reel Packaging Dimensions:

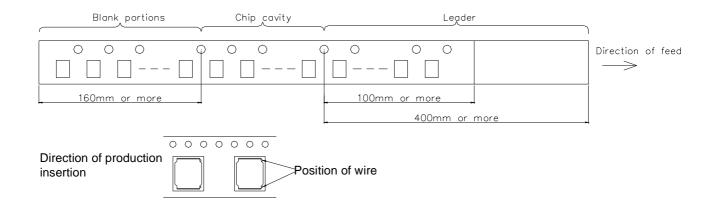
Dimensions Unit: mm





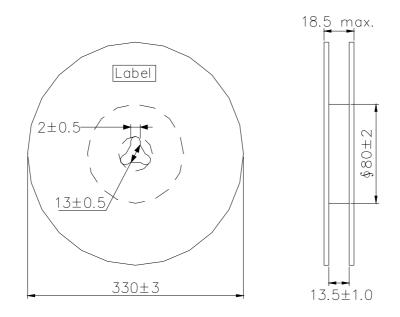
Ao	Bo	W	F	E	P1	P ₂	Po	D ₀	Т	K
4.3 ±0.1	4.3 ±0.1	12.0 ±0.3	5.5 ±0.1	1.75 ±0.1	8.0 ±0.1	2.0 土0.1	4.0 ±0.1	Ф1.5 +0.1 -0	0.3 ±0.05	1.4 ±0.1

Direction of rolling

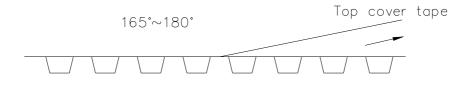




Reel



Top tape strength



Peel-off strength: 0.1N~1.3N Peel-off angle:165°~180° Peel-off speed: 300mm/mm

Quantity per reel : 5K pcs

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