



Description

- Standard 3.2 x 2.5mm crystal oscillator
Ceramic package with a seam sealed metal lid, hermetically sealed
- Model: CFPS-39
- Model Issue number: 4

Frequency Parameters

- Frequency: 48.0MHz
- Frequency Tolerance @ 25°C: INCLUSIVE
- Frequency Stability: ±50.00ppm
- Operating Temperature Range: -40.00 to 85.00°C
- Ageing: ±3ppm max per year @ 25°C

Electrical Parameters

- Supply Voltage: 3.3V
- Supply Voltage Tolerance: ±10%
- Current Draw: 20mA max

Output Details

- Output Compatibility: CMOS
- Output Load: 15pF max
- Rise and Fall time: 5ns max
- Duty Cycle: 45/55%

Output Control

- Standby Operation:
Logic '1' (>70% VS) to pad 1 enables oscillator output
Logic '0' (<30% VS) to pad 1 disables oscillator output; when disabled the oscillator output goes to the high impedance state
No connection to pad 1 enables oscillator output
Standby Current: 10µA max
Start-Up Time: 10ms max

Environmental Parameters

- Shock: MIL-STD-202F, Method 213B: 1000G, 0.5ms, 1/2 sine wave
- Vibration: MIL-STD-202F, Method 204D, Test Condition D: 20G (10Hz-2000Hz), 4hrs in 3 mutually perpendicular planes (total 12hrs)
- Storage Temperature Range: -55 to 125°C

Compliance

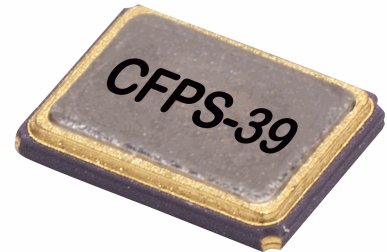
- RoHS Status: Compliant
- REACH Status: Compliant
- MSL Rating (JDEC-STD-033): Not Applicable

Packaging

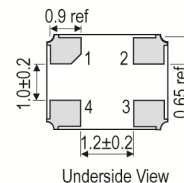
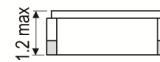
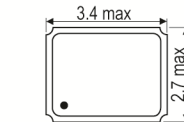
- Pack Type: Reel: Tape & reel in accordance with EIA-481-D
- Pack Size: 1,000

- Alternative packing option available

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Outline (mm)

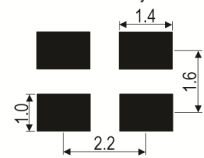


Underside View

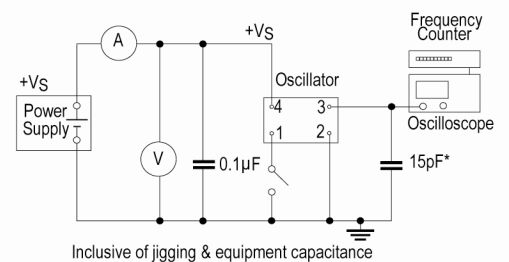
Pad Connections

- Standby Operation
- GND
- Output
- +Vs

Solder Pad Layout



Test Circuit



Inclusive of jigging & equipment capacitance

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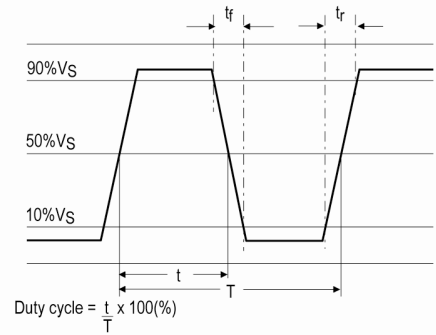
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Wave Form



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