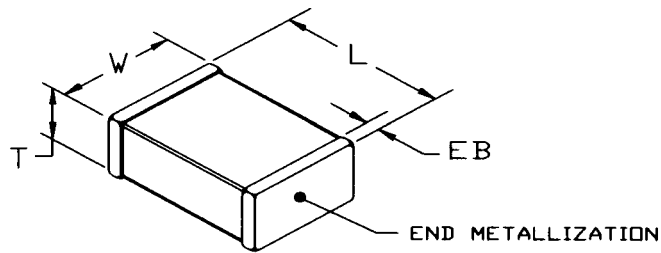


ROHS COMPLIANT



SPECIFICATIONS:

ELECTRICAL CHARACTERISTICS:

| | |
|-------------------------|---|
| DIELECTRIC | X7R |
| RATED VOLTAGE | 50 VDC |
| CAPACITANCE RANGE (EIA) | 1000 pFd (102) |
| CAPACITANCE TOLERANCE | K (± 10%) |
| TEST PARAMETERS | 1 kHz ±50 Hz @ 1.0 VRMS, 25°C |
| TEMPERATURE COEFFICIENT | ±15% (-55°C TO +125°C) |
| DISSIPATION FACTOR | < 2.5% @ 1kHz, 25°C |
| INSULATION RESISTANCE | > 1000 Ohm-Farad OR 100 G-Ohms, whichever is less @ WVDC, 25°C (@ 125°C IR is 10% of 25°C requirement) |
| DIELECTRIC STRENGTH | > 2.5 x WVDC, 50 mA max |

MECHANICAL CHARACTERISTICS

| LENGTH "L" | WIDTH "W" | THICKNESS "T" | ENDBAND "EB" |
|------------|-----------|---------------|--------------|
| .050±.010 | .040±.010 | .040 MAX | .010±.005 |

TERMINATION DESCRIPTION:

TERMINATION: P - PALLADIUM SILVER

MARKING:

4 - NOT MARKED

| | | | | | | | |
|----------------------|------------------|-----------|------|---|--|--------------------------------------|------|
| | | | | | | 15191 BLEDSOE ST SYLMAR, CA 91342 | |
| | | | | TITLE MULTILAYER CHIP CAPACITOR | | | |
| REV | DESCRIPTION | APPROVED | DATE | PART NO. 500R11W102KP4 | | DWG SCALE NONE | |
| ENG. MGR. | DATE | Q.A. MGR. | DATE | DWG. NO. 500R11W102KP4 | | SHEET 1 OF 1 | RE V |
| DRN BY C. ENGLAND | DATE 11/25/09 | CHKD BY | DATE | | | | |

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