### MIC5365/6



### **High-Performance Single 150mA LDO**

### **General Description**

The MIC5365/6 is an advanced general purpose linear regulator offering high power supply rejection (PSRR) in an ultra-small 1mm × 1mm package. The MIC5366 includes an auto-discharge feature that is activated when the enable pin is low. The MIC5365/6 is capable of sourcing 150mA output current and offers high PSRR making it an ideal solution for any portable electronic application.

Ideal for battery-powered applications, the MIC5365/6 offers 2% initial accuracy, low dropout voltage (155mV @ 150mA), and low ground current (typically 29 $\mu$ A). The MIC5365/6 can also be put into a zero-off-mode current state, drawing virtually no current when disabled.

The MIC5365/6 is available in several advanced packages including a lead-free (RoHS-compliant) 1mm  $\times$  1mm Thin MLF® occupying only 1mm² of PCB area, a 75% reduction in board area compared to SC-70 and 2mm  $\times$  2mm MLF packages. It is also available in a thin SOT23-5 package.

The MIC5365/6 has an operating junction temperature range of –40°C to 125°C.

Data sheets and support documentation can be found on Micrel's web site at: <a href="https://www.micrel.com">www.micrel.com</a>.

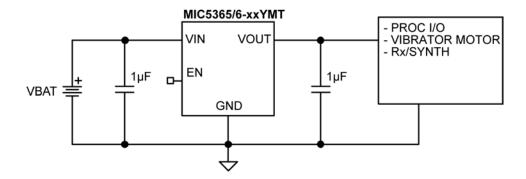
#### **Features**

- Tiny 1mm x 1mm thin MLF, SC-70-5, and thin SOT23-5 packages
- Input voltage range: 2.5V to 5.5V
- 150mA guaranteed output current
- Stable with 1µF ceramic output capacitors
- Low dropout voltage 155mV @ 150mA
- Excellent load/line transient response
- Low quiescent current 29µA
- High PSRR 70dB
- Output discharge circuit MIC5366
- High output accuracy
  - ±2% initial accuracy
- Thermal-shutdown and current-limit protection

### **Applications**

- Mobile phones
- Digital cameras
- · GPS, PDAs, PMP, handhelds
- Portable electronics

## **Typical Application**

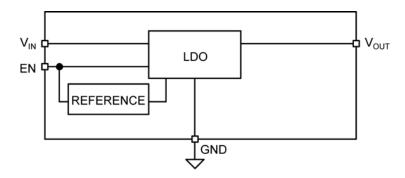


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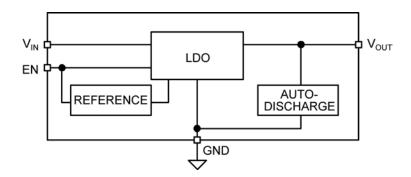
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## **Block Diagrams**



MIC5365 Block Diagram



MIC5366 Block Diagram

# **Ordering Information**

Part Number	Marking Code	Output Voltage	Temperature Range	Package	Lead Finish
MIC5365-1.0YMT	5C	1.0V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-1.2YMT	54	1.2V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-1.3YMT	55	1.3V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-1.5YMT	5F	1.5V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-1.8YMT	5G	1.8V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-2.0YMT	5H	2.0V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-2.5YMT	5J	2.5V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-2.6YMT	5K	2.6V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-2.7YMT	5L	2.7V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-2.8YMT	5M	2.8V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-2.85YMT	5N	2.85V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-2.9YMT	50	2.9V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-3.0YMT	5P	3.0V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-3.1YMT	5Q	3.1V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-3.3YMT	5S	3.3V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5365-1.0YC5	<u>65</u> C	1.0V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5365-1.2YC5	<u>65</u> 4	1.2V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5365-1.3YC5	<u>65</u> 5	1.3V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5365-1.5YC5	<u>65</u> F	1.5V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5365-1.8YC5	<u>65</u> G	1.8V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5365-2.0YC5	<u>65</u> H	2.0V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5365-2.5YC5	<u>65</u> J	2.5V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5365-2.6YC5	<u>65</u> K	2.6V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5365-2.7YC5	<u>65</u> L	2.7V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5365-2.8YC5	<u>65</u> M	2.8V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5365-2.85YC5	<u>65</u> N	2.85V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5365-2.9YC5	<u>65</u> O	2.9V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5365-3.0YC5	<u>65</u> P	3.0V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5365-3.3YC5	<u>65</u> S	3.3V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5365-1.2YD5	<u>64</u> 5	1.2V	-40°C to +125°C	5-Pin TSOT23	Pb-Free
MIC5365-1.8YD5	<u>6G</u> 5	1.8V	-40°C to +125°C	5-Pin TSOT23	Pb-Free
MIC5365-2.8YD5	<u>6M</u> 5	2.8V	-40°C to +125°C	5-Pin TSOT23	Pb-Free
MIC5365-2.85YD5	<u>6N</u> 5	2.85V	-40°C to +125°C	5-Pin TSOT23	Pb-Free
MIC5365-3.3YD5	<u>6S</u> 5	3.3V	-40°C to +125°C	5-Pin TSOT23	Pb-Free
MIC5366-1.0YMT*	6C	1.0V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5366-1.2YMT*	64	1.2V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5366-1.3YMT*	65	1.3V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5366-1.5YMT*	6F	1.5V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5366-1.8YMT*	6G	1.8V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5366-2.0YMT*	6H	2.0V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5366-2.5YMT*	6J	2.5V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5366-2.6YMT*	6K	2.6V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5366-2.7YMT*	6L	2.7V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free

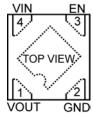
## **Ordering Information (Continued)**

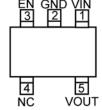
Part Number	Marking Code	Output Voltage	Temperature Range	Package	Lead Finish
MIC5366-2.8YMT*	6M	2.8V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5366-2.85YMT*	6N	2.85V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5366-2.9YMT*	60	2.9V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5366-3.0YMT*	6P	3.0V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5366-3.1YMT*	6Q	3.1V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5366-3.3YMT*	6S	3.3V	-40°C to +125°C	4-Pin 1mm × 1mm Thin MLF	Pb-Free
MIC5366-1.0YC5*	<u>66</u> C	1.0V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5366-1.2YC5*	<u>66</u> 4	1.2V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5366-1.3YC5*	<u>66</u> 5	1.3V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5366-1.5YC5*	<u>66</u> F	1.5V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5366-1.8YC5*	<u>66</u> G	1.8V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5366-2.0YC5*	<u>66</u> H	2.0V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5366-2.5YC5*	<u>66</u> J	2.5V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5366-2.6YC5*	<u>66</u> K	2.6V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5366-2.7YC5*	<u>66</u> L	2.7V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5366-2.8YC5*	<u>66</u> M	2.8V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5366-2.85YC5*	<u>66</u> N	2.85V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5366-2.9YC5*	<u>66</u> O	2.9V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5366-3.0YC5*	<u>66</u> P	3.0V	-40°C to +125°C	SC-70-5	Pb-Free
MIC5366-3.3YC5*	<u>66</u> S	3.3V	-40°C to +125°C	SC-70-5	Pb-Free

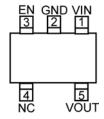
#### Notes:

- 1. Other voltages available. Contact Micrel for details.
- 2. Under bar symbol ( \_ ) may not be to scale.
- 3. Thin MLF  $\triangle$  = Pin 1 identifier.
- 4. Thin MLF is a GREEN RoHS compliant package. Lead finish is NiPdAu. Mold compound is Halogen Free.
- \* MIC5366 offers Auto-Discharge function.

## **Pin Configuration**







4-Pin 1mm × 1mm Thin MLF (MT) Fixed Version

5-Pin Thin SOT23 (D5) Fixed Version

5-Pin SC-70 (C5) Fixed Version

# **Pin Description**

Pin Number	Pin Name Thin MLF-4	Pin Name TSOT23-5	Pin Name SC-70-5	Pin Function
1	VOUT	_	ı	Output Voltage.
1	_	VIN	VIN	Supply Input.
2	GND	GND	GND	Ground
3	EN	EN	EN	Enable Input: Active High. High = ON; Low = OFF. Do not leave floating.
4	VIN	_	_	Supply Input.
4	_	NC	NC	No connect. Not internally connected.
5	_	VOUT	VOUT	Output Voltage.
EP	HS Pad	NA	NA	Exposed Heatsink Pad.

## **Absolute Maximum Ratings**(1)

### 

## Operating Ratings<sup>(2)</sup>

Supply Voltage (V <sub>IN</sub> )	2.5V to 5.5V
Enable Voltage (V <sub>EN</sub> )	0V to V <sub>IN</sub>
Junction Temperature (T <sub>J</sub> )	40°C to +125°C
Junction Thermal Resistance	
1mm × 1mm Thin MLF-4 $(\theta_{JA})$	240°C/W
SC-70-5 (θ <sub>JA</sub> )	256.5°C/W
Thin SOT23-5 (θ μ)	253°C/W

### Electrical Characteristics<sup>(5)</sup>

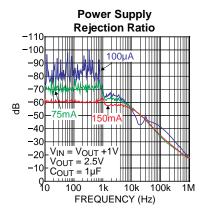
 $V_{IN} = V_{EN} = V_{OUT} + 1V; C_{IN} = C_{OUT} = 1 \mu F; I_{OUT} = 100 \mu A; T_J = 25 ^{\circ}C, \ \textbf{bold} \ values \ indicate -40 ^{\circ}C \ to \ +125 ^{\circ}C, \ unless \ noted.$ 

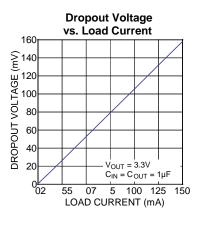
Parameter	Condition	Min.	Тур.	Max.	Units
Output Voltage Acquirecy	Variation from nominal V <sub>OUT</sub>	-2.0		+2.0	%
Output Voltage Accuracy	Variation from nominal V <sub>OUT</sub> ; –40°C to +125°C	-3.0		+3.0	%
Line Regulation	$V_{IN} = V_{OUT} + 1V$ to 5.5V; $I_{OUT} = 100\mu A$	0.3	%		
Load Regulation <sup>(6)</sup>	I <sub>OUT</sub> = 100μA to 150mA		0.3	1	%
	I <sub>OUT</sub> = 50mA; V <sub>OUT</sub> ≥ 2.8V		55	110	mV
Dropout Voltage <sup>(7)</sup>	I <sub>OUT</sub> = 150mA; V <sub>OUT</sub> ≥ 2.8V		155	310	mV
Diopout voltage	$I_{OUT} = 50 \text{mA}$ ; $V_{OUT} < 2.8 \text{V}$		60	135	mV
	I <sub>OUT</sub> = 150mA; V <sub>OUT</sub> < 2.8V		180	380	mV
Ground Pin Current <sup>(8)</sup>	I <sub>OUT</sub> = 0mA		29	39	μΑ
Ground Pin Current in Shutdown	V <sub>EN</sub> ≤ 0.2V		0.05	1	μΑ
Disale Deiretten	$f = up to 1kHz; C_{OUT} = 1\mu F$		80		dB
Ripple Rejection	$f = 1kHz - 10kHz$ ; $C_{OUT} = 1\mu F$		65		dB
Current Limit	V <sub>OUT</sub> = 0V	200	325	550	mA
Output Voltage Noise	$C_{OUT} = 1\mu F$ , 10Hz to 100kHz		200		$\mu V_{RMS}$
Auto-Discharge NFET Resistance	MIC5366 Only; $V_{EN} = 0V$ ; $V_{IN} = 3.6V$ ; $I_{OUT} = -3mA$		30		Ω
Enable Input					
Frankla lanut Valtana	Logic Low			0.2	V
Enable Input Voltage	Logic High	1.2			V
Enable Input Current	V <sub>IL</sub> ≤ 0.2V		0.01	1	μΑ
Enable Input Current	V <sub>IH</sub> ≥ 1.2V		0.01	1	μΑ
Turn-on Time	$C_{OUT} = 1\mu F; I_{OUT} = 150mA$		50	125	μs

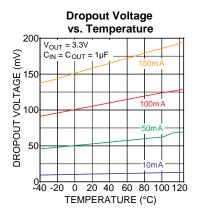
#### Notes:

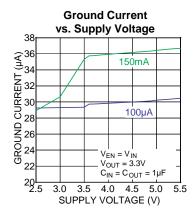
- 1. Exceeding the absolute maximum rating may damage the device.
- 2. The device is not guaranteed to function outside its operating rating.
- The maximum allowable power dissipation of any T<sub>A</sub> (ambient temperature) is P<sub>D(max)</sub> = T<sub>J(max)</sub> T<sub>A</sub>) / θ<sub>JA</sub>. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown.
- 4. Devices are ESD sensitive. Handling precautions recommended. Human body model, 1.5kΩ in series with 100pF.
- 5. Specification for packaged product only.
- 6. Regulation is measured at constant junction temperature using low duty cycle pulse testing, changes in output voltage due to heating effects are covered by the thermal regulation specification.
- 7. Dropout voltage is defined as the input-to-output differential at which the output voltage drops 2% below its nominal value measured at 1V differential. For outputs below 2.5V, dropout voltage is the input-to-output differential with the minimum input voltage 2.5V.
- 8. Ground pin current is the regulator quiescent current. The total current drawn from the supply is the sum of the load current plus the ground pin current.

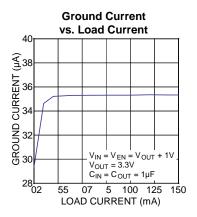
## **Typical Characteristics**

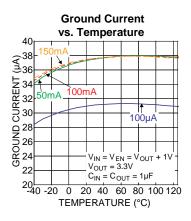


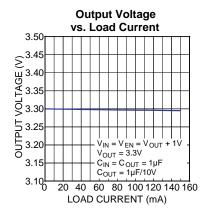


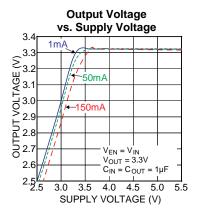


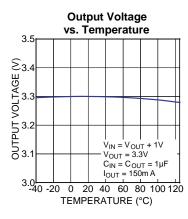




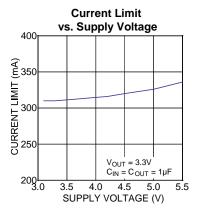


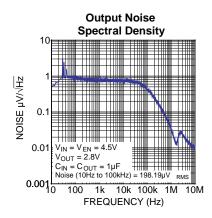




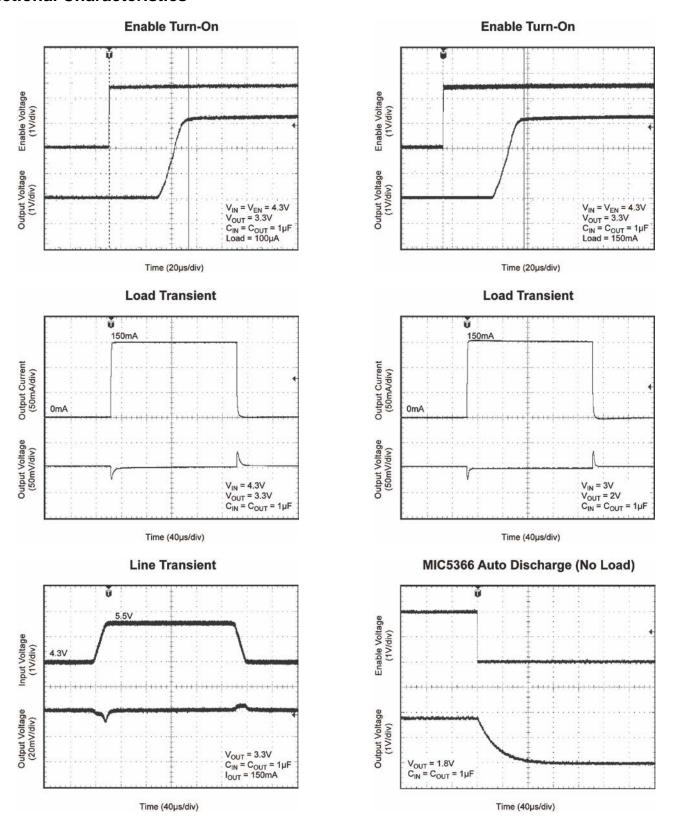


## **Typical Characteristics (Continued)**





### **Functional Characteristics**



### **Application Information**

MIC5365 and MIC5366 are Low noise 150mA LDOs. The MIC5366 includes an auto-discharge circuit that is switched on when the regulator is disabled through the Enable pin. The MIC5365/6 regulator is fully protected from damage due to fault conditions, offering linear current limiting and thermal shutdown.

#### **Input Capacitor**

The MIC5365/6 is a high-performance, high bandwidth device. An input capacitor of 1µF is required from the input to ground to provide stability. Low-ESR ceramic capacitors provide optimal performance at a minimum of space. Additional high-frequency capacitors, such as small-valued NPO dielectric-type capacitors, help filter out high-frequency noise and are good practice in any RF-based circuit. X5R or X7R dielectrics are recommended for the input capacitor. Y5V dielectrics lose most of their capacitance over temperature and are therefore, not recommended.

#### **Output Capacitor**

The MIC5365/6 requires an output capacitor of  $1\mu F$  or greater to maintain stability. The design is optimized for use with low-ESR ceramic chip capacitors. High ESR capacitors are not recommended because they may cause high frequency oscillation. The output capacitor can be increased, but performance has been optimized for a  $1\mu F$  ceramic output capacitor and does not improve significantly with larger capacitance.

X7R/X5R dielectric-type ceramic capacitors recommended because of their temperature performance. X7R-type capacitors change capacitance by 15% over their operating temperature range and are the most stable type of ceramic capacitors. Z5U and Y5V dielectric capacitors change value by as much as 50% and 60%, respectively, over their operating temperature ranges. To use a ceramic chip capacitor with Y5V dielectric, the value must be much higher than an X7R ceramic capacitor to ensure the same minimum capacitance over the equivalent operating temperature range.

#### **No-Load Stability**

Unlike many other voltage regulators, the MIC5365/6 will remain stable and in regulation with no load. This is especially important in CMOS RAM keep-alive applications.

#### Enable/Shutdown

The MIC5365/6 comes with an active-high enable pin that allows the regulator to be disabled. Forcing the enable pin low disables the regulator and sends it into a "zero" off-mode-current state. In this state, current consumed by the regulator goes nearly to zero. Forcing the enable pin high enables the output voltage. The active-high enable pin uses CMOS technology and the enable pin cannot be left floating; a floating enable pin may cause an indeterminate state on the output.

#### **Thermal Considerations**

The MIC5365/6 is designed to provide 150mA of continuous current in a very small package. Maximum ambient operating temperature can be calculated based on the output current and the voltage drop across the part. For example if the input voltage is 3.6V, the output voltage is 2.8V, and the output current = 150mA. The actual power dissipation of the regulator circuit can be determined using the equation:

$$P_D = (V_{IN} - V_{OUT1}) I_{OUT} + V_{IN} I_{GND}$$

Because this device is CMOS and the ground current is typically  $<100\mu\text{A}$  over the load range, the power dissipation contributed by the ground current is <1% and can be ignored for this calculation:

$$P_D = (3.6V - 2.8V) \times 150 \text{mA}$$
  
 $P_D = 0.120 \text{W}$ 

To determine the maximum ambient operating temperature of the package, use the junction-to-ambient thermal resistance of the device and the following basic equation:

$$P_{D(MAX)} = \left(\frac{T_{J(max)} - T_{A}}{\theta_{JA}}\right)$$

 $T_{J(max)}=125^{\circ}C,$  the maximum junction temperature of the die,  $\theta_{JA}$  thermal resistance = 250°C/W for the YMT package, 256.5°C/W for the SC-70-5 package and 235°C/W for the MIC5365/6 in the TSOT23-5 package .

Substituting  $P_D$  for  $P_{D(max)}$  and solving for the ambient operating temperature will give the maximum operating conditions for the regulator circuit. The junction-to-ambient thermal resistance for the minimum footprint is 250°C/W.

The maximum power dissipation must not be exceeded for proper operation.

For example, when operating the MIC5365-2.8YMT at an input voltage of 3.6V and 150mA load with a minimum footprint layout, the maximum ambient operating temperature  $T_A$  can be determined as follows:

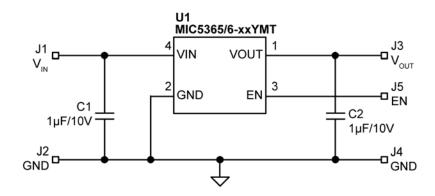
$$0.120W = (125^{\circ}C - T_A)/(250^{\circ}C/W)$$
  
 $T_A = 95^{\circ}C$ 

Therefore the maximum ambient operating temperature of 95°C is allowed in a 1mm x 1mm MLF package. For a full discussion of heat sinking and thermal effects on voltage regulators, refer to the "Regulator Thermals" section of *Micrel's Designing with Low-Dropout Voltage Regulators* handbook. This information can be found on Micrel's website at:

http://www.micrel.com/\_PDF/other/LDOBk\_ds.pdf

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# **Typical Application (YMT)**



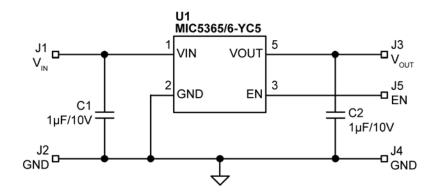
## **Bill of Materials**

Item	Part Number	Manufacturer	Description	Qty.
C1, C2	GRM155R61A105KE15D	Murata <sup>(1)</sup>	Capacitor, 1µF Ceramic, 10V, X5R, Size 0402	2
U1	MIC5365/6-xxYMT	Micrel, Inc. <sup>(2)</sup>	High-Performance Single 150mA LDO	1

#### Notes:

Murata: <a href="https://www.murata.com">www.murata.com</a>.
 Micrel, Inc.: <a href="https://www.micrel.com">www.micrel.com</a>.

# **Typical Application (YC5)**



## **Bill of Materials**

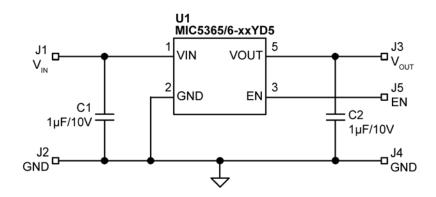
Item	Part Number	Manufacturer	Description	Qty.
C1, C2	C1005X5R1A105K	TDK <sup>(1)</sup>	Capacitor, 1µF Ceramic, 10V, X5R, Size 0402	2
U1	MIC5365/6-xxYC5	Micrel, Inc. <sup>(2)</sup>	High-Performance Single 150mA LDO	1

#### Notes:

1. TDK: www.tdk.com.

2. Micrel, Inc.: www.micrel.com.

# **Typical Application (YD5)**



## **Bill of Materials**

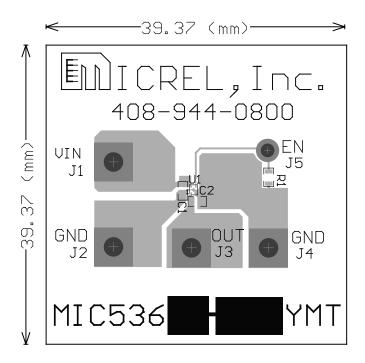
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C1, C2	C1005X5R1A105K	TDK <sup>(1)</sup>	Capacitor, 1µF Ceramic, 10V, X5R, Size 0402	2
U1	MIC5365/6-xxYD5	Micrel, Inc. <sup>(2)</sup>	High-Performance Single 150mA LDO	1

#### Notes:

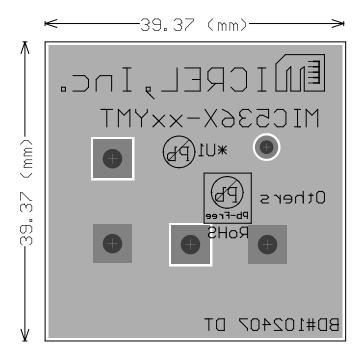
1. TDK: www.tdk.com.

2. Micrel, Inc.: www.micrel.com.

## PCB Layout Recommendations (1mm x 1mm Thin MLF)

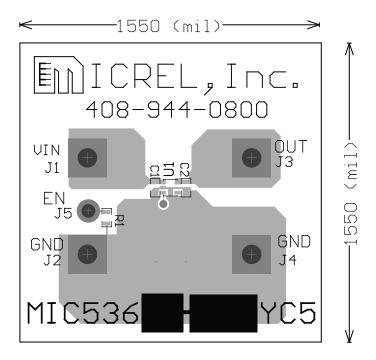


**Top Layer** 

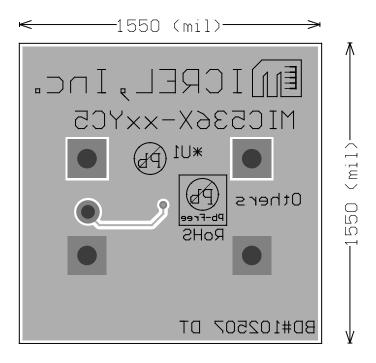


**Bottom Layer** 

## **PCB Layout Recommendations (SC-70-5)**

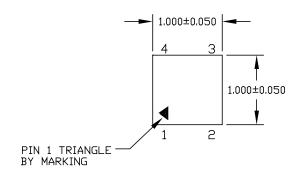


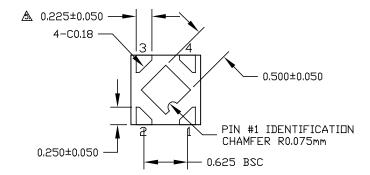
**Top Layer** 



**Bottom Layer** 

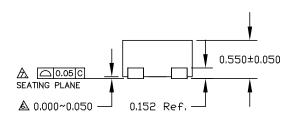
# Package Information<sup>(1)</sup>





TOP VIEW

BOTTOM VIEW



- NOTE:

  1. ALL DIMENSIONS ARE IN MILLIMETERS.
  2. MAX. PACKAGE WARPAGE IS 0.05 mm.
  3. MAXIMUM ALLOWABE BURRS IS 0.076 mm IN ALL DIRECTIONS.
  4. PIN #1 ID ON TOP WILL BE LASE/INK MARKED.
  DIMENSION APPLIES TO METALIZED TERMINAL AND IS MEASURED BETWEEN 0.20 AND 0.25 mm FROM TERMINAL TIP.

  APPLIED ONLY FOR TERMINALS.
- APPLIED ONLY FOR TERMINALS.

  APPLIED FOR EXPOSED PAD AND TERMINALS.

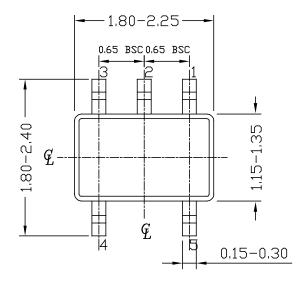
SIDE VIEW

4-Pin 1mm × 1mm Thin MLF (MT)

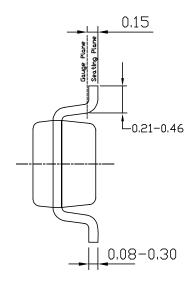
#### Note:

Package information is correct as of the publication date. For updates and most current information, go to <a href="www.micrel.com">www.micrel.com</a>.

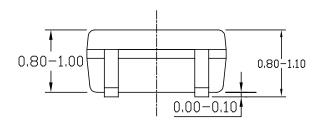
# Package Information<sup>(1)</sup> (Continued)



TOP VIEW



END VIEW



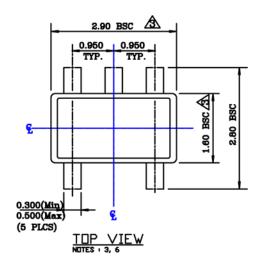
SIDE VIEW

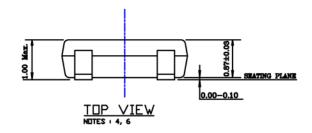
#### NOTE

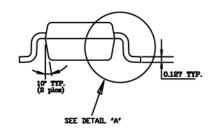
- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONS ARE INCLUSIVE OF PLATING.
- 3. DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH & METAL BURR.

5-Pin SC-70 (C5)

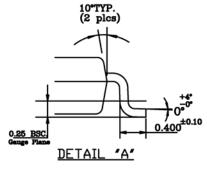
# Package Information<sup>(1)</sup> (Continued)

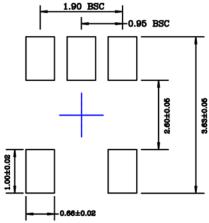












RECOMMENDED LAND PATTERN

### NOTE:

- Dimensions and tolerances are as per ANSI Y14.5M, 1994.
- Die is facing up for mold. Die is facing down for trim/form, ie. reverse trim/form.
- ⚠ Dimensions are exclusive of mold flash and gate burr.
- 4. The footlength measuring is based on the gauge plane method.
- 5. All specification comply to Jedec Spec M0193 Issue C.
- 6. All dimensions are in millimeters.

5-Pin Thin SOT23 (D5)

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TCR3DF285,LM(CT TCR3DF31,LM(CT TCR3DF45,LM(CT TLF4949EJ L9708 L970813TR 030014BB 059985X NCP121AMX173TCG
NCP4687DH15T1G NCV8703MX30TCG 701326R 702087BB 755078E TCR2EN28,LF(S LM1117DT-1.8/NO LT1086CM#TRPBF
AZ1085S2-1.5TRE1 MAX15101EWL+T NCV8170AXV250T2G SCD337BTG TCR3DF27,LM(CT TCR3DF19,LM(CT
TCR3DF125,LM(CT TCR2EN18,LF(S MAX15103EWL+T TS2937CZ-5.0 C0 MAX8878EUK30-T MAX663CPA NCV4269CPD50R2G
NCV8716MT30TBG AZ1117IH-1.2TRG1 MP2013GQ-P AP2112R5A-3.3TRG1 AP7315-25W5-7