

32-bit Kinetis L Series MCUs

# Kinetis KL2x MCU Family

### Ultra-low-power MCUs with USB OTG

### Overview

The Kinetis KL2x family of ARM<sup>®</sup> Cortex<sup>®</sup>-M0+ MCUs combine ultra-low-power performance with a rich suite of analog, communication, timing and control peripherals, including a USB 2.0 On-the-Go controller. Family members start from 32 KB of flash in a small 3.5 x 3.5 mm2 XFBGA package, extending up to 256 KB in a 121 MAPBGA package. The KL2x MCU family is compatible with the ARM Cortex-M4 based Kinetis K20 MCU family, offering a migration path to higher performance and feature integration.

# Kinetis KL2x MCU Family Block Diagram



Features

#### Ultra-Low-Power

- Next-generation 32-bit ARM Cortex-M0+ core. Two times more CoreMark/mA than the closest 8/16-bit architecture. Single-cycle fast I/O access port facilitates bit banging and software protocol emulation, maintaining an 8-bit 'look and feel'.
- Multiple flexible low-power modes, including new compute mode which reduces dynamic power by placing peripherals in an asynchronous stop mode
- LPUART, SPI, I<sup>2</sup>C, Flex IO, ADC, DAC, LP timer and DMA support low-power mode operation without waking up the core

#### Memory

- Up to 256 KB flash with 64 byte flash cache, up to 32 KB RAM
- 16 KB ROM with integrated bootloader
- Security circuitry to prevent unauthorized access to RAM and flash contents



### **Target Applications**

- Battery-operated applications
- Consumer applications
- Low-power applications
- USB peripherals



### Features cont.

#### Performance

- ARM Cortex-M0+ core, 48 MHz core frequency over full voltage and temperature range (-40 °C +105 °C)
- Bit manipulation engine for improved bit handling of peripheral modules
- Thumb instruction set combines high code density with 32-bit performance
- Up to 4-channel DMA for peripheral and memory servicing with reduced CPU loading and faster system throughput
- Independent-clocked COP guards against clock skew or code runaway for fail-safe applications

#### Mixed Signal

- Up to 16-bit ADC with configurable resolution, sample time and conversion speed/power. Integrated temperature sensor. Single or differential input mode operation in order to achieve improved noise rejection
- High-speed comparator with internal 6-bit DAC
- 12-bit DAC with DMA support
- 1.2 V high-accuracy internal voltage reference

#### **Timing and Control**

- One 6-channel and two 2-channel,16-bit low-power timer PWM modules with DMA support
- 2-channel 32-bit periodic interrupt timer provides time base for RTOS task schedule or trigger source for ADC conversion
- Low-power timer allows operation in all power modes except for VLLS0
- Real-time clock

#### HMI

- Capacitive touch sense interface supports up to 16 external electrodes and DMA data transfer
- GPIO with pin interrupt support, DMA request capability and other pin control options

#### **Connectivity and Communications**

- USB 2.0 On-The-Go (full-speed). Integrated USB low-voltage regulator supplies up to 120 mA off chip at 3.3 volts to power external components from 5-volt input
- Two I<sup>2</sup>C with DMA support, up to 1Mb/s and compatible with SMBus V2 features
- Three UART with up to two LPUART, and DMA support
- Two SPI with DMA support

- I<sup>2</sup>S module for audio applications
- Flex IO with capability of emulating multiple serial interface, such as IrDA, UART, SPI, I<sup>2</sup>C, etc.

#### Software and Tools

- Freescale Freedom Development Platforms and Tower System modules
- Kinetis Software Development Kit (SDK)
- Integrated development environment (IDE)
  - Kinetis Design Studio IDE
  - CodeWarrior for Microcontrollers V10.x (Eclipse) IDE with Processor Expert software modeling tool
  - IAR Embedded Workbench, Keil MDK, Atollic
- Runtime software and RTOS
  - MQX<sup>™</sup> Lite, FreeRTOS, CodeSourcery G++ (GNU)
- Full ARM ecosystem support

## Kinetis KL2x MCU Family Options

	Part Number		Mem	nory	Features												√ Package										
Sub- Family		CPU (MHz)														~				FM	FT	DA	LH	LK	LL	MP	MC
			Flash (KB)	SRAM (KB)	DMA	Low-Power UART	UART	ISO7 816-3	SPI	I²C	TSI	I²S	Flex IO	RTC	12-bit DAC	16-bit ADC w DP Ch.	12-bit ADC	Total I/Os	Other	32 QFN (5 x 5, 0.5 mm)	48 QFN (7 x 7, 0.5 mm)	36X FBGA (3.5 x 3.5, 0.5 mm)	64 LQFP (10 x 10, 0.5 mm)	80 LQFP (12 x 12, 0.5 mm)	100 LQFP (14 x 14, 0.5 mm)	64 MAPBGA (5 x 5, 0.5 mm)	121 MAPBGA (8 x 8, 0.65 mm)
KL24	MKL24Z32xxx4	48 MHz	32	4	$\checkmark$	1	2		2	2				1			1	23~66	USB 2.0 FS OTG/Host/Device	$\checkmark$	√	$\checkmark$	$\checkmark$				
	MKL24Z64xxx4	48 MHz	64	8	$\checkmark$	1	2		2	2				1			√	23~66	USB 2.0 FS OTG/Host/Device	$\checkmark$	√	$\checkmark$	$\checkmark$				
KL25	MKL25Z32xxx4	48 MHz	32	4	$\checkmark$	1	2		2	2	$\checkmark$			1	$\checkmark$	1		23~66	USB 2.0 FS OTG/Host/Device	$\checkmark$	√	$\checkmark$	$\checkmark$				
	MKL25Z64xxx4	48 MHz	64	8	$\checkmark$	1	2		2	2	$\checkmark$			$\checkmark$	$\checkmark$	$\checkmark$		23~66	USB 2.0 FS OTG/Host/Device	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$				
	MKL25Z128xxx4	48 MHz	128	16	$\checkmark$	1	2		2	2	$\checkmark$			$\checkmark$	$\checkmark$	$\checkmark$		23~66	USB 2.0 FS OTG/Host/Device	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$				
KL26	MKL26Z32xxx4	48 MHz	32	4	$\checkmark$	1	2		2	2	$\checkmark$	$\checkmark$		$\checkmark$	$\checkmark$	$\checkmark$		23~50	USB 2.0 FS OTG/Host/Device	$\checkmark$	$\checkmark$	$\checkmark$					
	MKL26Z64xxx4	48 MHz	64	8	$\checkmark$	1	2		2	2	$\checkmark$	$\checkmark$		$\checkmark$	$\checkmark$	$\checkmark$		23~50	USB 2.0 FS OTG/Host/Device	$\checkmark$	$\checkmark$	$\checkmark$					
	MKL26Z128xxx4	48 MHz	128	16	$\checkmark$	1	2		2	2	$\checkmark$	$\checkmark$		$\checkmark$	$\checkmark$	$\checkmark$		23~80	USB 2.0 FS OTG/Host/Device	$\checkmark$	$\checkmark$	$\checkmark$		$\checkmark$		$\checkmark$	√
	MKL26Z256xxx4	48 MHz	256	32	$\checkmark$	1	2		2	2	$\checkmark$	$\checkmark$		$\checkmark$	$\checkmark$	$\checkmark$		50~80	USB 2.0 FS OTG/Host/Device			$\checkmark$		$\checkmark$		$\checkmark$	√
KL27	MKL27Z128xxx4	48 MHz	128	32	$\checkmark$	2	1	1	2x 16b	2		$\checkmark$	$\checkmark$	V	$\checkmark$	$\checkmark$		23~50	USB 2.0 FS Device, with crystal-less USB	$\checkmark$	$\checkmark$	$\checkmark$					V
	MKL27Z256xxx4	48 MHz	256	32	$\checkmark$	2	1	1	2x 16b	2		$\checkmark$	$\checkmark$	V	$\checkmark$	$\checkmark$		23~50	USB 2.0 FS Device, with crystal-less USB	$\checkmark$	$\checkmark$	$\checkmark$					V
	MKL27Z32xxx4	48 MHz	32	8	$\checkmark$	2	1	1	2x 16b	2			$\checkmark$	V		V		23~50	Crystal-less USB, Device Only, Low Power Keep Alive	*	*	$\checkmark$	$\checkmark$			*	
	MKL27Z64xxx4	48 MHz	64	16	1	2	1	1	2x 16b	2			V	V		V		23~50	Crystal-less USB, Device Only, Low Power Keep Alive	*	*	$\checkmark$	$\checkmark$			*	

\* This package is included in a Package Your Way program for Kinetis MCUs. Please visit Freescale.com/KPYW for more detail.



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