





April 2014



- Pletronics' SM77D Series is a quartz crystal controlled precision square wave generator with a CMOS output.
- The SM77D series will directly interface TTL devices also.
- The package is designed for high density surface mount designs.
- This is a low cost mass produced oscillator.
- · Tape and Reel or cut tape packaging is available.

- 70 to 135 MHz
- 5 x 7 mm LCC Ceramic Package
- Enable/Disable Function
- Disable function includes low standby power mode
- 3rd Overtone Crystals used
- Improved circuit to minimize oscillator issues such as multi-mode output signal.
- · Low Jitter
- Capable of driving up to 30pF capacitive loads

Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2011/65/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.17 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020C

Second Level Interconnect code: e4

Absolute Maximum Ratings:

Parameter	Unit
V _{CC} Supply Voltage	-0.5V to +7.0V
Vi Input Voltage	-0.5V to V _{CC} + 0.5V
Vo Output Voltage	-0.5V to V _{CC} + 0.5V

Thermal Characteristics

The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.



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Part Number:

SM77	45	D	E	W	-125.0M	-XX	
							Packaging code or blank T250 = 250 per Tape and Reel T500 = 500 per Tape and Reel T1K = 1,000 per Tape and Reel
							Frequency in MHz
							Supply Voltage V _{cc} W = 2.5V <u>+</u> 10%
							Optional Enhanced OTR Blank = Temp. range -10 to +70°C C = Temp. range -20 to +70°C E = Temp. range -40 to +85°C
							Series Model
							Frequency Stability 45 = ± 50 ppm 44 = ± 25 ppm 20 = ± 20 ppm
							Series Model

Part Marking and Marking Legend:

PLE SM77 FF.FFF M • YMDxx PLE SM77
FF.FFF M
• YYWWxx

7xYWWxx FF.FFF M • PLE xxx

PLE = Pletronics

FF.FFF M = Frequency in MHz

YYWW or YWW or YMD = Date of Manufacture (year and week, or year-month-day)

All other marking is internal factory codes

Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD

	-																	
Code	0	1	2	3	4	Code	Α	В	С	D	Е	F	G	Н	J	K	L	M
Year	2010	2011	2012	2013	2014	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC
	Code		1	2	3	4	5	6	7	8	9	Α	В	С	D	Е	F	G
	Day		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
(Code		Н	J	K	L	M	N	Р	R	Т	U	٧	W	Х	Υ	Z	
	Day		17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

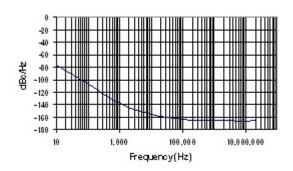


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Electrical Specification for 2.50V ±10% over the specified temperature range

Item	Min	Max	Unit	Condition	
Frequency Range	70	135	MHz		
Frequency Accuracy "45"	-50 +50		ppm	For all supply voltages, load changes, aging for 1	
"44"	-25	+25		year, shock, vibration and temperatures	
"20"	-20	+20			
Output Waveform		CMOS	3		
Output High Level	90	-	%	of V _{cc} (See load circuit)	
Output Low Level	1	10	%	of V _{cc} (See load circuit)	
Output Symmetry	45	55	%	at 50% point of V _{cc} (See load circuit)	
Jitter	1	0.6	pS RMS	12 KHz to 20 MHz from the output frequency	
	1	2.5	pS RMS	10 Hz to 1 MHz from the output frequency	
Enable/Disable Internal Pull-up	50	-	Kohm	to V _{cc}	
V disable	1	30	%	of V _{cc} applied to pad 1	
V enable	70	-	%	of V _{cc} applied to pad 1	
Output leakage V _{OUT} = V _{CC}	-10	+10	uA	Pad 1 low, device disabled	
V _{OUT} = 0V	-10	+10	uA		
Standby Current I _{cc}	1	3	uA		
Enable time	ı	2.0	mS	Time for output to reach a logic state	
Disable time	1	100	nS	Time for output to reach a high Z state	
Start up time	1	10	mS	Time for output to reach specified frequency	
Operating Temperature Range	-10	+70	°C	Standard Temperature Range	
	-20	+70	°C	Extended Temperature Range "C" Option	
	-40	+85	°C	Extended Temperature Range "E" Option	
Storage Temperature Range	-55	+125	°C		

Typical phase-noise characteristics at 106.25MHz





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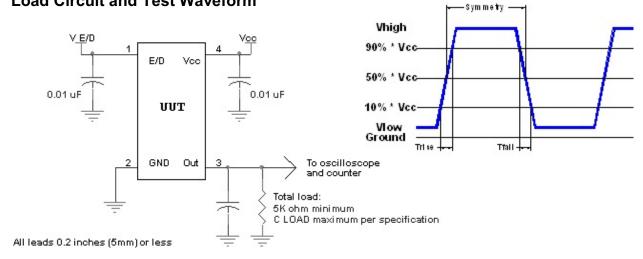
Item	Min	Тур	Max	Unit	Condition		
V _{OUT} High (V _{OH})	1.75	1.95	-	V	V _{CC} = 2.25V, I _{OH} = +8 mA		
V _{OUT} Low (V _{OL})	-	0.3	0.4	V	V_{CC} = 2.25V, I_{OL} = -8 mA		
Output T_{RISE} and T_{FALL}	1	1.0	3.0	nS	>110 MHz	$C_{LOAD} = 15 \text{ pF}$	
	-	1.0	3.0	nS	>80 MHz and <u><</u> 110 MHz	10% to 90% of V _{cc} See Load Circuit	
	1	2.0	4.0	nS	<u><</u> 80 MHz		
	1	2.5	4.0	nS	>110 MHz	$C_{LOAD} = 30 \text{ pF}$	
	1	2.5	4.0	nS	>80 MHz and <110 MHz	10% to 90% of V _{cc} See Load Circuit	
	1	3.5	6.0	nS	<u><</u> 80 MHz		
V _{cc} Supply Current	1	25	60	mA	>110 MHz	C _{LOAD} = 15 pF	
(I _{cc})	1	20	50	mA	>80 MHz and <110 MHz		
	-	15	40	mA	<u><</u> 80 MHz		
	-	38	70	mA	>110 MHz	C _{LOAD} = 30 pF	
	-	31	60	mA	>80 MHz and <110 MHz		
	-	20	45	mA	<80 MHz		

Specifications with Pad 1 E/D open circuit



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Load Circuit and Test Waveform



Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

ESD Rating

Model	Minimum Voltage	Conditions
Human Body Model	1500	MIL-STD-883 Method 3115
Charged Device Model	1000	JESD 22-C101

Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

 Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

2nd LvL Interconnect

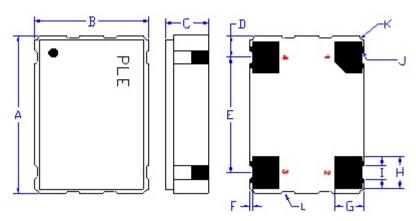
Category=e4

Max Safe Temp=260C for 10s 2X Max



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Mechanical:



The detents marked "L" on the package ends are optional

Not to Scale

¹ Typical dimensions

	Inches	mm
Α	0.276 <u>+</u> 0.006	7.00 <u>+</u> 0.15
В	0.197 <u>+</u> 0.006	5.00 <u>+</u> 0.15
С	0.068 <u>+</u> 0.018	1.73 <u>+</u> 0.44
D¹	0.038	0.96
E¹	0.200	5.08
F ¹	0.004	0.10
G¹	0.050	1.27
H¹	0.055	1.40
l ¹	0.024	0.60
J ¹	0.004	0.10R
K¹	0.008	0.020R

Contacts (pads):

Gold 11.8 to 39.4 µinches (0.3 to 1.0 µm) over Nickel 50 to 350 µinches (1.27 to 8.89 µm)

Pad	Function	Note
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is logic low the output will be inhibited (high impedance state.) Recommend connecting this pad to $V_{\rm cc}$ if the oscillator is to be always on.
2	Ground (GND)	
3	Output	
4	Supply Voltage (V _{cc})	Recommend connecting appropriate power supply bypass capacitors as close as possible.

Layout and application information



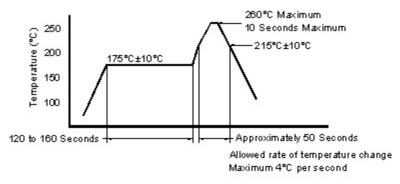
For Optimum Jitter Performance, Pletronics recommends:

- · a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.



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Reflow Cycle (typical for lead free processing)



The part may be reflowed 3 times without degradation.

Tape and Reel: available for quantities of 250 to 1000 per reel (< 250 = cut tape)

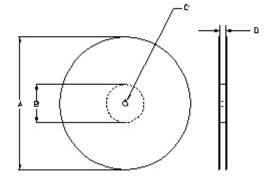
	Constant Dimensions Table 1											
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max				
8mm		1.0			2.0							
12mm	1.5	1.5	1.75	4.0	<u>+</u> 0.05			2.4				
16mm	+0.1 -0.0	1.5	<u>+</u> 0.1	<u>+</u> 0.1	2.0	0.6	0.6	0.1				
24mm		1.5			<u>+</u> 0.1							

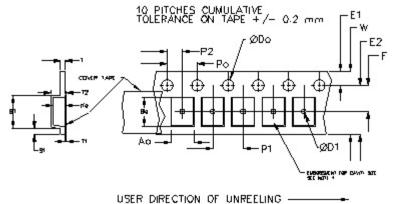
	Variable Dimensions Table 2											
Tape B1 E2 Min F P1 T2 W Ao, Bo & Ko Size Max Max Ko												
16 mm	12.1	14.25	7.5 <u>+</u> 0.1	8.0 <u>+</u> 0.1	8.0	16.3	Note 1					

Note 1: Embossed cavity to conform to EIA-481-B

Dimensions in mm

Not to scale





		REE	REEL DIMENSIONS							
Α	inches	7.0	10.0	13.0						
	mm	177.8	254.0	330.2						
В	inches	2.50	4.00	3.75						
	mm	63.5	101.6	95.3	Tape Width					
С	mm	13	13.0 +0.5 / -0.2							
D	mm	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.0					

Reel dimensions may vary from the above

www.pletronics.com 425-776-1880



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