## SM77D Series 2.5 V CMOS Clock Oscillators

April 2014

- Pletronics' SM77D Series is a quartz crystal controlled precision square wave generator with a CMOS output.
- The SM77D series will directly interface TTL devices also.
- The package is designed for high density surface mount designs.
- This is a low cost mass produced oscillator.
- Tape and Reel or cut tape packaging is available.
- 70 to 135 MHz
- $5 \times 7 \mathrm{~mm}$ LCC Ceramic Package
- Enable/Disable Function
- Disable function includes low standby power mode
- $3^{\text {rd }}$ Overtone Crystals used
- Improved circuit to minimize oscillator issues such as multi-mode output signal.
- Low Jitter
- Capable of driving up to 30 pF capacitive loads

Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2011/65/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following:
Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's
Weight of the Device: 0.17 grams
Moisture Sensitivity Level: 1 As defined in J-STD-020C
Second Level Interconnect code: e4

Absolute Maximum Ratings:

| Parameter | Unit |
| :--- | :--- |
| $\mathrm{V}_{\mathrm{cc}}$ Supply Voltage | -0.5 V to +7.0 V |
| Vi Input Voltage | -0.5 V to $\mathrm{V}_{\mathrm{CC}}+0.5 \mathrm{~V}$ |
| Vo Output Voltage | -0.5 V to $\mathrm{V}_{\mathrm{CC}}+0.5 \mathrm{~V}$ |

## Thermal Characteristics

The maximum die or junction temperature is $155^{\circ} \mathrm{C}$
The thermal resistance junction to board is 30 to $50^{\circ} \mathrm{C} / \mathrm{W}$ att depending on the solder pads, ground plane and construction of the PCB.

PLETRONICS
Part Number:


## Part Marking and Marking Legend:

| PLE SM 77 |
| :--- |
| FF.FFF M |
| - YMDXX |


| PLE SM 77 |
| :--- |
| FF.FFF M |
| - YYWW $x x$ |


| 7xYWW $x x$ |
| :--- |
| FF.FFF M |
| - PLE $x x x$ |

PLE = Pletronics
FF.FFF M = Frequency in MHz
YYWW or YWW or YMD = Date of Manufacture (year and week, or year-month-day)
All other marking is internal factory codes
Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD

| Code | $\mathbf{0}$ | $\mathbf{1}$ | $\mathbf{2}$ | $\mathbf{3}$ | $\mathbf{4}$ | Code | A | B | C | $\mathbf{D}$ | E | F | $\mathbf{G}$ | $\mathbf{H}$ | $\mathbf{J}$ | K | $\mathbf{L}$ | $\mathbf{M}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Year | 2010 | 2011 | 2012 | 2013 | 2014 | Month | JAN | FEB | MAR | APR | MAY | JUN | JUL | AUG | SEP | OCT | NOV | DEC |


| Code | $\mathbf{1}$ | $\mathbf{2}$ | $\mathbf{3}$ | $\mathbf{4}$ | $\mathbf{5}$ | $\mathbf{6}$ | $\mathbf{7}$ | $\mathbf{8}$ | $\mathbf{9}$ | $\mathbf{A}$ | $\mathbf{B}$ | $\mathbf{C}$ | $\mathbf{D}$ | $\mathbf{E}$ | $\mathbf{F}$ | $\mathbf{G}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Day | $\mathbf{1}$ | 2 | 3 | 4 | 5 | 6 | 7 | 8 | $\mathbf{9}$ | 10 | 11 | 12 | 13 | 14 | 15 | 16 |
| Code | $\mathbf{H}$ | $\mathbf{J}$ | $\mathbf{K}$ | $\mathbf{L}$ | $\mathbf{M}$ | $\mathbf{N}$ | $\mathbf{P}$ | $\mathbf{R}$ | $\mathbf{T}$ | $\mathbf{U}$ | $\mathbf{V}$ | $\mathbf{W}$ | $\mathbf{X}$ | $\mathbf{Y}$ | $\mathbf{Z}$ |  |
| Day | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 | 29 | 30 | 31 |  |

# SM77D Series 2.5 V CMOS Clock Oscillators 

PLETRONICS
April 2014
Electrical Specification for $2.50 \mathrm{~V} \mathbf{+ 1 0 \%}$ over the specified temperature range

| Item | Min | Max | Unit | Condition |
| :---: | :---: | :---: | :---: | :---: |
| Frequency Range | 70 | 135 | MHz |  |
| Frequency Accuracy " 45 " | -50 | +50 | ppm | For all supply voltages, load changes, aging for 1 year, shock, vibration and temperatures |
| "44" | -25 | +25 |  |  |
| "20" | -20 | +20 |  |  |
| Output Waveform | CMOS |  |  |  |
| Output High Level | 90 | - | \% | of $\mathrm{V}_{\mathrm{cc}} \quad$ (See load circuit) |
| Output Low Level | - | 10 | \% | of $\mathrm{V}_{\mathrm{cc}} \quad$ (See load circuit) |
| Output Symmetry | 45 | 55 | \% | at $50 \%$ point of $V_{c c}$ (See load circuit) |
| Jitter | - | 0.6 | pS RMS | 12 KHz to 20 MHz from the output frequency |
|  | - | 2.5 | pS RMS | 10 Hz to 1 MHz from the output frequency |
| Enable/Disable Internal Pull-up | 50 | - | Kohm | to $\mathrm{V}_{\mathrm{cc}}$ |
| $\checkmark$ disable | - | 30 | \% | of $\mathrm{V}_{\mathrm{cc}}$ applied to pad 1 |
| $\checkmark$ enable | 70 | - | \% | of $\mathrm{V}_{\mathrm{cc}}$ applied to pad 1 |
| Output leakage $\quad \mathrm{V}_{\text {OUT }}=\mathrm{V}_{\mathrm{CC}}$ | -10 | +10 | uA | Pad 1 low, device disabled |
| $\mathrm{V}_{\text {OUT }}=0 \mathrm{~V}$ | -10 | +10 | uA |  |
| Standby Current $\mathrm{I}_{\mathrm{cc}}$ | - | 3 | uA |  |
| Enable time | - | 2.0 | mS | Time for output to reach a logic state |
| Disable time | - | 100 | nS | Time for output to reach a high Z state |
| Start up time | - | 10 | mS | Time for output to reach specified frequency |
| Operating Temperature Range | -10 | +70 | ${ }^{\circ} \mathrm{C}$ | Standard Temperature Range |
|  | -20 | +70 | ${ }^{\circ} \mathrm{C}$ | Extended Temperature Range "C" Option |
|  | -40 | +85 | ${ }^{\circ} \mathrm{C}$ | Extended Temperature Range "E" Option |
| Storage Temperature Range | -55 | +125 | ${ }^{\circ} \mathrm{C}$ |  |

Typical phase-noise characteristics at 106.25 MHz


## SM77D Series 2.5 V CMOS Clock Oscillators

PLETRONICS
Electrical Specification for $\mathbf{2 . 5 0 V} \mathbf{\pm 1 0 \%}$ over the specified temperature range

| Item | Min | Typ | Max | Unit | Condition |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\text {OUT }} \operatorname{High}\left(\mathrm{V}_{\text {OH }}\right)$ | 1.75 | 1.95 | - | V | $\mathrm{V}_{\mathrm{CC}}=2.25 \mathrm{~V}, \mathrm{I}_{\mathrm{OH}}=+8 \mathrm{~mA}$ |  |
| $\mathrm{V}_{\text {OUT }}$ Low ( $\mathrm{V}_{\text {OL }}$ ) | - | 0.3 | 0.4 | V | $\mathrm{V}_{\mathrm{CC}}=2.25 \mathrm{~V}, \mathrm{I}_{\mathrm{OL}}=-8 \mathrm{~mA}$ |  |
| Output $\mathrm{T}_{\text {RISE }}$ and $\mathrm{T}_{\text {FALL }}$ | - | 1.0 | 3.0 | nS | $>110 \mathrm{MHz}$ | $\begin{aligned} & \mathrm{C}_{\text {LOAD }}=15 \mathrm{pF} \\ & 10 \% \text { to } 90 \% \text { of } \mathrm{V}_{\mathrm{cc}} \\ & \text { See Load Circuit } \end{aligned}$ |
|  | - | 1.0 | 3.0 | nS | $>80 \mathrm{MHz}$ and $\leq 110 \mathrm{MHz}$ |  |
|  | - | 2.0 | 4.0 | nS | $\leq 80 \mathrm{MHz}$ |  |
|  | - | 2.5 | 4.0 | nS | $>110 \mathrm{MHz}$ | $\begin{aligned} & C_{\text {LOAD }}=30 \mathrm{pF} \\ & 10 \% \text { to } 90 \% \text { of } V_{c \mathrm{c}} \end{aligned}$See Load Circuit |
|  | - | 2.5 | 4.0 | nS | $>80 \mathrm{MHz}$ and $\leq 110 \mathrm{MHz}$ |  |
|  | - | 3.5 | 6.0 | nS | $\leq 80 \mathrm{MHz}$ |  |
| $\mathrm{V}_{\mathrm{cc}}$ Supply Current ( $\mathrm{I}_{\mathrm{cc}}$ ) | - | 25 | 60 | mA | $>110 \mathrm{MHz}$ | $C_{\text {LOAD }}=15 \mathrm{pF}$ |
|  | - | 20 | 50 | mA | $>80 \mathrm{MHz}$ and $\leq 110 \mathrm{MHz}$ |  |
|  | - | 15 | 40 | mA | $\leq 80 \mathrm{MHz}$ |  |
|  | - | 38 | 70 | mA | $>110 \mathrm{MHz}$ | $\mathrm{C}_{\text {LOAD }}=30 \mathrm{pF}$ |
|  | - | 31 | 60 | mA | $>80 \mathrm{MHz}$ and $\leq 110 \mathrm{MHz}$ |  |
|  | - | 20 | 45 | mA | $\leq 80 \mathrm{MHz}$ |  |

Specifications with Pad 1 E/D open circuit

## Load Circuit and Test Waveform



All leads 0.2 inches ( 5 mm ) or less


Reliability: Environmental Compliance

| Parameter | Condition |
| :--- | :--- |
| Mechanical Shock | MIL-STD-883 Method 2002, Condition B |
| Vibration | MIL-STD-883 Method 2007, Condition A |
| Solderability | MIL-STD-883 Method 2003 |
| Thermal Shock | MIL-STD-883 Method 1011, Condition A |

## ESD Rating

| Model | Minimum Voltage | Conditions |
| :--- | :--- | :--- |
| Human Body Model | 1500 | MIL-STD-883 Method 3115 |
| Charged Device Model | 1000 | JESD 22-C101 |

## Package Labeling

Label is $1^{\prime \prime} \times 2.6^{\prime \prime}(25.4 \mathrm{~mm} \times 66.7 \mathrm{~mm})$
Font is Courier New
Bar code is 39-Full ASCII

Label is $1^{\prime \prime} \times 2.6$ " $(25.4 \mathrm{~mm} \times 66.7 \mathrm{~mm})$
Font is Arial

| P/N: \|||||||||||||||||||||||||||||||||||||| ${ }^{\text {aple }}$ |  |
| :---: | :---: |
|  |  |
| Qty: \|||||||||||||||||||||| |  |

## RoHS Compliant <br> 2nd LvL Interconnect <br> Category=e4

Max Safe Temp=260C for 10s 2X Max

## Mechanical:



The detents marked " $L$ " on the package ends are optional

Not to Scale
${ }^{1}$ Typical dimensions
Contacts (pads) :
Gold 11.8 to $39.4 \mu$ inches ( 0.3 to $1.0 \mu \mathrm{~m}$ ) over Nickel 50 to $350 \mu$ inches ( 1.27 to $8.89 \mu \mathrm{~m}$ )

| Pad | Function | Note |
| :---: | :--- | :--- |
| 1 | Output <br> Enable/Disable | When this pad is not connected the oscillator shall operate. <br> When this pad is logic low the output will be inhibited (high impedance state.) <br> Recommend connecting this pad to $\mathrm{V}_{\mathrm{cc}}$ if the oscillator is to be always on. |
| 2 | Ground (GND) |  |
| 3 | Output |  |
| 4 | Supply Voltage <br> $\left(\mathrm{V}_{\mathrm{cc}}\right)$ | Recommend connecting appropriate power supply bypass capacitors as close as <br> possible. |

## Layout and application information

For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.

April 2014

## Reflow Cycle (typical for lead free processing)



The part may be reflowed 3 times without degradation.

Tape and Reel: available for quantities of $\mathbf{2 5 0}$ to $\mathbf{1 0 0 0}$ per reel (< $\mathbf{2 5 0}=$ cut tape)

| Constant Dimensions Table 1 |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Tape Size | D0 | $\begin{gathered} \text { D1 } \\ \text { Min } \end{gathered}$ | E1 | P0 | P2 | $\begin{aligned} & \text { S1 } \\ & \text { Min } \end{aligned}$ | $\begin{gathered} \mathrm{T} \\ \mathrm{Max} \end{gathered}$ | T1 <br> Max |
| 8 mm |  | 1.0 |  |  | $\begin{gathered} 2.0 \\ +0.05 \end{gathered}$ |  |  |  |
| 12 mm | 1.5 | 1.5 | 1.75 | 4.0 |  |  |  |  |
| 16mm | $\begin{aligned} & +0.1 \\ & -0.0 \end{aligned}$ | 1.5 | $\pm 0.1$ | $\pm 0.1$ | $\begin{gathered} 2.0 \\ \pm 0.1 \end{gathered}$ |  |  |  |
| 24 mm |  | 1.5 |  |  |  |  |  |  |


| Variable Dimensions Table 2 |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Tape <br> Size | B1 <br> Max | E2 Min | F | P1 | T2 <br> Max | W <br> Max |  <br> Ko |
| 16 mm | 12.1 | 14.25 | $7.5 \pm 0.1$ | $8.0 \pm 0.1$ | 8.0 | 16.3 | Note 1 |

Note 1: Embossed cavity to conform to EIA-481-B Dimensions in mm Not to scale



Reel dimensions may vary from the above

USER DIRECTION DF UNREELING $\qquad$

## IMPORTANT NOTICE

Pletronics Incorporated (PLE) reserves the right to make corrections, improvements, modifications and other changes to this product at anytime. PLE reserves the right to discontinue any product or service without notice. Customers are responsible for obtaining the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to PLE's terms and conditions of sale supplied at the time of order acknowledgment.

PLE warrants performance of this product to the specifications applicable at the time of sale in accordance with PLE's limited warranty. Testing and other quality control techniques are used to the extent PLE deems necessary to support this warranty. Except where mandated by specific contractual documents, testing of all parameters of each product is not necessarily performed.

PLE assumes no liability for application assistance or customer product design. Customers are responsible for their products and applications using PLE components. To minimize the risks associated with the customer products and applications, customers should provide adequate design and operating safeguards.

PLE products are not designed, intended, authorized or warranted to be suitable for use in life support applications, devices or systems or other critical applications that may involve potential risks of death, personal injury or severe property or environmental damage. Inclusion of PLE products in such applications is understood to be fully at the risk of the customer. Use of PLE products in such applications requires the written approval of an appropriate PLE officer. Questions concerning potential risk applications should be directed to PLE.

PLE does not warrant or represent that any license, either express or implied, is granted under any PLE patent right, copyright, artwork or other intellectual property right relating to any combination, machine or process which PLE product or services are used. Information published by PLE regarding third-party products or services does not constitute a license from PLE to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from PLE under the patents or other intellectual property of PLE.

Reproduction of information in PLE data sheets or web site is permissible only if the reproduction is without alteration and is accompanied by associated warranties, conditions, limitations and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. PLE is not responsible or liable for such altered documents.

Resale of PLE products or services with statements different from or beyond the parameters stated by PLE for that product or service voids all express and implied warranties for the associated PLE product or service and is an unfair or deceptive business practice. PLE is not responsible for any such statements.

## Contacting Pletronics Inc.

Pletronics Inc.

```
19013 36 th Ave. West
Lynnwood, WA 98036-5761 USA
```

Tel: 425-776-1880
Fax: 425-776-2760
E-mail: ple-sales@pletronics.com
URL: www.pletronics.com

Copyright © 2006, 2007, 2010, 2011 Pletronics Inc.

## X-ON Electronics

Largest Supplier of Electrical and Electronic Components
Click to view similar products for Standard Clock Oscillators category:
Click to view products by Pletronics manufacturer:
Other Similar products are found below :
EP1400SJTSC-125.000M 601137 601252 CSX750FBC-24.000M-UT CSX750FBC-33.333M-UT CSX750FCC-3.6864M-UT F335-12 F33525 F535L-50 DSC506-03FM2 ASA-20.000MHZ-L-T ASA-25.000MHZ-L-T ASA-27.000MHZ-L-T ASV-20.000MHZ-LR-T ECS-2018-160-BN-TR EL13C7-H2F-125.00M MXO45HS-2C-66.6666MHZ NBXDBB017LN1TAG NBXHBA019LN1TAG SiT1602BI-22-33E50.000000E SIT8003AC-11-33S-2.04800X SiT8256AC-23-33E-156.250000X SIT8918AA-11-33S-50.000000G SM4420TEV-40.0M-T1K SMA4306-TL-H F335-24 F335-40 F335-50 F535L-10 F535L-12 F535L-16 F535L-24 F535L-27 F535L-48 PE7744DW-100.0M CSX750FBC-20.000M-UT CSX-750FBC33333000T CSX750FBC-4.000M-UT CSX750FBC-7.3728M-UT CSX750FBC-8.000M-UT CSX750FCC14745600T CSX750FCC-16.000M-UT CSX-750FCC40000000T CSX750FCC-4.000M-UT ASA-22.000MHZ-L-T ASA2-26.000MHZ-L-T ASA-40.000MHZ-L-T ASA-48.000MHZ-L-T ASA-60.000MHZ-L-T ASF1-3.686MHZ-N-K-S

