## Features and Benefits

Wide operating voltage range: from 2.7 V to 24 VAccurate switching thresholdsReverse Supply Voltage Protection
$\square$ Output Current Limit with Auto-Shutoff

- Under-Voltage Lockout Protection
- Thermal Protection

Traceability with integrated unique ID
$\square$ High ESD rating / Excellent EMC performance
Thin SOT23 3L Green Compliant package

## Application Examples

. Automotive, Consumer and Industrial

- Solid-state switch

Brake sensor

- Clutch sensor
- Sunroof/Tailgate opener
- Steering Column Lock
$\square$ Open / Close detection

|  | Ordering Information |  |  | Comment |
| :---: | :---: | :---: | :---: | :---: |
| Part No. | Temperature Code | Package Code |  |  |
| MLX92231LSE-AAA-xxx-RE | $\mathrm{L}\left(-40^{\circ} \mathrm{C}\right.$ to $\left.150^{\circ} \mathrm{C}\right)$ | SE (TSOT-3L) | RE (Reel) |  |
| MLX92231LUA-AAA-xxx-BU | $\mathrm{L}\left(-40^{\circ} \mathrm{C}\right.$ to $\left.150^{\circ} \mathrm{C}\right)$ | UA (TO-92) | BU (Bulk) |  |

## 1 Functional Diagram



## 2 General Description

The Melexis MLX92231 is a Hall-effect unipolar switch designed in mixed signal CMOS technology.

The device integrates a voltage regulator, Hall sensor with advanced offset cancellation system, automotive qualified EEPROM and an open-drain output driver, all in a single package.

Based on the existing robust 922xx platform, the magnetic core has been equipped with a non-volatile memory that is used to accurately trim the switching thresholds and define the needed output magnetic characteristics (TC, Bop, Brp, Output pole functionality).

In addition to that an ID has been integrated on the IC to have a complete traceability throughout the process flow.

The included voltage regulator operates from 2.7 to 24 V , hence covering a wide range of applications. With the built-in reverse voltage protection, a serial resistor or diode on the supply line is not required so that even remote sensors can be specified for low voltage operation down to 2.7 V while being reverse voltage tolerant.

In the event of a drop below the minimum supply voltage during operation, the under-voltage lock-out protection will automatically freeze the device, preventing the electrical perturbation to affect the magnetic measurement circuitry. The output state is therefore only updated based on a proper and accurate magnetic measurement result.

The open drain output is fully protected against shortcircuit with a built-in current limit. An additional automatic output shut-off is activated in case of a prolonged short-circuit condition. A self-check is then periodically performed to switch back to normal operation if the short-circuit condition is released.
The on-chip thermal protection also switches off the output if the junction temperature increases above an abnormally high threshold. It will automatically recover once the temperature decreases below a safe value. MLX92231-AAA-xxx
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## 3 Absolute Maximum Ratings

| Parameter | Symbol | Value | Units |
| :---: | :---: | :---: | :---: |
| Supply Voltage ${ }^{(1,2)}$ | $V_{D D}$ | +27V | V |
| Supply Voltage (Load Dump) ${ }^{(1,4)}$ | $V_{D D}$ | +32V | V |
| Supply Current ${ }^{(1,2,3)}$ | $I_{\text {D }}$ | +20 | mA |
| Supply Current ${ }^{(1,4,3)}$ | $\mathrm{I}_{\mathrm{DD}}$ | +50 | mA |
| Reverse Supply Voltage ${ }^{(1,2)}$ | $\mathrm{V}_{\text {dDREV }}$ | -24 | V |
| Reverse Supply Voltage ${ }^{(4)}$ | $\mathrm{V}_{\text {dDREV }}$ | -30 | V |
| Reverse Supply Voltage (Load Dump) ${ }^{(11)}$ | $V_{\text {dDREV }}$ | -35 | V |
| Reverse Supply Current ${ }^{(1,2,5)}$ | I DDREV | -20 | mA |
| Reverse Supply Current ${ }^{(1,4, ~}{ }^{\text {, }}$ ) | Iddrev | -50 | mA |
| Output Voltage ${ }^{(1,}{ }^{2)}$ | $\mathrm{V}_{\text {OUT }}$ | +27 | V |
| Output Current $\left.{ }^{(1,}{ }^{2},{ }^{5}\right)$ | Iout | +20 | mA |
| Output Current ${ }^{(1,4,6)}$ | Iout | +75 | mA |
| Reverse Output Voltage ${ }^{(1)}$ | $\mathrm{V}_{\text {outrev }}$ | -0.5 | V |
| Reverse Output Current ${ }^{(1,}{ }^{2)}$ | Ioutrev | -100 | mA |
| Maximum Junction Temperature ${ }^{(7)}$ | TJ | +165 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | Ts | -55 to +165 | ${ }^{\circ} \mathrm{C}$ |
| ESD Sensitivity - HBM ${ }^{(8)}$ | - | 4000 | V |
| ESD Sensitivity - MM ${ }^{(9)}$ | - | 500 | V |
| ESD Sensitivity - CDM ${ }^{(10)}$ | - | 1000 | V |
| Magnetic Flux Density | B | Unlimited | mT |

Table 1: Absolute maximum ratings
Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

[^0]
## 4 General Electrical Specifications

DC Operating Parameters $\mathrm{V}_{\mathrm{DD}}=2.7 \mathrm{~V}$ to $24 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $150^{\circ} \mathrm{C}$ (unless otherwise specified)

| Parameter | Symbol | Test Conditions | Min | Typ ${ }^{(2)}$ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply Voltage | VDD | Operating | 2.7 | - | 24 | V |
| Supply Current | ldo |  | 1.5 | 3.0 | 4.5 | mA |
| Reverse supply current | IDDREV | $V_{D D}=-16 \mathrm{~V}$ |  |  | 1 | mA |
| Output Saturation Voltage | V dson | $\mathrm{V}_{\mathrm{DD}}=3.5$ to 24 V , lout $=20 \mathrm{~mA}$ |  | 0.3 | 0.5 | V |
| Output Leakage | IofF | Vout $=12 \mathrm{~V}, \mathrm{~V}_{\text {DD }}=12 \mathrm{~V}$ |  |  | 10 | $\mu \mathrm{A}$ |
| Output Rise Time ${ }^{(1,6)}$ (Rpu dependent) | $\mathrm{t}_{\mathrm{R}}$ | $\begin{aligned} & R_{\text {PU }}=1 \mathrm{k} \Omega, \mathrm{~V}_{\mathrm{DD}}=12 \mathrm{~V}, \mathrm{~V}_{\mathrm{PU}}=5 \mathrm{~V} \\ & \mathrm{C}_{\text {LOAD }}=50 \mathrm{pF} \text { to } \mathrm{GND} \end{aligned}$ | 0.1 | 0.3 | 1 | $\mu \mathrm{s}$ |
| Output Fall Time ${ }^{(1,6)}$ (On-chip controlled) | $\mathrm{t}_{\mathrm{F}}$ | $\begin{aligned} & R_{P U}=1 \mathrm{k} \Omega, V_{D D}=12 \mathrm{~V}, \mathrm{~V}_{\mathrm{PU}}=5 \mathrm{~V} \\ & \mathrm{C}_{\text {LOAD }}=50 \mathrm{pF} \text { to } \mathrm{GND} \end{aligned}$ | 0.1 | 0.3 | 1 | $\mu \mathrm{s}$ |
| Power-On Time ${ }^{(3,4,7)}$ | ton | $V_{D D}=5 \mathrm{~V}, \mathrm{dV} \mathrm{V}_{\text {d }} \mathrm{dt}>2 \mathrm{~V} / \mathrm{us}$ | - | 40 | 70 | $\mu \mathrm{s}$ |
| Power-On Output State | - | t < ton | High (Vpu) |  |  | - |
| Output Current Limit | ICL | $\mathrm{V}_{\text {DD }}=3.5$ to 24V, Vout $=12 \mathrm{~V}$ | 25 | 40 | 70 | mA |
| Output ON Time under Current Limit conditions ${ }^{(8)}$ | tclon | $V_{\text {PU }}=12 \mathrm{~V}, \mathrm{R}_{\mathrm{PU}}=100 \Omega$ | 150 | 240 |  | $\mu \mathrm{s}$ |
| Output OFF Time under Current Limit conditions ${ }^{8}$ ) | tcloff | $V_{P U}=12 \mathrm{~V}, \mathrm{R}_{\mathrm{PU}}=100 \Omega$ |  | 3.5 |  | ms |
| Chopping Frequency | fС CHOP |  |  | 340 | - | kHz |
| Refresh Period | tper |  | - | 6 | - | $\mu \mathrm{s}$ |
| Output Jitter (p-p) ${ }^{(1)}$ | tıitter | Over 1000 successive switching events @1kHz square wave magnetic field, B > $\pm($ Bopmax +20 mT ) | - | $\pm 4$ | - | $\mu \mathrm{s}$ |
| Maximum Switching Frequency (1,5) | fsw | B > $\pm$ (BормAX +1 mT ), square wave magnetic field | 30 | 50 | - | kHz |
| Under-voltage Lockout Threshold | VuvL |  | - | - | 2.7 | V |
| Under-voltage Lockout Reaction time ${ }^{\text {(1) }}$ | tuvi |  | - | 1 | - | $\mu \mathrm{s}$ |
| Thermal Protection Threshold | TPROT | Junction temperature | - | 190 | - | ${ }^{\circ} \mathrm{C}$ |
| Thermal Protection Release | $\mathrm{T}_{\text {REL }}$ | Junction temperature | - | 180 | - | ${ }^{\circ} \mathrm{C}$ |

Table 2: General specifications

```
Guaranteed by design and verified by characterization, not production tested
Typical values are defined at T =+25`C and V =12V
The Power-On Time represents the time from reaching V 
Power-On Slew Rate is not critical for the proper device start-up.
Maximum switching frequency corresponds to the maximum frequency of the applied magnetic field which is detected without loss of pulses
R and V are respectively the external pull-up resistor and pull-up power supply
B PU B + PU +1mT for direct output sensors or B < B -1mT for inverted output sensors
```



```
after t CLOFF time interval
```

MLX92231-AAA-xxx
3-Wire Hall Effect Switch

## 5 Magnetic Specifications

### 5.1 MLX92231LSE-AAA-004

DC Operating Parameters $\mathrm{V}_{\mathrm{DD}}=3.5 \mathrm{~V}$ to $24 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=-40^{\circ} \mathrm{C}$ to $165^{\circ} \mathrm{C}$

| Test Condition | Operating Point Bop (mT) |  |  | $\begin{gathered} \text { Release Point } \\ B_{\text {PP }}(\mathrm{mT}) \end{gathered}$ |  |  | $\begin{gathered} \mathrm{TC} \\ \left(\mathrm{ppm} /{ }^{\circ} \mathrm{C}\right) \end{gathered}$ | Output behaviour | Active Pole |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min | Typ ${ }^{(10)}$ | Max | Min | Typ ${ }^{(10)}$ | Max | Typ ${ }^{(10)}$ |  | Out = Low (V $\mathrm{V}_{\text {Son }}$ ) |
| $\mathrm{T}_{\mathrm{J}}=-40^{\circ} \mathrm{C}$ | -8.4 | -6 | -3.6 | -6.1 | -3.6 | -1.5 | -1100 | Direct | North pole |
| $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ | -7.1 | -5.5 | -3.9 | -5.3 | -3.5 | -2 |  |  |  |
| $\mathrm{T}_{\mathrm{J}}=150^{\circ} \mathrm{C}$ | -7.1 | -4.8 | -2 | -6.7 | -3.5 | -1.3 |  |  |  |

### 5.2 MLX92231LSE-AAA-007

DC Operating Parameters $\mathrm{V}_{\mathrm{DD}}=3.5 \mathrm{~V}$ to $24 \mathrm{~V}, \mathrm{~T}_{J}=-40^{\circ} \mathrm{C}$ to $165^{\circ} \mathrm{C}$

| Test Condition | $\underset{\operatorname{Bop}^{\text {Operating PT Point }}}{ }$ |  |  | $\begin{gathered} \text { Release Point } \\ \mathrm{B}_{\mathrm{RP}}(\mathrm{mT}) \end{gathered}$ |  |  | $\begin{gathered} \mathrm{TC} \\ \left(\mathrm{ppm} /{ }^{\circ} \mathrm{C}\right) \end{gathered}$ | Output behaviour | Active Pole |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min | Typ ${ }^{(10)}$ | Max | Min | Typ ${ }^{(10)}$ | Max | Typ ${ }^{(10)}$ |  | Out = Low (VDSoN) |
| $\mathrm{T}_{\mathrm{J}}=-40^{\circ} \mathrm{C}$ | 20 | 26 | 33 | 14 | 20 | 25 | 0 | Direct | South pole |
| $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ | 20 | 26 | 33 | 14 | 20 | 25 |  |  |  |
| $\mathrm{T}_{J}=150^{\circ} \mathrm{C}$ | 20 | 26 | 33 | 14 | 20 | 25 |  |  |  |

### 5.3 MLX92231LSE-AAA-009

DC Operating Parameters $\mathrm{V}_{\mathrm{DD}}=3.5 \mathrm{~V}$ to $24 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=-40^{\circ} \mathrm{C}$ to $165^{\circ} \mathrm{C}$

| Test Condition | Operating Point Bop (mT) |  |  | Release Point $B_{\text {RP }}(\mathrm{mT}$ ) |  |  | $\begin{gathered} \mathrm{TC} \\ \left(\mathrm{ppm} /{ }^{\circ} \mathrm{C}\right) \end{gathered}$ | Output behaviour | Active Pole |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min | Typ ${ }^{(10)}$ | Max | Min | Typ ${ }^{(10)}$ | Max | Typ ${ }^{(10)}$ |  | Out = Low (V $\mathrm{V}_{\text {DSon }}$ ) |
| $\mathrm{T}_{J}=-40^{\circ} \mathrm{C}$ | 2 | 3.5 | 5 | 1.4 | 2.5 | 3.3 | 0 | Direct | South pole |
| $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$ | 2.5 | 3.5 | 4.5 | 1.8 | 2.5 | 3.1 |  |  |  |
| $\mathrm{T}_{J}=150^{\circ} \mathrm{C}$ | 2 | 3.5 | 5 | 1.4 | 2.5 | 3.3 |  |  |  |

### 5.4 MLX92231LSE-AAA-010

DC Operating Parameters $\mathrm{V}_{\mathrm{DD}}=3.5 \mathrm{~V}$ to $24 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=-40^{\circ} \mathrm{C}$ to $165^{\circ} \mathrm{C}$

| $\begin{gathered} \text { Test } \\ \text { Condition } \end{gathered}$ | Operating Point Bop (mT) |  |  | $\begin{aligned} & \text { Release Point } \\ & B_{R P}(\mathrm{mT} \text { T) } \end{aligned}$ |  |  | $\begin{gathered} \mathrm{TC} \\ \left(\mathrm{ppm} /{ }^{\circ} \mathrm{C}\right) \end{gathered}$ | Output behaviour | Active Pole |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min | Typ ${ }^{(10)}$ | Max | Min | Typ ${ }^{(10)}$ | Max | Typ ${ }^{(10)}$ |  | Out = Low (Voson) |
| $\mathrm{T}_{\mathrm{J}}=-40^{\circ} \mathrm{C}$ | 8.7 | 10.7 | 13.4 | 6.8 | 9.0 | 11.5 | -1100 | Direct | South pole |
| $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$ | 8.3 | 10 | 11.8 | 6.8 | 8.5 | 10.1 |  |  |  |
| $\mathrm{T}_{\mathrm{J}}=150^{\circ} \mathrm{C}$ | 6.1 | 8.6 | 11.4 | 4.9 | 7.3 | 9.8 |  |  |  |

## 6 Magnetic Behaviour

Operation Point $B_{O P}$ - magnetic threshold for activation of the device output, turning in ON (low) state.
Release Point $B_{R P}$ - magnetic threshold for release of the device output, turning in OFF (high) state).
Hysteresis $B_{H Y S}$ - magnetic hysteresis, $B_{H Y S}=B_{O P}-B_{R P}$

### 6.1 Latch sensor

| Parameter | Pole Active | Remark |
| :--- | :--- | :--- |
| Option 1 | South | Fig.1 |
| Option 2 | North | Fig.2 |




Fig. 1 -South Pole Active


Fig. 2 -North Pole Active

## 7 Detailed General Description

Based on mixed signal CMOS technology, Melexis MLX92231 is a Hall-effect device with a pre-programmed magnetic threshold. It allows using generic magnets, weak magnets or larger air gap.

The chopper-stabilized amplifier uses switched capacitor techniques to suppress the offset generally observed with Hall sensors and amplifiers. The CMOS technology makes this advanced technique possible and contributes to smaller chip size and lower current consumption than bipolar technology. The small chip size is also an important factor to minimize the effect of physical stress.
This combination results in more stable magnetic characteristics and enables faster and more precise design.
The operating voltage from 2.7 V to 24 V , pre-programmed tc and an operating temperature range according to "L" specification make this device suitable for automotive, industrial and consumer low voltage applications.

The output signal is open-drain type. Such output allows simple connectivity with TTL or CMOS logic by using a pullup resistor tied between a pull-up voltage and the device output

## 8 Latching Characteristic

The MLX92231-AAA exhibits bipolar magnetic switching characteristics.


Latch characteristic

Typically, the device behaves as a latch with symmetric operating and release switching points ( $\mathrm{BOP}=|\mathrm{BRP}|$ ). A bipolar switch is closely operating as a latch as it requires both magnetic poles to turn the output ON and OFF. However, magnetic parameters limits are defined so that the magnetic memory is not guaranteed. In absence of magnetic field, the output could keep or change its state depending on the operating conditions.

## 9 Application Information

### 9.1 Typical Three-Wire Application Circuit



Notes:

1. For proper operation, a 10 nF to 100 nF bypass capacitor should be placed as close as possible to the $\mathrm{V}_{\mathrm{DD}}$ and ground pin.
2. The pull-up resistor $R_{P U}$ value should be chosen in to limit the current through the output pin below the maximum allowed continuous current for the device.
3. A capacitor connected to the output is not needed, because the output slope is generated internally.

### 9.2 Automotive and Harsh, Noisy Environments Three-Wire Circuit



Notes:

1. For proper operation, a 10 nF to 100 nF bypass capacitor should be placed as close as possible to the $V_{D D}$ and ground pin.
2. The device could tolerate negative voltage down to -24 V , so if negative transients over supply line $\mathrm{V}_{\text {PEAK }}<-30 \mathrm{~V}$ are expected, usage of the diode D1 is recommended. Otherwise only R1 is sufficient.
When selecting the resistor R1, three points are important:

- the resistor has to limit $I_{D D} / I_{\text {DDREV }}$ to 50 mA maximum
- the resistor has to withstand the power dissipated in both over voltage conditions ( $\mathrm{V}_{\mathrm{R} 1}{ }^{2} / \mathrm{R} 1$ )
- the resulting device supply voltage $V_{D D}$ has to be higher than $V_{D D} \min \left(V_{D D}=V_{C C}-R 1 . I_{D D}\right)$

3. The device could tolerate positive supply voltage up to +27 V (until the maximum power dissipation is not exceeded), so if positive transients over supply line with $\mathrm{V}_{\text {PEAK }}>32 \mathrm{~V}$ are expected, usage a zener diode $\mathrm{Z1}$ is recommended. The R1-Z1 network should be sized to limit the voltage over the device below the maximum allowed.

## 10 Standard information regarding manufacturability of Melexis products with different soldering processes

Our products are classified and qualified regarding soldering technology, solderability and moisture sensitivity level according to following test methods:

## Reflow Soldering SMD's (SUurface Mount Devices)

- IPC/JEDEC J-STD-020

Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices (classification reflow profiles according to table 5-2)

- EIA/JEDEC JESD22-A113

Preconditioning of Nonhermetic Surface Mount Devices Prior to Reliability Testing
(reflow profiles according to table 2)

## Wave Soldering SMD's (즈urface Mount Devices) and THD's (Through Hole Devices)

- EN60749-20

Resistance of plastic- encapsulated SMD's to combined effect of moisture and soldering heat

- EIA/JEDEC JESD22-B106 and EN60749-15

Resistance to soldering temperature for through-hole mounted devices

## Iron Soldering THD's (Through Hole Devices)

- EN60749-15

Resistance to soldering temperature for through-hole mounted devices

## Solderability SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

- EIA/JEDEC JESD22-B102 and EN60749-21 Solderability

For all soldering technologies deviating from above mentioned standard conditions (regarding peak temperature, temperature gradient, temperature profile etc) additional classification and qualification tests have to be agreed upon with Melexis.

The application of Wave Soldering for SMD's is allowed only after consulting Melexis regarding assurance of adhesive strength between device and board.

Melexis is contributing to global environmental conservation by promoting lead free solutions. For more information on qualifications of RoHS compliant products (RoHS = European directive on the Restriction Of the use of certain Hazardous Substances) please visit the quality page on our website: http://www.melexis.com/quality.aspx

## 11 ESD Precautions

Electronic semiconductor products are sensitive to Electro Static Discharge (ESD).
Always observe Electro Static Discharge control procedures whenever handling semiconductor products.

## 12 Package Information

### 12.1 SE (TSOT-3L) Package Information



TOP VIEW


Hall plate location


| SE Pin № | Name | Type | Function |
| :--- | :--- | :--- | :--- |
| 1 | VDD | Supply | Supply Voltage pin |
| 2 | OUT | Output | Open Drain output pin |
| 3 | GND | Ground | Ground pin |

[^1]
### 12.2 UA (TO92-3L) Package Information



Hall plate location


| UA Pin № | Name | Type | Function |
| :--- | :--- | :--- | :--- |
| 1 | VDD | Supply | Supply Voltage pin |
| 2 | GND | Ground | Ground pin |
| 3 | OUT | Output | Open Drain output pin |

[^2]
## 13 Disclaimer

Devices sold by Melexis are covered by the warranty and patent indemnification provisions appearing in its Term of Sale. Melexis makes no warranty, express, statutory, implied, or by description regarding the information set forth herein or regarding the freedom of the described devices from patent infringement. Melexis reserves the right to change specifications and prices at any time and without notice. Therefore, prior to designing this product into a system, it is necessary to check with Melexis for current information. This product is intended for use in normal commercial applications. Applications requiring extended temperature range, unusual environmental requirements, or high reliability applications, such as military, medical life-support or life-sustaining equipment are specifically not recommended without additional processing by Melexis for each application.
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[^0]:    ${ }^{1}$ The maximum junction temperature should not be exceeded
    ${ }^{2}$ For maximum 1 hour
    ${ }^{3}$ Including current through protection device
    ${ }_{5}^{4}$ For maximum 500 ms
    ${ }^{5}$ Through protection device
    ${ }^{6}$ For V
    ${ }^{7}$ For 1000 hours.
    ${ }^{8}$ Human Model according AEC-Q100-002 standard
    ${ }^{9}$ Machine Model according AEC-Q100-003 standard
    ${ }^{10}$ Charged Device Model according AEC-Q100-011 standard
    ${ }^{11}$ For maximum 100 ms

[^1]:    Table 1: SE Package pinout

[^2]:    Table 2: UA Package pinout

