Ordering number : EN7299A

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DATA SHEET

N-Channel Silicon MOSFET

CPH6414 — General-Purpose Switching Device Applications

Features

- Low ON-resistance.
- 4V drive.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		30	V
Gate-to-Source Voltage	V_{GSS}		±20	V
Drain Current (DC)	I_D		5	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	20	A
Allowable Power Dissipation	P_D	Mounted on a ceramic board (1200mm²×0.8mm)	1.6	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		−55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0V	30			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=30V, V_{GS}=0V			1	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}= ±16V, V_{DS}=0V			±10	μA
Cutoff Voltage	$V_{GS}(off)$	V_{DS}=10V, I_D=1mA	1.2		2.6	V
Forward Transfer Admittance	$\|yfs\|$	V_{DS}=10V, I_D=3A	3.1	4.5		S
Static Drain-to-Source On-State Resistance	$R_{DS}(on)1$	I_D=3A, V_{GS}=10V		37	48	mΩ
	$R_{DS}(on)2$	I_D=1.5A, V_{GS}=4V		63	88	mΩ
Input Capacitance	Ciss	V_{DS}=10V, f=1MHz		460		pF
Output Capacitance	Coss	V_{DS}=10V, f=1MHz		95		pF
Reverse Transfer Capacitance	Crss	V_{DS}=10V, f=1MHz		75		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		11		ns
Rise Time	t_r	See specified Test Circuit.		12		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit.		31		ns
Fall Time	t_f	See specified Test Circuit.		18		ns

Marking : KQ

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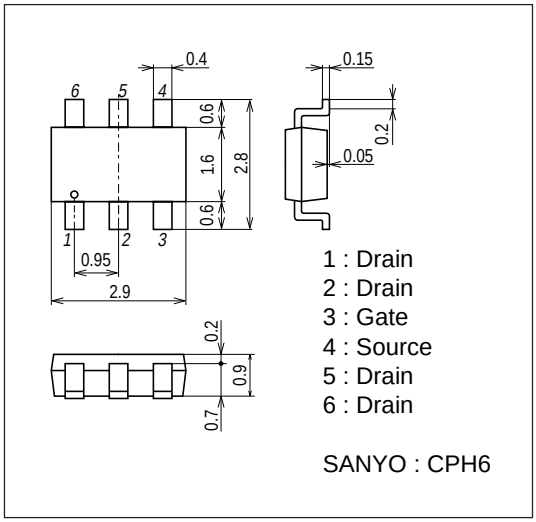
Continued from preceding page.

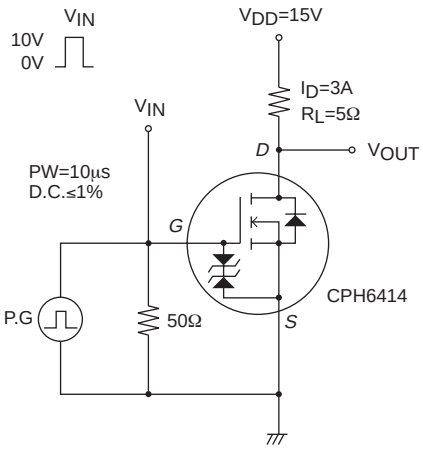
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Total Gate Charge	Qg	V_{DS}=10V, V_{GS}=10V, I_D=5A		8.5		nC
Gate-to-Source Charge	Qgs	V_{DS}=10V, V_{GS}=10V, I_D=5A		1.8		nC
Gate-to-Drain “Miller” Charge	Qgd	V_{DS}=10V, V_{GS}=10V, I_D=5A		1.3		nC
Diode Forward Voltage	V_{SD}	I_S=5A, V_{GS}=0V		0.86	1.2	V

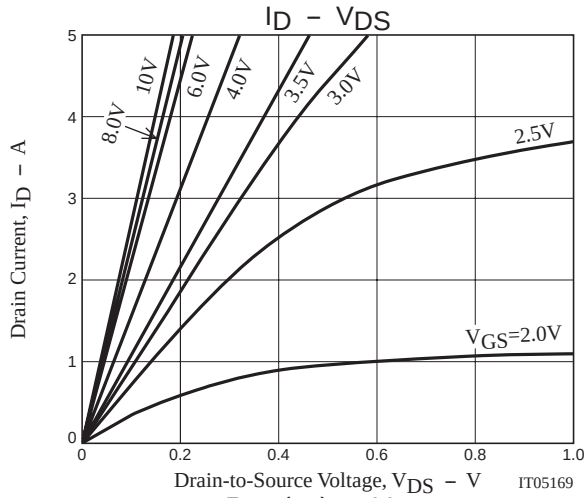
Package Dimensions

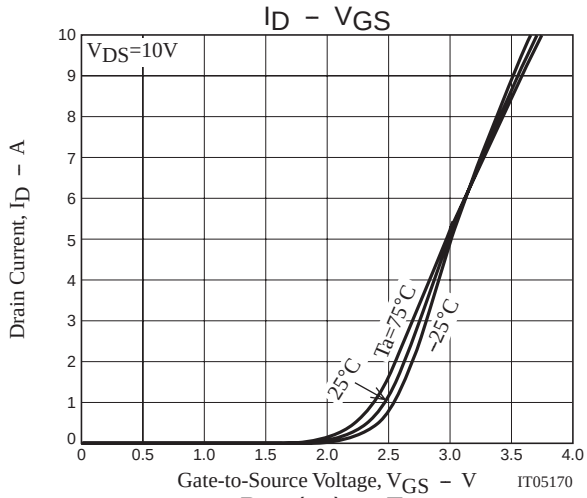
unit : mm
7018-003

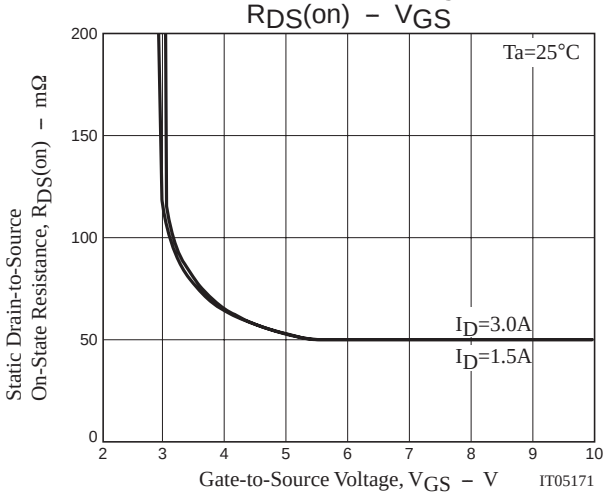


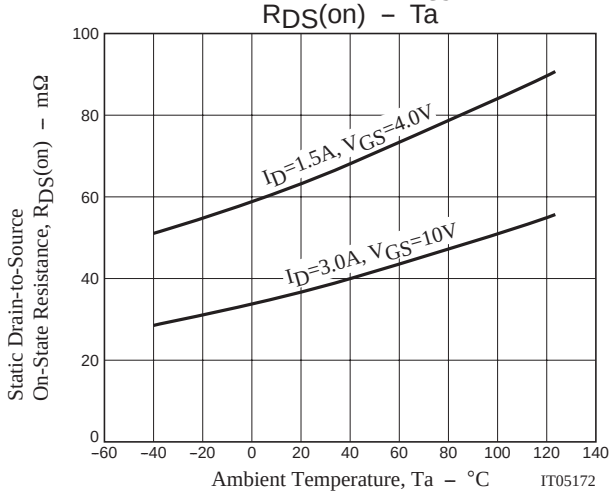
Switching Time Test Circuit

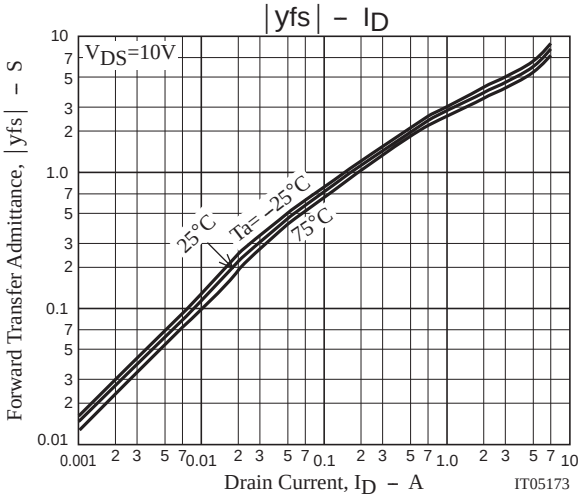


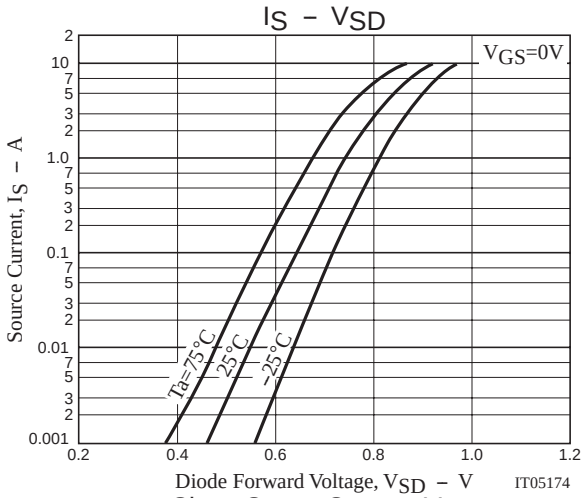


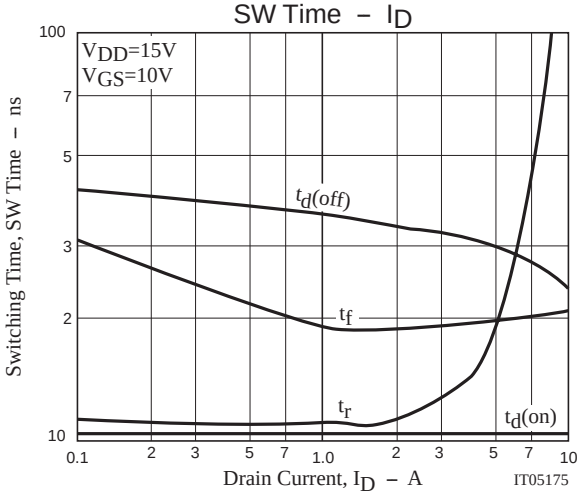


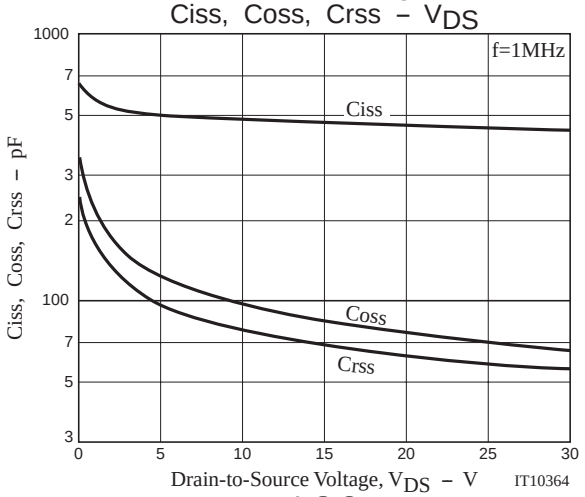


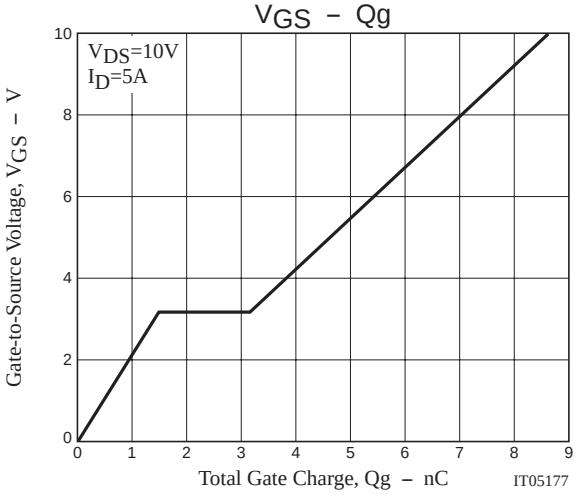


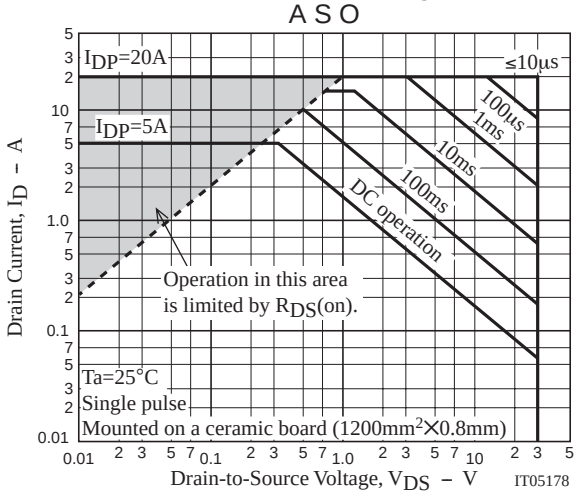

|yfs| – ID
VDS=10V
Ta= –25°C
25°C
75°C
Forward Transfer Admittance, |yfs| – S
Drain Current, ID – A
IT05173

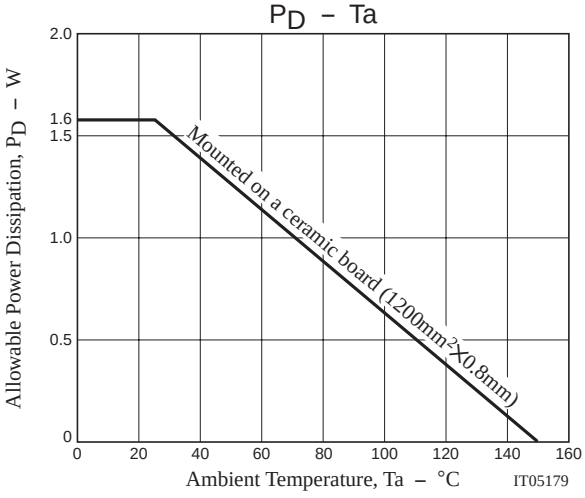

IS – VSD
VGS=0V
Ta=75°C
25°C
–25°C
Source Current, IS – A
Diode Forward Voltage, VSD – V
IT05174


SW Time – ID
VDD=15V
VGS=10V
td(off)
tf
tr
td(on)
Switching Time, SW Time – ns
Drain Current, ID – A
IT05175


Ciss, Coss, Crss – VDS
f=1MHz
Ciss
Coss
Crss
Ciss, Coss, Crss – pF
Drain-to-Source Voltage, VDS – V
IT10364


VGS – Qg
VDS=10V
ID=5A
Gate-to-Source Voltage, VGS – V
Total Gate Charge, Qg – nC
IT05177


A S O
IDP=20A
IDP=5A
≤10μs
100μs
1ms
10ms
100ms
DC operation
Operation in this area
is limited by RDS(on).
Ta=25°C
Single pulse
Mounted on a ceramic board (1200mm2×0.8mm)
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT05178


PD – Ta
Mounted on a ceramic board (1200mm2×0.8mm)
Allowable Power Dissipation, PD – W
Ambient Temperature, Ta – °C
IT05179

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