

**CRYSTAL OSCILLATOR
LOW-JITTER SAW OSCILLATOR**

XG-1000CA/CB

- Output frequency range : 50 MHz to 170 MHz
- Supply voltage : 1.8 V / 2.5 V / 3.3 V
- Frequency tolerance : $\pm 50 \times 10^{-6}$, $\pm 100 \times 10^{-6}$
- Output : CMOS
- Function : Output enable (OE)
- Package size : CA: 7.0×5.0×1.2 mm
CB: 5.0×3.2×1.1 mm

- Very low jitter and low phase noise by SAW unit.



Product Number (please contact us)
XG-1000CA: Q3851CA00xxxx00
XG-1000CB: Q3851CB00xxxx00



Actual size

XG-1000CA

XG-1000CB



Specifications (characteristics)

Item	Symbol	Specifications			Conditions / Remarks
		E	D	C	
Output frequency range *1	fo	50.000 MHz to 170.000 MHz 75.000 MHz, 98.304 MHz, 100.000 MHz, 106.250 MHz, 125.000 MHz, 150.000 MHz			Standard frequency
Supply voltage	Vcc	1.8 V ±0.1V	2.5 V ±0.125 V	3.3 V ±0.3V	
Storage temperature	T_stg	-40 °C to +100 °C			Storage as single product.
Operating temperature	T_use	-10 °C to +70 °C			
Frequency tolerance *2	f_tol	B: $\pm 50 \times 10^{-6}$ C: $\pm 100 \times 10^{-6}$			
Current consumption	I_cc	20 mA Max.	25 mA Max.	35 mA Max.	OE=Vcc, No load condition
Disable current	I_dis	15 mA Max.	20 mA Max.	30 mA Max.	OE=GND
Symmetry	SYM	40 % to 60 %			fo ≤ 125 MHz fo > 125 MHz
Output voltage	V_OH V_OL	Vcc-0.35 V Min 0.35 V Max.			E: I_OH = -6 mA / C, D: I_OH = -8 mA E: I_OL = 6 mA / C, D: I_OL = 8 mA
Output load condition (CMOS)	L_CMOS	15 pF Max.			
Input voltage	V_IH V_IL	70 % Vcc Min. 30 % Vcc Max.			OE terminal
Rise time / Fall time	t_r / t_f	2 ns Max.			Between 20% Vcc and 80% Vcc level, L_CMOS ≤ Max
Start-up time	t_str	10 ms Max.			Time at minimum supply voltage to be 0 s
Jitter *3	t_RMS t_p-p	3 ps Typ. 25 ps Typ.			σ (RMS of total distribution) Peak to Peak
Frequency aging	f_aging	$\pm 5 \times 10^{-6}$ / year Max.			+25 °C, First year, Vcc=1.8 V, 2.5 V, 3.3 V

*1 Please contact us for requirements non-standard frequencies.

*2 This includes initial frequency tolerance, temperature variation, supply voltage variation and load variation.

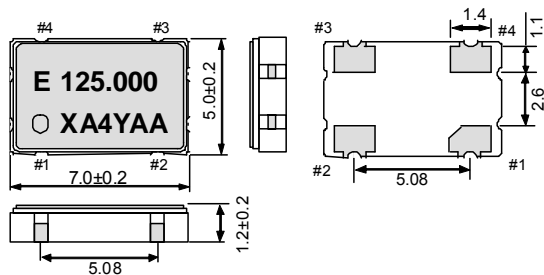
*3 Tested using a DTS-2075 Digital timing system made by WAVECREST with jitter analysis software VISI6.

Operating voltage	E: 1.8V	D: 2.5V	C: 3.3V
Frequency tolerance and operating temperature	EB	DB	CB
	EC	DC	CC

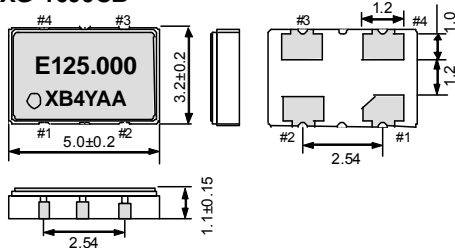
External dimensions

(Unit:mm)

● XG-1000CA



● XG-1000CB



Pin map

Pin	Connection
1	OE
2	GND
3	OUT
4	Vcc

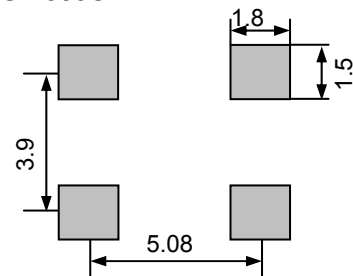
OE pin = HIGH : Specified frequency output.
OE pin = LOW : Output is high impedance

#2 is connected to the cover

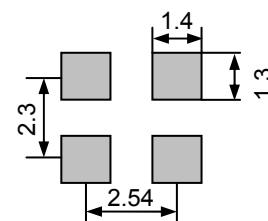
Footprint (Recommended)

(Unit:mm)

● XG-1000CA



● XG-1000CB



To maintain stable operation, provide a 0.01uF to 0.1uF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.




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	<p>► Pb free.</p>
	<p>► Complies with EU RoHS directive. *About the products without the Pb-free mark. Contains Pb in products exempted by EU RoHS directive. (Contains Pb in sealing glass, high melting temperature type solder or other.)</p>
	<p>► The products have been designed for high reliability applications such as Automotive.</p>

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