SANYO Semiconductors DATA SHEET

## LA47536 - Four-Channel 45 W BTL Car Audio Power Amplifier

## Overview

The LA47536 is a 4-channel BTL power amplifier IC developed for use in car audio systems. The output stage features a pure complimentary structure that uses V-pnp transistors on the high side and npn transistors on the low side to provide high power and superb audio quality.
The LA47536 includes almost all the functions required for car audio use, including a standby switch, a muting function, and each protection circuit. It also provides a self-diagnosis function (output offset detection).

## Functions and Features

- Voltage gain: 32 dB
- High output power
- Pomax $(1)=45 \mathrm{~W}$ (typical)
(At $\mathrm{V}_{\mathrm{CC}}=14.4 \mathrm{~V}, \mathrm{f}=1 \mathrm{kHz}, \mathrm{Vi}=2.5 \mathrm{Vrms}, \mathrm{RL}=4 \Omega$ )
$-\operatorname{Pomax}(2)=40 \mathrm{~W}$ (typical)
(At $\mathrm{V}_{\mathrm{CC}}=13.7 \mathrm{~V}, \mathrm{f}=1 \mathrm{kHz}, \mathrm{Vi}=2.5 \mathrm{Vrms}, \mathrm{RL}=4 \Omega$ )
- Po = 28 W (typical)
(At $\mathrm{V}_{\mathrm{CC}}=14.4 \mathrm{~V}, \mathrm{f}=1 \mathrm{kHz}, \mathrm{THD}=10 \%, \mathrm{RL}=4 \Omega$ )
- Muting function (pin 22)
- Standby switch (pin 4)
- Built-in various protection circuits
- Output pin-to- $\mathrm{V}_{\mathrm{CC}}$ short
- Output pin-to-GND short
- Load short
- Over voltage
- Thermal shut down
- Self-diagnosis function that detects the output offset (pin 25)
- Output offset detection on/off switch (pin 1)

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## Specifications

Maximum Ratings at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :--- | :---: | :--- | ---: | :---: |
| Maximum supply voltage | $\mathrm{V}_{\mathrm{CC}} \mathrm{max} 1$ | No signal, $\mathrm{t}=1$ minute | 26 | V |
|  | $\mathrm{~V}_{\mathrm{CC}} \mathrm{max} 2$ | With an input signal applied | 18 | V |
| Maximum output current | IO peak | Per channel | $4.5 / \mathrm{ch}$ | A |
| Allowable power dissipation | Pd max | Assuming an infinite heat sink | 50 | W |
| Operating temperature | Topr |  | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | Tstg |  | -40 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Junction to case thermal resistance | $\theta \mathrm{j}-\mathrm{C}$ |  | 1 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

Recommended Operating Conditions at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :--- | :---: | :---: | :---: | :---: |
| Recommended supply voltage | $\mathrm{V}_{\mathrm{CC}}$ |  | 14.4 | V |
| Recommended load resistance | $\mathrm{R}_{\mathrm{L}} \mathrm{Op}$ |  | 4 | $\Omega$ |
| Operating supply voltage range | $\mathrm{V}_{\mathrm{CC}}$ op | The range where Pd max is not exceeded | 9 to 18 | V |

Electrical Characteristics at $\mathrm{Ta}=25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=14.4 \mathrm{~V}, \mathrm{RL}=4 \Omega, \mathrm{f}=1 \mathrm{kHz}, \mathrm{Rg}=600 \Omega$

| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| Quiescent current | ICCO | $\mathrm{R}_{\mathrm{L}}=\infty, \mathrm{Rg}=0$ |  | 200 | 400 | mA |
| Standby current | Ist | Vst $=0$ |  |  | 10 | $\mu \mathrm{A}$ |
| Output offset | $\mathrm{V}_{\mathrm{N}}$ offset | $\mathrm{Rg}=0$ | -150 |  | +150 | mV |
| Voltage gain | VG | $\mathrm{V}_{\mathrm{O}}=0 \mathrm{dBm}$ | 31 | 32 | 33 | dB |
| Voltage gain difference | $\Delta \mathrm{VG}$ |  | -1 |  | +1 | dB |
| Output power | $\mathrm{P}_{\mathrm{O}}^{1}$ | THD $=10 \%$ | 23 | 28 |  | W |
|  | $P_{0}$ max1 | $\mathrm{V}_{\mathrm{CC}}=13.7 \mathrm{~V}, \mathrm{~V}_{\mathrm{IN}}=2.5 \mathrm{Vrms}$ |  | 40 |  | W |
|  | $\mathrm{P}_{\mathrm{O}} \mathrm{max} 2$ | $\mathrm{V}_{\mathrm{IN}}=2.5 \mathrm{Vrms}$ |  | 45 |  | W |
| Total harmonic distortion | THD | $\mathrm{P}_{\mathrm{O}}=4 \mathrm{~W}$ |  | 0.1 | 0.4 | \% |
| Channel separation | CHsep | V O $=0 \mathrm{dBm}, \mathrm{Rg}=10 \mathrm{k} \Omega$ | 50 | 60 |  | dB |
| Ripple rejection ratio | SVRR | $\mathrm{Rg}=0, \mathrm{fr}=100 \mathrm{~Hz}, \mathrm{Vccr}=0 \mathrm{dBm}, 20 \mathrm{~Hz}$ to 20 kHz | 40 | 45 |  | dB |
| Output noise voltage | $\mathrm{V}_{\mathrm{NO}}$ | $\mathrm{Rg}=0,20 \mathrm{~Hz}$ to 20 kHz |  | 300 | 450 | $\mu \mathrm{V}$ rms |
| Muting attenuation | Ma | $\mathrm{V}_{\mathrm{O}}=20 \mathrm{dBm}$ | 70 | 90 |  | dB |
| Output center point potential | VN | $\mathrm{Rg}=0$ |  | 2.65 |  | V |

## Package Dimensions

unit : mm
3236



## Functional Description

## 1. Standby Switch Function (pin 4)

The pin 4 threshold voltage is set to be 2 VBE . When Vst is 2.0 V or higher, the amplifier will be on, and when Vst is 0.7 V or lower, the amplifier will be off. Note that pin 4 requires an operating current of at least $40 \mu \mathrm{~A}$.
2. Muting Function

The IC is set to the muted state by setting pin 22 to the ground potential. In this state, the audio output is muted. The time constant with which the muting function operates is set by an external RC circuit, and this time constant influences the pop noise that occurs when the amplifier is turned on or off.
The muting on and off times due to the recommended external component values $(\mathrm{R}=10 \mathrm{k} \Omega, \mathrm{C}=3.3 \mu \mathrm{~F})$ are as follows. Muting on time: 50 ms
Muting off time: 20 ms
3. Self-Diagnosis Function (Speaker burnout prevention)

During steady state operation, the LA47536 detects, internally, whether or not an abnormal amplifier output offset has occurred, and outputs this signal from pin 25. Applications can prevent speaker burnout and other problems by having the system microcontroller detect this pin 25 output signal and control either the standby state or the power supply. (An abnormal output offset may be caused by, for example, input capacitor leakage current.)
The pin 25 signal is turned off by setting pin 1 to the ground potential.
4. Oscillator Stability

In some cases, parasitic oscillations may be induced by the PCB layout. This oscillation can be eliminated by adding the components listed below. Note that the optimal capacitor value must be verified by testing in the actual mounted state in the end product.

- Connect a capacitor and resistor $(0.1 \mu \mathrm{~F}$ and $2.2 \Omega)$ in series between each output pin and ground.

5. Audio Quality (Low band)

The frequency characteristics in the low frequencies can be improved by making the capacitance of the input capacitors variable.
The recommended capacitance is $2.2 \mu \mathrm{~F}$ and smaller.

## 6. Protection Circuits

Do not ground the outputs with the STBY voltage at around 1.4 V . Also, do not turn the IC off in the grounded state with a time constant provided for the STBY voltage.
7. Pop Noise

Although the LA47536 includes an pop noise prevention circuit, pop noise can be reduced even further by using the muting function as well.

- Activate the muting function at the same time as power is applied. Then, after the output DC potential has stabilized, turn off the muting function.
- When turning the amplifier off, first turn on the muting function and then turn off the power supply. These two methods are effective at minimizing pop noise.

Block and Application Diagram




THD - PO


THD - f




THD - PO


Response - f



$\mathrm{V}_{\mathrm{NO}}-\mathrm{Rg}$






SVRR - VCCR



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