

Current Transducer CTSR 1-P

I_{PRN} = **1A**

For the electronic measurement of current: DC, AC, pulsed..., with galvanic isolation between the primary (high power) and the secondary circuit (electronic circuit).

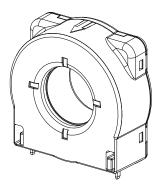














Features

- Closed loop (compensated) current transducer
- Voltage output
- Single supply voltage
- Isolated plastic case material recognized according to UL 94-V0
- PCB mounting

Advantages

- High accuracy
- · Very low offset drift over temperature
- Wide aperture (20.1 mm)
- · High overload capability
- · High isolation capability
- Reference pin with two modes, Ref In and Ref Out
- · Degauss and test functions

Applications

- · Residual current measurement
- Leakage current measurement in transformerless PV inverters
- First human contact protection of PV arrays
- Failure detection in power sources
- Symmetrical fault detection (e.g. after motor inverter)
- · Leakage current detection in stacked DC sources
- Single phase or three phase nominal current measurement up to ± 30 A per wire (DC or AC)

Standards

- EN 50178
- IEC 61010-1 (safety)

Application Domain

- Industrial
- Suitable to fulfil VDE 0126-1-1 and UL 1741



Absolute maximum ratings

Parameter	Symbol	Unit	Value
Supply voltage	v _c	V	7
Primary conductor temperature		°C	110
Impulse overload (100 µs, 500 A/µs)		А	3300

Stresses above these ratings may cause permanent damage. Exposure to absolute maximum ratings for extended periods may degrade reliability.

Isolation characteristics

Parameter	Symbol	Unit	Value	Comment	
RMS voltage for AC isolation test 50/60Hz/1 min	V _d	kV	5.4		
Impulse withstand voltage 1.2/50 μs	v _w	kV	10.1		
Partial discharge extinction voltage @ 10 pC (rms)	V _e	kV	1.65		
Clearance distance (pri sec.)	dCI	mm	11	Shortest distance through air	
Creepage distance (pri sec.)	dCp	mm	11	Shortest path along device body	
Comparative tracking index	СТІ	V	600		
Application example	-	-	1000 V CAT III PD2	Basic isolation, non uniform field according to EN 50178	
Application example	-	-	600 V CAT III PD3	Basic isolation, non uniform field according to EN 50178, IEC 61010	
According to UL 508: primary potential involved in Volt rms AC or DC	-	V	600	For use in a pollution degree 3 environment	
Primary conductor shall be connected after an overvo	oltage device or	system ev	aluated by star	ndard UL 1449	

Primary conductor shall be connected after an overvoltage device or system evaluated by standard UL 1449.

Environmental and mechanical characteristics

Parameter	Symbol	Unit	Min	Тур	Max	Comment
Ambient operating temperature	T _A	°C	-40		105	
Ambient storage temperature	T _S	°C	-50		105	
Mass	m	g		28		
Standards	EN 50178, IEC 61010-1					



Electrical data

At $\mathbf{T}_{\rm A}$ = 25°C, $\mathbf{V}_{\rm C}$ = + 5 V, output voltage referred to $\mathbf{V}_{\rm REF}$ unless otherwise noted.

Parameter	Symbol	Unit	Min	Тур	Max	Comment
Primary nominal residual current rms	I _{PRN}	А		1		
Primary residual current, measuring range	I _{PRM}	А	-1.7		1.7	
Supply voltage	V _C	V	4.75	5	5.25	
Current consumption	I c	mA	16	17.5	21.6	+ I _P (mA)/N _S With N _S = 1000 turns - 40°C 105°C
Reference voltage @ I _P = 0	V _{REF}	V	2.495	2.5	2.505	Internal reference
External reference voltage	V _{REF}	V	2.3		4	Internal resistance of V _{REF} input = 499 Ω Note 1
Electrical offset current referred to primary (Note 2)	I _{OE}	mA	-24	+7	24	
Temperature coefficient of V _{REF}	TCV _{REF}	ppm/K			±50	
Temperature coefficient of \mathbf{V}_{OE} @ \mathbf{I}_{P} = 0	TCV _{OE}	ppm/K		±50	±500	ppm/K of 2.5 V - 40°C 105°C
Theoretical sensitivity	G th	V/A		1.2		
Sensitivity error (Note 2)	$\epsilon_{_{ m G}}$	%	-1.6	0.5	1.6	R _L > 500 kΩ
Temperature coefficient of G	TCG	ppm/K			±200	- 40°C 105°C
Linearity error	$\epsilon_{\scriptscriptstyle \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \!$	% of I _{PRM}		0.5	1	
Reaction time @ 10 % of I _{PRN}	t _{ra}	μs		7		R _L > 500 kΩ, di/dt > 5 A/μs
Response time @ 90 % of I _{PRN}	t,	μs		50		$ m R_L > 500 \ k\Omega,$ di/dt > 5 A/µs
Frequency bandwidth (- 1 dB)	BW	kHz		9.5		$R_L > 500 \text{ k}\Omega$
Output voltage noise (1 Hz 10 kHz)	V _{no}	mV rms		6		$R_L > 500 \text{ k}\Omega$
Magnetic offset current referred to primary (1000 x I _{PRN})	I _{OM}	mA		57		
Accuracy (Note 3)	x	%			1.9	$= (\varepsilon_{\rm G}^2 + \varepsilon_{\rm L}^2)^{1/2}$
Output voltage referred to GND (during Degauss cycle)		V		0.3	0.5	Note 1
Output voltage referred to V _{REF} (Test current)		V	V _{OE} + 0.2	V _{OE} + 0.35	V _{OE} + 0.5	Note 1

 $\underline{\text{Notes}}\textsc{:}\ ^{\text{1)}}$ See "Application information" section.

²⁾ Only with a primary nominal residual current, see paragraph "Primary nominal residual current and primary nominal curent".

 $^{^{3)} \}text{ Accuracy } \textcircled{0} \textbf{ T}_{A} \text{ and } \textbf{I}_{P} : \textbf{ X}_{\textbf{TA}} = (\textbf{X}^2 + (\textbf{TCG} \cdot 100 \cdot (\textbf{T}_{A} - 25))^2 + (\textbf{TCV}_{OE} \cdot 2.5 \cdot (\textbf{T}_{A} - 25)/\textbf{G} \text{th} \cdot 100/\textbf{I}_{P})^2)^{1/2}$



Application information

Filtering, decoupling CTSR transducer

Supply voltage Vc (5 V):

The CTSR transducers have internal decoupling capacitors, but in the case of a power supply track on the application PCB having a high impedance, it is advised to provide local decoupling, 100 nF or more, located close to the transducer.

Reference Vref:

Ripple present on the Vref pin can be filtered with a low value of capacitance because of the internal 499 ohm series resistance. The CTSR transducers have an internal capacitor of 22 nF between Vref pin and Gnd pin and the maximum filter capacitance value which could be added is 1 μ F. Adding a larger decoupling capacitor will increase the activation delay of degauss.

Output Vout:

The CTSR transducers have an internal low pass filter 470 ohm/22 nF; if a decoupling capacitor is added on **V**out pin, the bandwidth and the response time will be affected. In case of short circuit, the transducer CTSR can source or sink up to a maximum of 10 mA on its output **V**out.

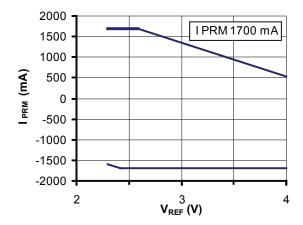
Using an external reference voltage

If the Vref pin of the transducer is not used it could be either left unconnected or filtered according to the previous paragraph "Reference Vref".

The Vref pin has two modes Ref Out and Ref In:

- In the Ref Out mode the 2.5 V internal precision reference is used by the transducer as the reference point for bipolar
 measurements; this internal reference is connected to the Vref pin of the transducer through a 499 ohms resistor. It
 tolerates sink or source currents up to ±5 mA, but the 499 ohms resistor prevents this current to exceed these limits.
- In the Ref In mode, an external reference voltage is connected to the Vref pin; this voltage is specified in the range 2.3 to 4 V and is directly used by the transducer as the reference point for measurements. The external reference voltage Vref must be able:
 - o either to source a typical current of $\frac{Vref-2.5}{499}$, the maximum value will be 3 mA when **V**ref = 4 V.
 - o or to sink a typical current of $\frac{2.5-\textit{Vref}}{499}$, the maximum value will be 0.4 mA when **V**ref = 2.3 V.

The following graphs show how the measuring range of the transducer depends on the external reference voltage value \mathbf{V} ref (\mathbf{V} c = 5 V).



Upper limit: I_p =1700 mA (Vref = 2.3..2.625 V) Upper limit: I_p =-833.3*Vref+3854.2 (Vref = 2.625..4 V) Lower limit: I_p =-833.3*Vref+312.5 (Vref = 2.3..2.375 V)

Lower limit: \vec{l}_{p} =-1700 mA (**V**ref = 2.375..4 V)



Primary nominal residual current and primary nominal current

The primary nominal residual current is the sum of the instantaneous values of all currents flowing through the aperture of the transducer.

The primary nominal current is the current flowing through any conductor placed into the aperture of the transducer.

The presence of a primary nominal current DC or AC leads to an additional uncertainty.

For example, with a primary nominal current of 30 A the uncertainty referred to primary is 3.6 mA.

CTSR transducer in Test mode

When the **V**ref pin is forced at a Low level voltage between 0 and 1 V and is maintained at this level, the output voltage **V**out of CTSR transducer is fixed at 1.2 V +/-0.5 V, as if it measured a primary test current.

The activation time of test mode is min 30 ms. The CTSR transducer can be maintained in test mode as long as needed for checking that it is fully operating.

CTSR transducer in Degauss mode

The CTSR transducers go in degauss mode automatically at each power on or on demand by using the Vref pin.

At power on:

A degauss is automatically generated at each power on of the CTSR transducer; during degaussing the output voltage **V**out is maintained at 0.3 V typ. (max 0.5 V). After c.a. 110 ms, the output voltage **V**out is released and takes the normal operation level in relation with the measured primary current.

Using Vref pin:

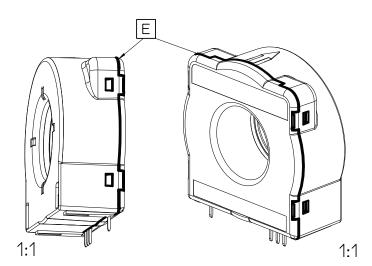
When the pin **V**ref is released from the Low level voltage defined in the Test mode above, there is a rising edge on **V**ref which generates an automatic degauss.

The activation of degauss takes typically 40 µs after releasing Vref pin, then degauss lasts typically 110 ms.

Isolation around the CTSR transducer housing

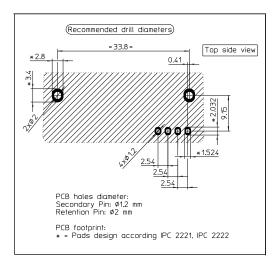
Due to the joint between the case and the cover of the CTSR transducer, there is some isolation distance to respect when primary conductors pass around the CTSR housing.

The figure below shows the joint and the apertures where the clearance between the secondary part inside the CTSR transducer and the surface of the housing is 3 mm (label E).





PCB Footprint



Assembly on PCB

- · Recommended PCB hole diameter
- · Maximum PCB thickness
- Wave soldering profile No clean process only

1.2 mm for secondary pin 2 mm for retention pin

2.4 mm

maximum 260 °C, 10 s

The two retention pins inserted into the case of the transducer are electrically isolated by the housing. Because one of them is close to the secondary connector, it is recommended to connect both to the secondary side GND or +Vc.

Consequently any primary conductor must be placed in the PCB at the requested isolation distance regarding these secondary sides (see also paragraph "Isolation around the CTSR transducer housing").

Safety

This transducer must be used in limited-energy secondary circuits according to IEC 61010-1.



This transducer must be used in electric/electronic equipment with respect to applicable standards and safety requirements in accordance with the manufacturer's operating instructions.



Caution, risk of electrical shock.

When operating the transducer, certain parts of the module can carry hazardous voltage (e.g. primary bus bar, power supply). Ignoring this warning can lead to injury and/or cause serious damage.

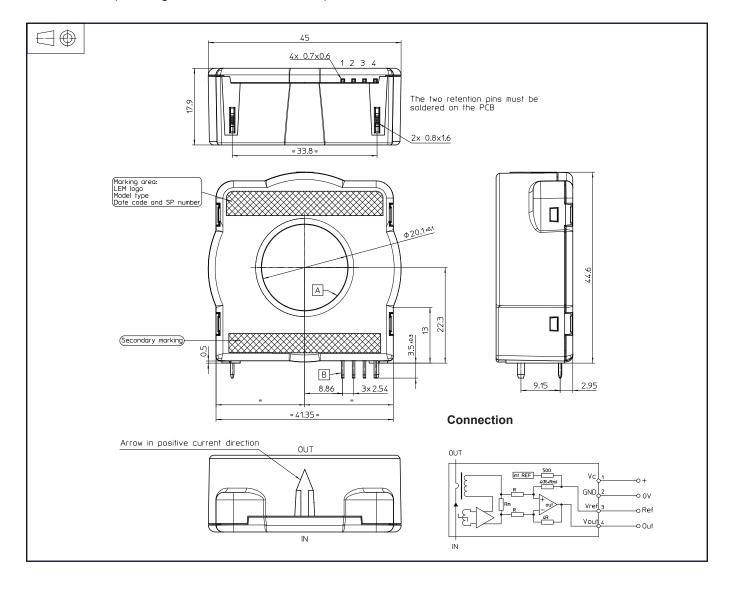
This transducer is a build-in device, whose conducting parts must be inaccessible after installation.

A protective housing or additional shield could be used.

Main supply must be able to be disconnected.



Dimensions (in mm, general tolerance ± 0.3 mm)



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