

HIGHDENSITY MOUNTING HIGH VOLTAGE DARLINGTON **OPTICALLY COUPLED ISOLATORS**



DESCRIPTION

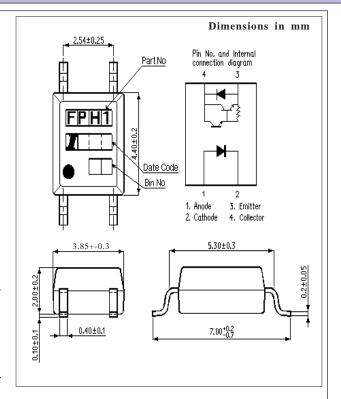
The IS127 is an optically coupled isolator consisting of an infrared light emitting diode and a high voltage NPN silicon photo darlington which has an integral base-emitter resistor to optimise switching speed and elevated temperature characteristics in a space efficient dual in line plastic package.

FEATURES

- Marked as FPH1.
- Current Transfer Ratio MIN. 1000%
- High collector-emitter voltage, Vceo=300V
- $Isolation Voltage (3.75kV_{RMS}, 5.3kV_{PK}) \\ All electrical parameters 100\% tested$
- Drop in replacement for Toshiba TLP127

APPLICATIONS

- Computer terminals
- Industrial systems controllers
- Measuring instruments
- Signal transmission between systems of different potentials and impedances



ISOCOMCOMPONENTSLTD

Unit 25B, Park View Road West, Park View Industrial Estate, Brenda Road Hartlepool, Cleveland, TS25 1UD Tel: (01429) 863609 Fax: (01429) 863581

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ABSOLUTEMAXIMUMRATINGS (25°C unless otherwise specified)

Storage Temperature	$_{-55^{\circ}\text{C}}$ to $+150^{\circ}\text{C}$
Operating Temperature	$_{-}$ -55°C to + 100°C
Lead Soldering Temperature	
(1/16 inch (1.6mm) from case for	10 secs) 260°C

INPUTDIODE

Forward Current	50mA	
Reverse Voltage	6V	
Power Dissipation	70mW	

OUTPUTTRANSISTOR

Collector-emitter Voltage BV _{CEO}	. 300V
Emitter-collector Voltage BV _{ECO}	0.1V
Collector Current	150mA
Power Dissipation	. 150mW

POWERDISSIPATION

Total Power Dissipation	170mW
(derate linearly 2.26mW/°C above 25°C)	

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ Unless otherwise noted)

	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITION
Input	Forward Voltage (V_F)		1.2	1.4	V	$I_{\rm F} = 10 \text{mA}$
	Reverse Current (I_R)			10	μΑ	$V_R = 4V$
Output	$Collector-emitter Breakdown (BV_{CEO})$	300			V	$I_{\rm C} = 0.1 \mathrm{mA}$
	$\begin{aligned} & Emitter-collector Breakdown (BV_{ECO}) \\ & Collector-emitter Dark Current (I_{CEO}) \end{aligned}$	0.1		200	V nA	$I_{\rm E} = 10 \text{uA}$ $V_{\rm CE} = 200 \text{V}$
Coupled	Current Transfer Ratio (CTR)	1000			%	$1 \text{mAI}_{\text{F}}, 2 \text{VV}_{\text{CE}}$
	$Collector\text{-}emitterSaturationVoltageV_{\text{CE}(SAT)}$			1.2	V	$20\mathrm{mAI}_\mathrm{F},100\mathrm{mAI}_\mathrm{C}$
	Input to Output Isolation Voltage $V_{\rm ISO}$	3750 5300			$\begin{matrix} V_{\text{RMS}} \\ V_{\text{PK}} \end{matrix}$	See note 1 See note 1
	Input-output Isolation Resistance $R_{\rm ISO}$	5x10 ¹⁰			Ω	$V_{IO} = 500V \text{ (note 1)}$
	Output Rise Time tr Output Fall Time tf		4 3	18 18	μs μs	$V_{CE} = 2V,$ $I_{C} = 2mA, R_{L} = 100\Omega$

Note 1 Measured with input leads shorted together and output leads shorted together.

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