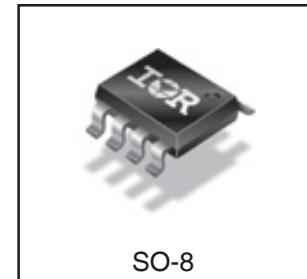
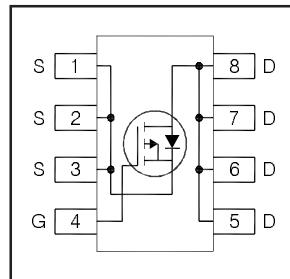


# IRF9328PbF

HEXFET® Power MOSFET

<b>V<sub>DS</sub></b>	<b>-30</b>	<b>V</b>
<b>R<sub>DS(on)</sub> max</b> (@V <sub>GS</sub> = -10V)	<b>11.9</b>	<b>mΩ</b>
<b>R<sub>DS(on)</sub> max</b> (@V <sub>GS</sub> = -4.5V)	<b>19.7</b>	<b>mΩ</b>
<b>Q<sub>g</sub> (typical)</b>	<b>18</b>	<b>nC</b>
<b>I<sub>D</sub></b> (@T <sub>A</sub> = 25°C)	<b>-12</b>	<b>A</b>



SO-8

## Applications

- Charge and Discharge Switch for Notebook PC Battery Application

## Features and Benefits

### Features

Industry-Standard SO8 Package
RoHS Compliant Containing no Lead, no Bromide and no Halogen

### Resulting Benefits

Multi-Vendor Compatibility
Environmentally Friendlier

Orderable part number	Package Type	Standard Pack		Note
		Form	Quantity	
IRF9328PbF	SO8	Tube/Bulk	95	
IRF9328TRPbF	SO8	Tape and Reel	4000	

## Absolute Maximum Ratings

	Parameter	Max.	Units
V <sub>DS</sub>	Drain-to-Source Voltage	-30	V
V <sub>GS</sub>	Gate-to-Source Voltage	± 20	
I <sub>D</sub> @ T <sub>A</sub> = 25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	-12	A
I <sub>D</sub> @ T <sub>A</sub> = 70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	-9.6	
I <sub>DM</sub>	Pulsed Drain Current ①	-96	
P <sub>D</sub> @ T <sub>A</sub> = 25°C	Power Dissipation ④	2.5	W
P <sub>D</sub> @ T <sub>A</sub> = 70°C	Power Dissipation ④	1.6	
	Linear Derating Factor	0.02	W/°C
T <sub>J</sub>	Operating Junction and	-55 to + 150	°C
T <sub>STG</sub>	Storage Temperature Range		

Notes ① through ⑥ are on page 2

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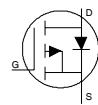
**Static @  $T_J = 25^\circ\text{C}$  (unless otherwise specified)**

	Parameter	Min.	Typ.	Max.	Units	Conditions
$\text{BV}_{\text{DSS}}$	Drain-to-Source Breakdown Voltage	-30	—	—	V	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$
$\Delta \text{BV}_{\text{DSS}/\Delta T_J}$	Breakdown Voltage Temp. Coefficient	—	0.021	—	$\text{V}/^\circ\text{C}$	Reference to $25^\circ\text{C}$ , $I_D = -1\text{mA}$
$R_{\text{DS(on)}}$	Static Drain-to-Source On-Resistance	—	10	11.9	$\text{m}\Omega$	$V_{\text{GS}} = -10\text{V}, I_D = -12\text{A}$ ③
		—	16.1	19.7		$V_{\text{GS}} = -4.5\text{V}, I_D = -9.6\text{A}$ ③
$V_{\text{GS(th)}}$	Gate Threshold Voltage	-1.3	-1.8	-2.4	V	$V_{\text{DS}} = V_{\text{GS}}, I_D = -25\mu\text{A}$
$\Delta V_{\text{GS(th)}}$	Gate Threshold Voltage Coefficient	—	-5.8	—	$\text{mV}/^\circ\text{C}$	
$I_{\text{DSS}}$	Drain-to-Source Leakage Current	—	—	-1.0	$\mu\text{A}$	$V_{\text{DS}} = -24\text{V}, V_{\text{GS}} = 0\text{V}$
		—	—	-150		$V_{\text{DS}} = -24\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$
$I_{\text{GSS}}$	Gate-to-Source Forward Leakage	—	—	-100	$\text{nA}$	$V_{\text{GS}} = -20\text{V}$
	Gate-to-Source Reverse Leakage	—	—	100		$V_{\text{GS}} = 20\text{V}$
$g_{\text{fs}}$	Forward Transconductance	20	—	—	S	$V_{\text{DS}} = -10\text{V}, I_D = -9.6\text{A}$
$Q_g$	Total Gate Charge ⑥	—	18	—	nC	$V_{\text{DS}} = -15\text{V}, V_{\text{GS}} = -4.5\text{V}, I_D = -9.6\text{A}$
$Q_g$	Total Gate Charge ⑥	—	35	52	nC	$V_{\text{GS}} = -10\text{V}$
$Q_{\text{gs}}$	Gate-to-Source Charge ⑥	—	5.3	—	nC	$V_{\text{DS}} = -15\text{V}$
$Q_{\text{gd}}$	Gate-to-Drain Charge ⑥	—	8.5	—	nC	$I_D = -9.6\text{A}$
$R_G$	Gate Resistance ⑥	—	15	—	$\Omega$	
$t_{\text{d(on)}}$	Turn-On Delay Time	—	19	—	ns	$V_{\text{DD}} = -15\text{V}, V_{\text{GS}} = -4.5\text{V}$ ③
$t_r$	Rise Time	—	57	—		$I_D = -1.0\text{A}$
$t_{\text{d(off)}}$	Turn-Off Delay Time	—	80	—		$R_G = 6.8\Omega$
$t_f$	Fall Time	—	66	—		See Figs. 20a &20b
$C_{\text{iss}}$	Input Capacitance	—	1680	—	pF	$V_{\text{GS}} = 0\text{V}$
$C_{\text{oss}}$	Output Capacitance	—	350	—		$V_{\text{DS}} = -25\text{V}$
$C_{\text{rss}}$	Reverse Transfer Capacitance	—	220	—		$f = 1.0\text{kHz}$

**Avalanche Characteristics**

	Parameter	Typ.	Max.	Units
$E_{\text{AS}}$	Single Pulse Avalanche Energy ②	—	120	$\text{mJ}$
$I_{\text{AR}}$	Avalanche Current ①	—	-9.6	A

**Diode Characteristics**

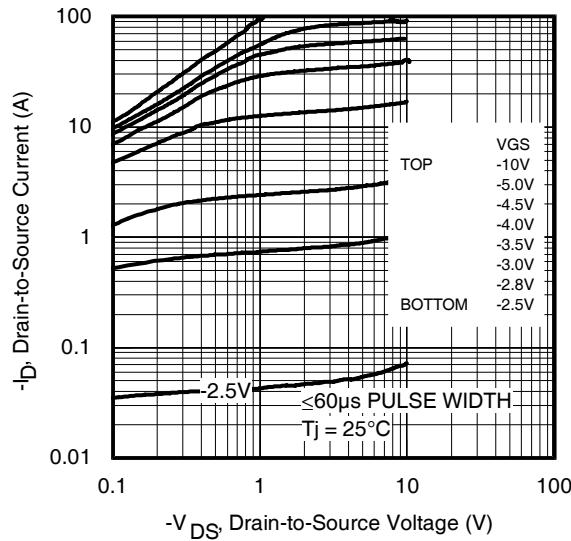
	Parameter	Min.	Typ.	Max.	Units	Conditions
$I_s$	Continuous Source Current (Body Diode)	—	—	-2.5	A	MOSFET symbol showing the integral reverse p-n junction diode.
	Pulsed Source Current (Body Diode) ①	—	—	-96		
$V_{\text{SD}}$	Diode Forward Voltage	—	—	-1.2	V	$T_J = 25^\circ\text{C}, I_S = -2.5\text{A}, V_{\text{GS}} = 0\text{V}$ ③
$t_{rr}$	Reverse Recovery Time	—	51	76	ns	$T_J = 25^\circ\text{C}, I_F = -2.5\text{A}, V_{\text{DD}} = -24\text{V}$
$Q_{rr}$	Reverse Recovery Charge	—	35	53	nC	$dI/dt = 100\text{A}/\mu\text{s}$ ③

**Thermal Resistance**

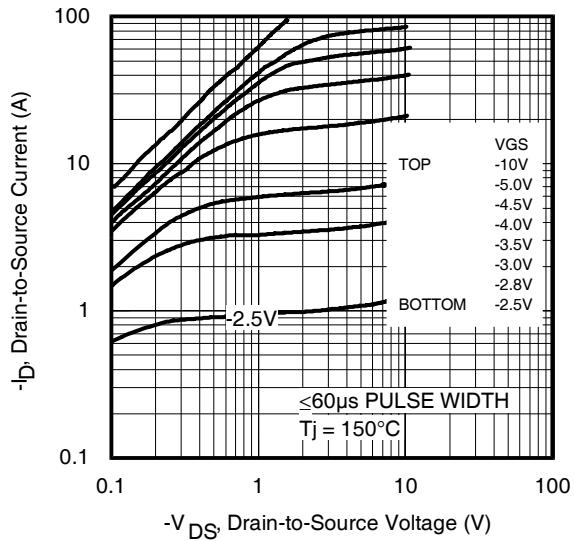
	Parameter	Typ.	Max.	Units
$R_{\text{0JL}}$	Junction-to-Drain Lead ⑤	—	20	$^\circ\text{C/W}$
	Junction-to-Ambient ④	—	50	

**Notes:**

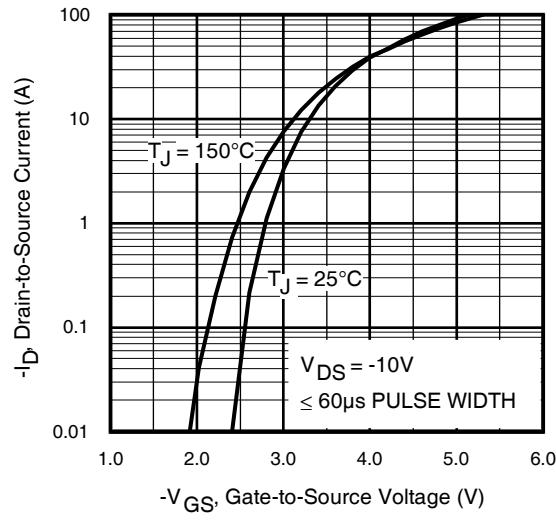
- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting  $T_J = 25^\circ\text{C}$ ,  $L = 2.6\text{mH}$ ,  $R_G = 25\Omega$ ,  $I_{\text{AS}} = -9.6\text{A}$ .
- ③ Pulse width  $\leq 400\mu\text{s}$ ; duty cycle  $\leq 2\%$ .
- ④ When mounted on 1 inch square copper board.
- ⑤  $R_\theta$  is measured at  $T_J$  of approximately  $90^\circ\text{C}$ .
- ⑥ For DESIGN AID ONLY, not subject to production testing.



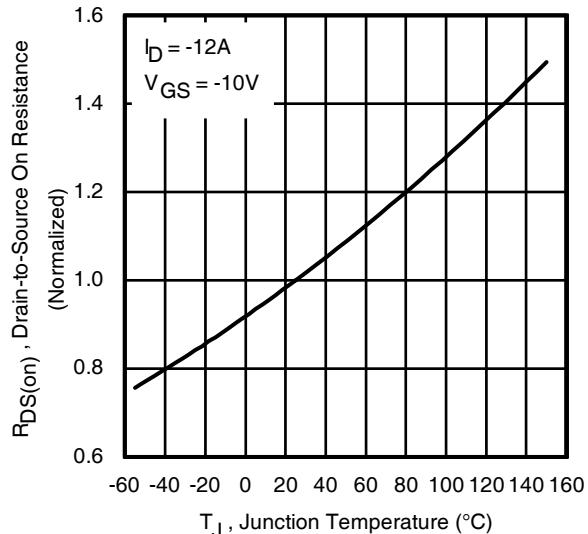
**Fig 1.** Typical Output Characteristics



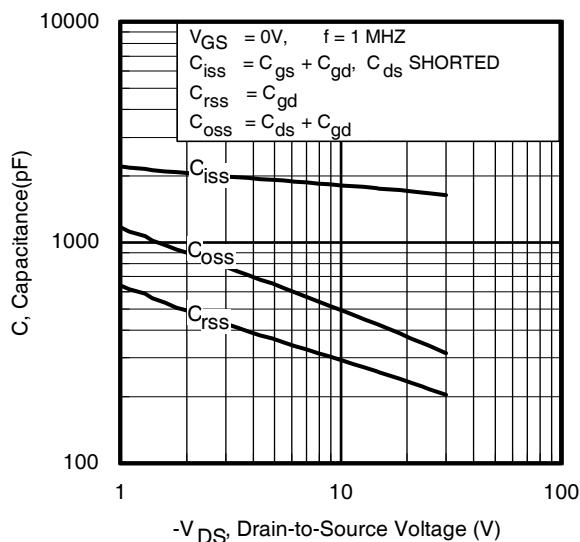
**Fig 2.** Typical Output Characteristics



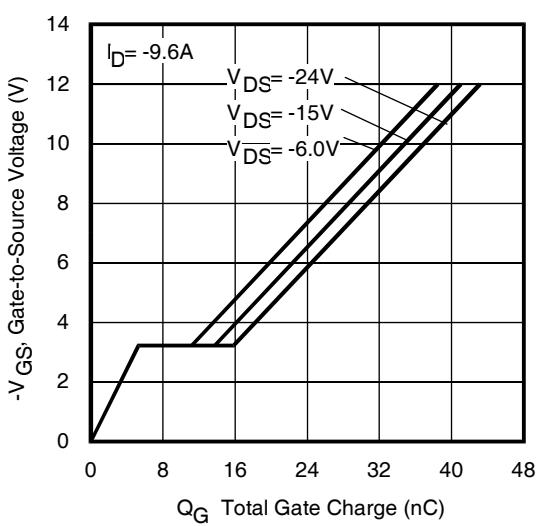
**Fig 3.** Typical Transfer Characteristics



**Fig 4.** Normalized On-Resistance vs. Temperature



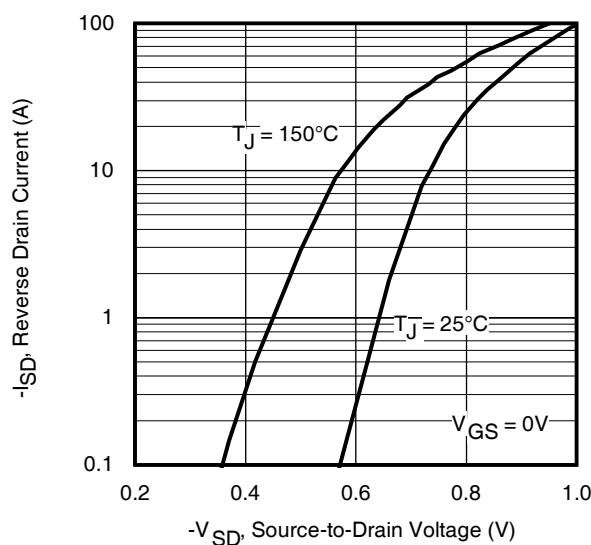
**Fig 5.** Typical Capacitance vs. Drain-to-Source Voltage  
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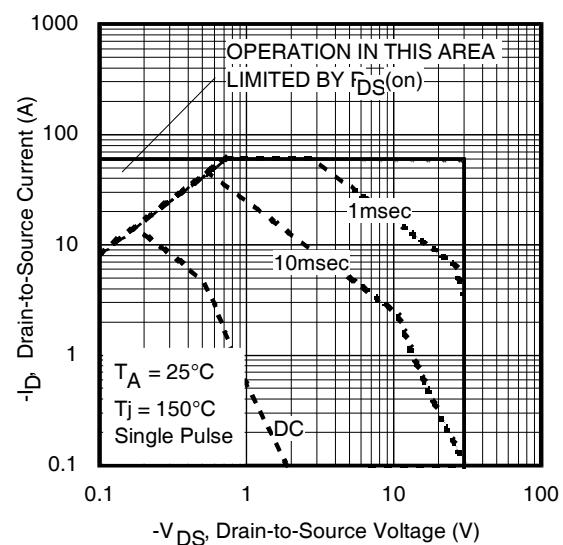
**Fig 6.** Typical Gate Charge vs. Gate-to-Source Voltage

# IRF9328PbF

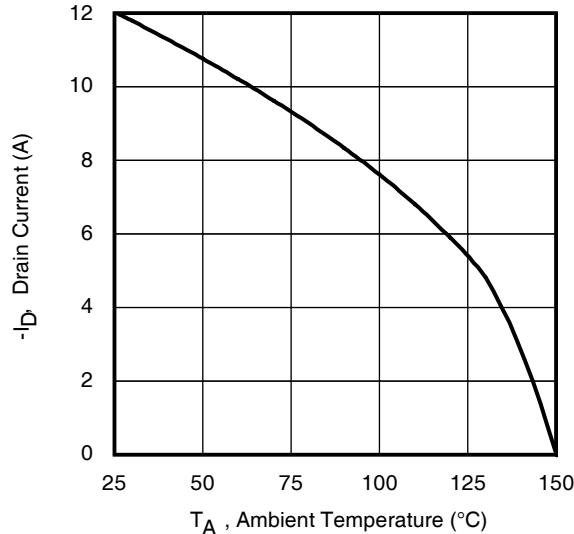
International  
Rectifier



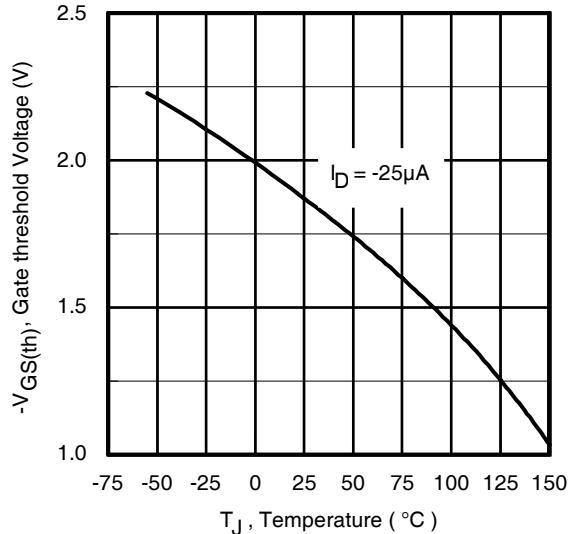
**Fig 7.** Typical Source-Drain Diode Forward Voltage



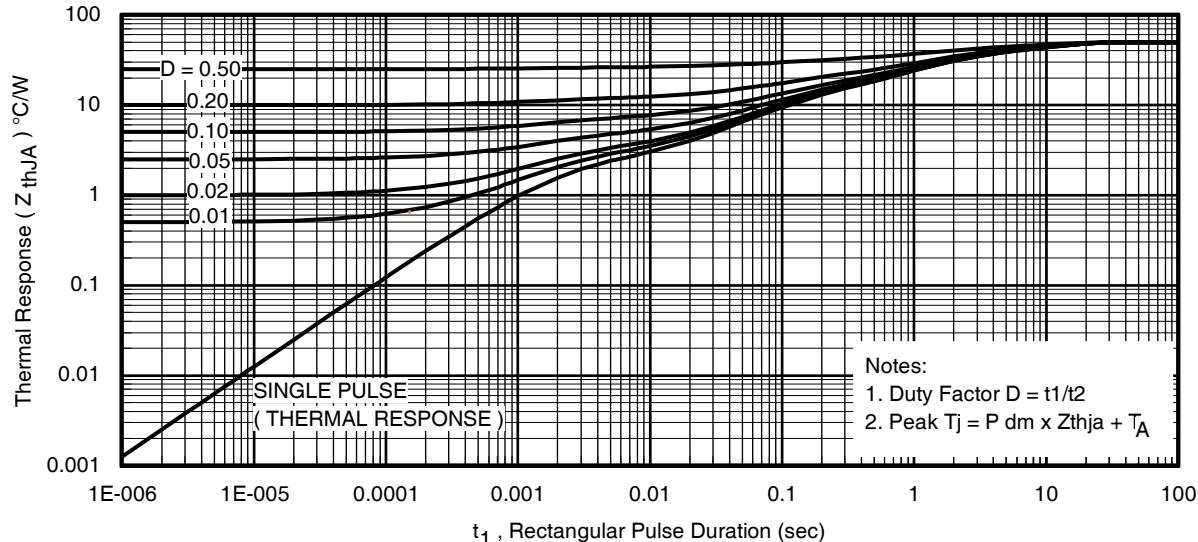
**Fig 8.** Maximum Safe Operating Area



**Fig 9.** Maximum Drain Current vs. Ambient Temperature



**Fig 10.** Threshold Voltage vs. Temperature



**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

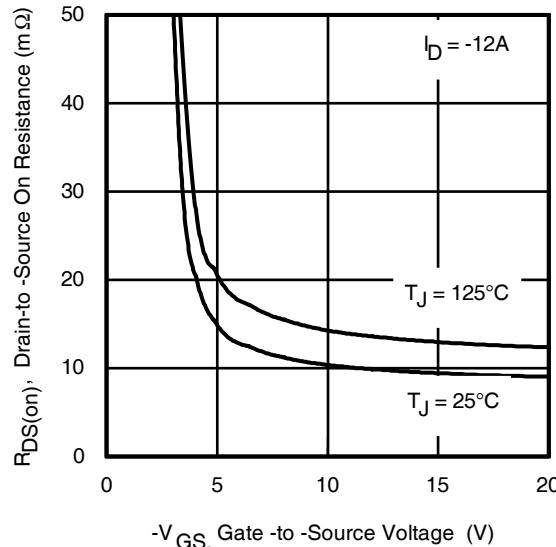


Fig 12. On-Resistance vs. Gate Voltage

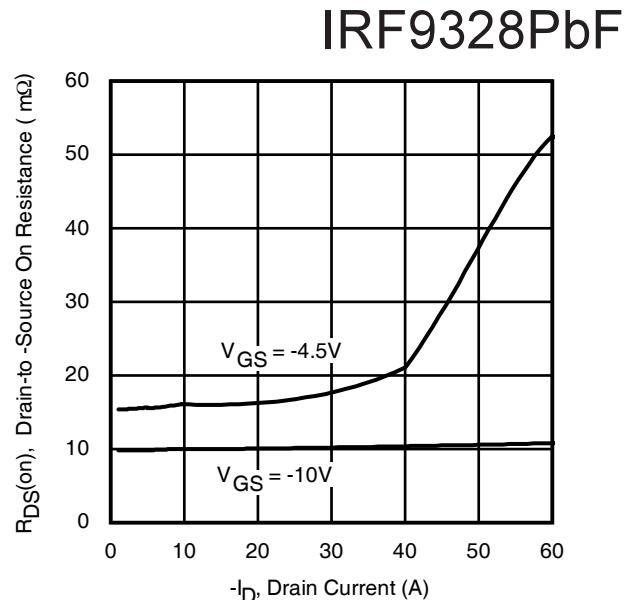


Fig 13. Typical On-Resistance vs. Drain Current

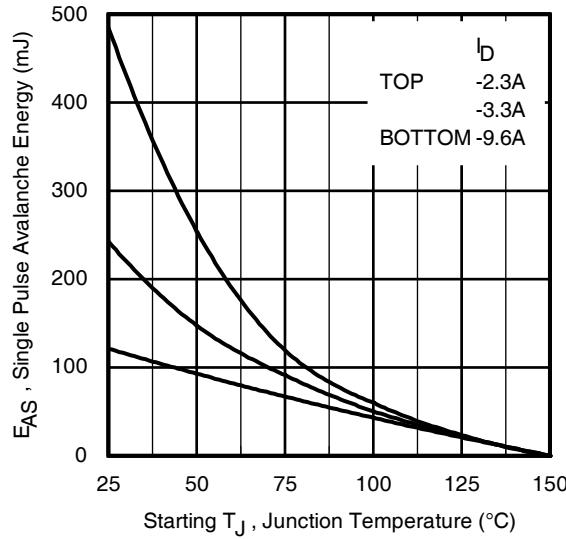


Fig 14. Maximum Avalanche Energy vs. Drain Current

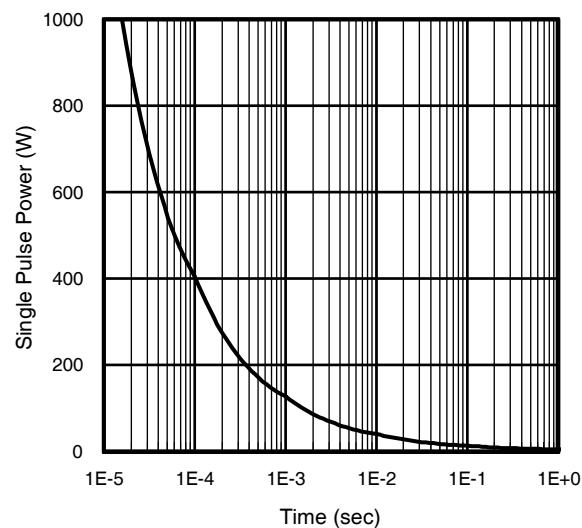
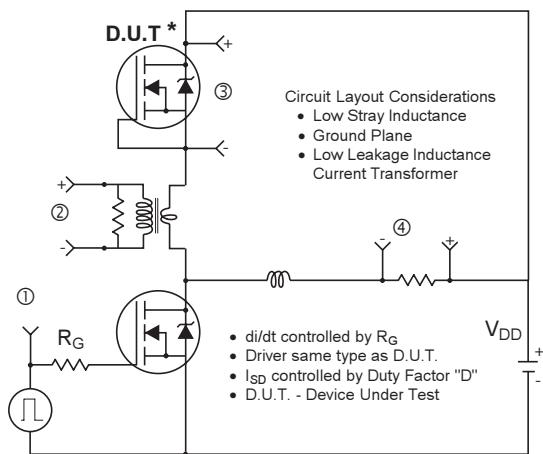


Fig 16. Typical Power vs. Time



\* Reverse Polarity of D.U.T for P-Channel

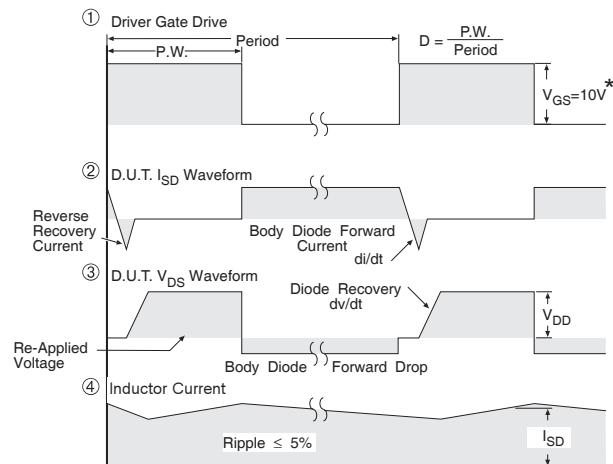
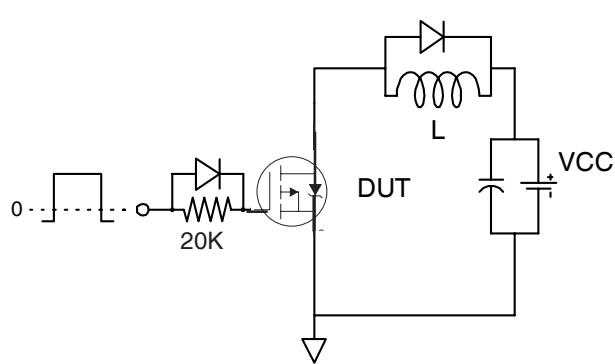
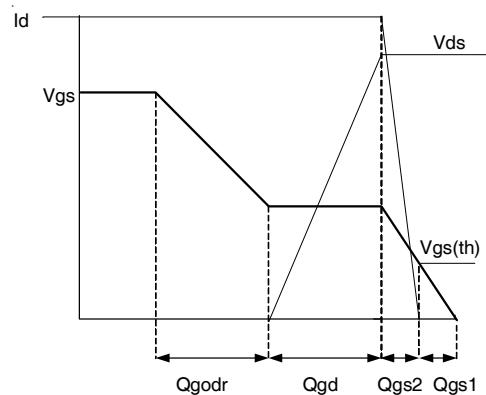


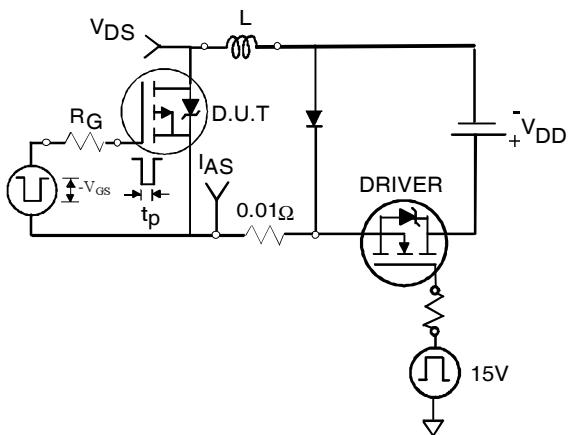
Fig 17. Diode Reverse Recovery Test Circuit for P-Channel HEXFET® Power MOSFETs



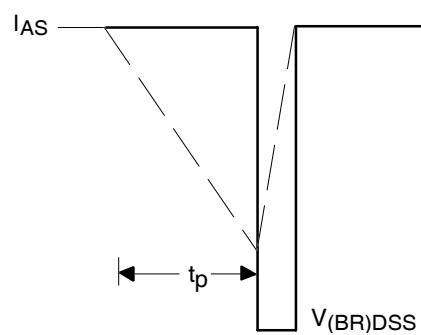
**Fig 18a.** Gate Charge Test Circuit



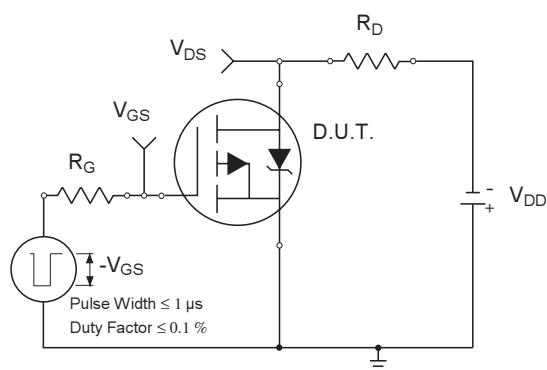
**Fig 18b.** Gate Charge Waveform



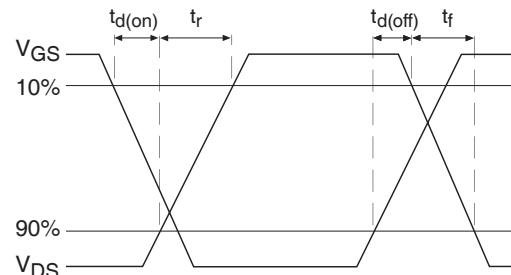
**Fig 19a.** Unclamped Inductive Test Circuit



**Fig 19b.** Unclamped Inductive Waveforms



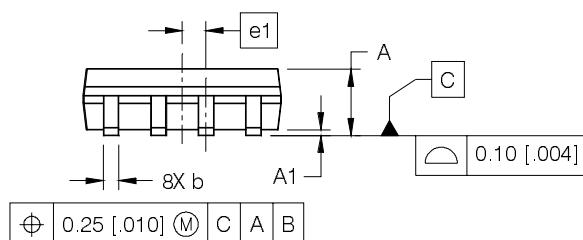
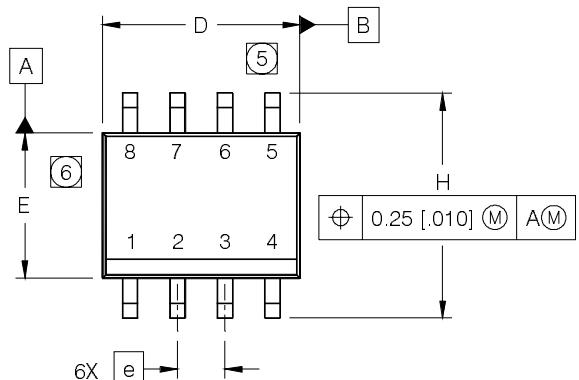
**Fig 20a.** Switching Time Test Circuit



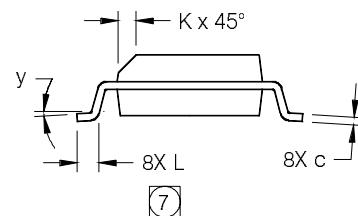
**Fig 20b.** Switching Time Waveforms

## SO-8 Package Outline (Mosfet & Fetky)

Dimensions are shown in millimeters (inches)



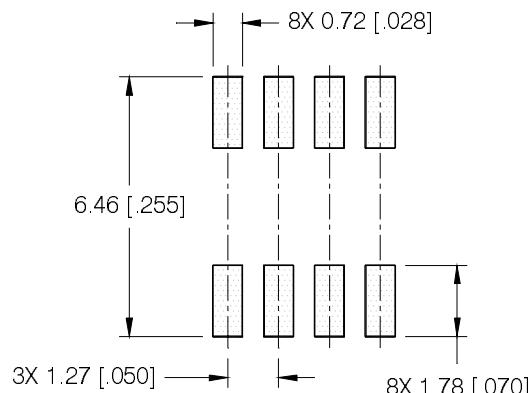
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
c	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
E	.1497	.1574	3.80	4.00
e	.050	BASIC	1.27	BASIC
e 1	.025	BASIC	0.635	BASIC
H	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.050	0.40	1.27
y	0°	8°	0°	8°



### NOTES:

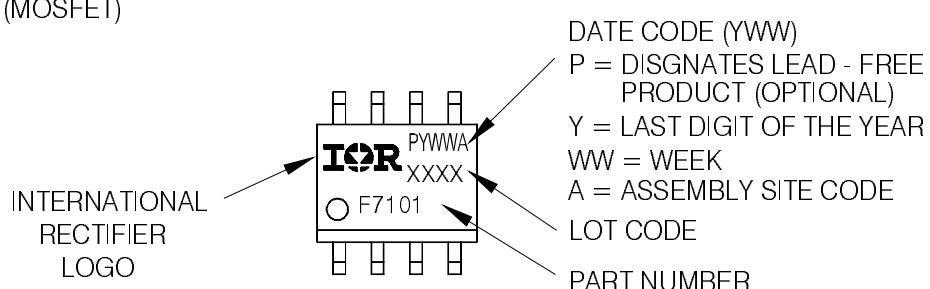
1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: MILLIMETER
3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
- ⑤ DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS.  
MOLD PROTRUSIONS NOT TO EXCEED 0.15 [.006].
- ⑥ DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS.  
MOLD PROTRUSIONS NOT TO EXCEED 0.25 [.010].
- ⑦ DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO  
A SUBSTRATE.

### FOOTPRINT



## SO-8 Part Marking Information

EXAMPLE: THIS IS AN IRF7101 (MOSFET)



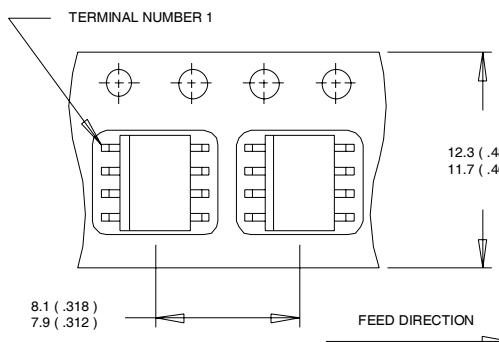
Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>

[www.irf.com](http://www.irf.com)

# IRF9328PbF

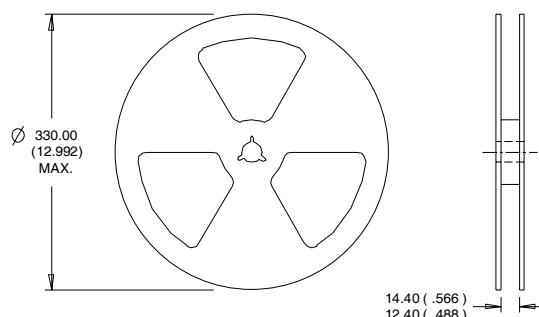
SO-8 Tape and Reel (Dimensions are shown in millimeters (inches))

International  
**IR** Rectifier



NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES :

1. CONTROLLING DIMENSION : MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

## Qualification Information<sup>†</sup>

Qualification level	Consumer <sup>††</sup>	
	(per JEDEC JESD47F <sup>†††</sup> guidelines)	
Moisture Sensitivity Level	SO-8	MSL1 (per JEDEC J-STD-020D <sup>†††</sup> )
RoHS Compliant	Yes	

<sup>†</sup> Qualification standards can be found at International Rectifier's web site  
<http://www.irf.com/product-info/reliability>

<sup>††</sup> Higher qualification ratings may be available should the user have such requirements.  
Please contact your International Rectifier sales representative for further information:  
<http://www.irf.com/whoto-call/salesrep/>

<sup>†††</sup> Applicable version of JEDEC standard at the time of product release.

Data and specifications subject to change without notice.

International  
**IR** Rectifier

IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105  
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[IPS70R2K0CEAKMA1](#) [RJK60S3DPP-E0#T2](#) [RJK60S5DPK-M0#T0](#) [APT5010JVFR](#) [APT12031JFLL](#) [APT12040JVR](#) [DMN3404LQ-7](#)  
[NTE6400](#) [JANTX2N6796U](#) [JANTX2N6784U](#) [JANTXV2N5416U4](#) [SQM110N05-06L-GE3](#) [SIHF35N60E-GE3](#)