PMZB380XN

30 V, single N-channel Trench MOSFET

1 August 2012

Product data sheet

1. Product profile

1.1 General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN1006B-3 (SOT883B) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

1.2 Features and benefits

- Fast switching
- Trench MOSFET technology
- Low threshold voltage
- Ultra thin package profile of 0.37mm height

1.3 Applications

- · Relay driver
- · High-speed line driver
- Low-side loadswitch
- Switching circuits

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{DS}	drain-source voltage	T _j = 25 °C		-	-	30	V
V _{GS}	gate-source voltage			-12	-	12	V
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	-	930	mA
Static characteristics							
R _{DSon}	drain-source on-state resistance	V_{GS} = 4.5 V; I_D = 200 mA; T_j = 25 °C		-	0.38	0.46	Ω

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².





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2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	1 🔲	D
2	S	source	3	
3	D	drain	Transparent top view	G V S
			DFN1006B-3 (SOT883B)	017aaa253

3. Ordering information

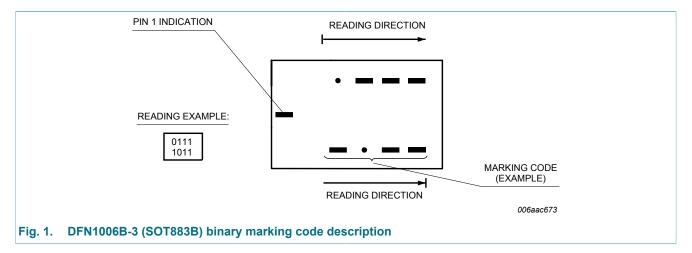
Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMZB380XN	DFN1006B-3	Leadless ultra small plastic package; 3 solder lands; body 1.0 x 0.6 x 0.37 mm	SOT883B			

4. Marking

Table 4. Marking codes

Type number	Marking code
PMZB380XN	0000 1001



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5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	30	V
V_{GS}	gate-source voltage			-12	12	V
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	930	mA
		V _{GS} = 4.5 V; T _{amb} = 100 °C	[1]	-	590	mA
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \mu s$		-	3.7	Α
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	360	mW
			[1]	-	715	mW
		T _{sp} = 25 °C		-	2700	mW
T _j	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain diode						
Is	source current	T _{amb} = 25 °C	[1]	-	670	mA

- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

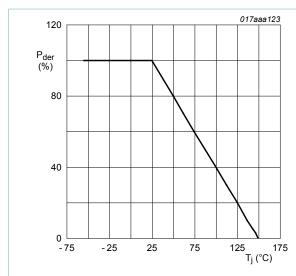


Fig. 2. Normalized total power dissipation as a function of junction temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

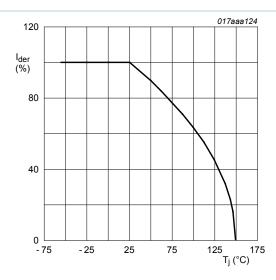


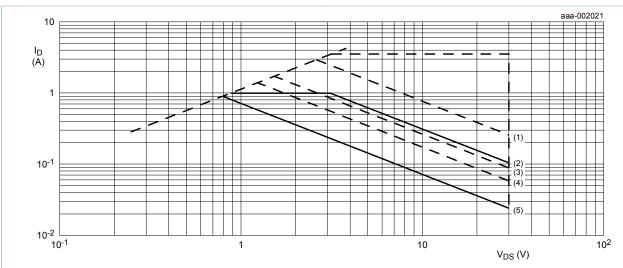
Fig. 3. Normalized continuous drain current as a function of junction temperature

$$I_{der} = \frac{I_D}{I_{D(25^{\circ}C)}} \times 100 \%$$

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I_{DM} is single pulse

- (1) $t_p = 1 \text{ ms}$
- (2) DC; T_{sp} = 25 °C
- (3) $t_p = 10 \text{ ms}$
- $(4) t_p = 100 \text{ ms}$
- (5) DC; $T_{amb} = 25 \, ^{\circ}C$; drain mounting pad 1 cm²

Fig. 4. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drainsource voltage

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)} thermal resistance from junction to ambient	thermal resistance	in free air	[1]	-	305	360	K/W
	_		[2]	-	150	175	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	40	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².

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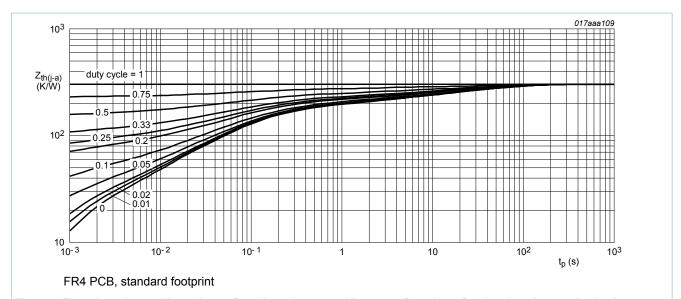


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

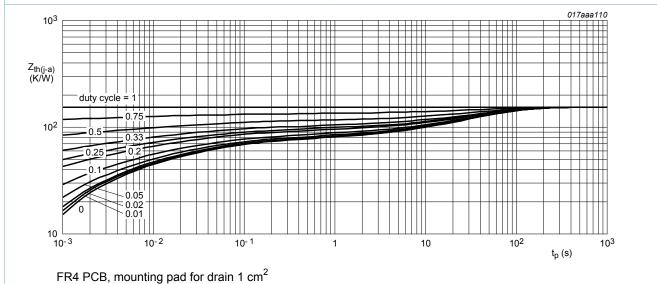


Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

7. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Static characteristics							
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 10 \mu A; V_{GS} = 0 V; T_j = 25 °C$		30	-	-	V
V_{GSth}	gate-source threshold voltage	I_D = 250 μ A; V_{DS} = V_{GS} ; T_j = 25 °C		0.5	1	1.5	V
I _{DSS}	drain leakage current	$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$		-	-	1	μA
		V _{DS} = 30 V; V _{GS} = 0 V; T _j = 150 °C		-	-	100	μA
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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{GSS}	gate leakage current	V _{GS} = 12 V; V _{DS} = 0 V; T _j = 25 °C		-	-	0.1	μΑ
		V_{GS} = -12 V; V_{DS} = 0 V; T_j = 25 °C		-	-	0.1	μΑ
R _{DSon}	drain-source on-state	$V_{GS} = 4.5 \text{ V}; I_D = 200 \text{ mA}; T_j = 25 ^{\circ}\text{C}$		-	0.38	0.46	Ω
	resistance	V_{GS} = 4.5 V; I_{D} = 200 mA; T_{j} = 150 °C		-	0.57	0.7	Ω
		V_{GS} = 2.5 V; I_{D} = 100 mA; T_{j} = 25 °C		-	0.55	0.68	mΩ
g _{fs}	forward transconductance	$V_{DS} = 5 \text{ V}; I_{D} = 200 \text{ mA}; T_{j} = 25 \text{ °C}$		-	1300	-	mS
Dynamic cl	haracteristics				'		_
Q _{G(tot)}	total gate charge	V_{DS} = 15 V; I_D = 1 A; V_{GS} = 4.5 V; T_i = 25 °C		-	0.65	0.87	nC
Q _{GS}	gate-source charge	T _j = 25 °C		-	0.14	-	nC
Q_{GD}	gate-drain charge			-	0.18	-	nC
C _{iss}	input capacitance	V_{DS} = 25 V; f = 1 MHz; V_{GS} = 0 V; T_j = 25 °C		-	37	56	pF
C _{oss}	output capacitance			-	8.6	-	pF
C _{rss}	reverse transfer capacitance			-	5.4	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 15 V; R_L = 15 Ω ; V_{GS} = 4.5 V;		-	6.5	13	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 ^{\circ}C$		-	9.5	-	ns
$t_{d(off)}$	turn-off delay time			-	14	28	ns
t _f	fall time	1		-	5.5	-	ns
Source-dra	in diode				,		
V _{SD}	source-drain voltage	I _S = 300 mA; V _{GS} = 0 V; T _j = 25 °C		-	0.78	1.2	V

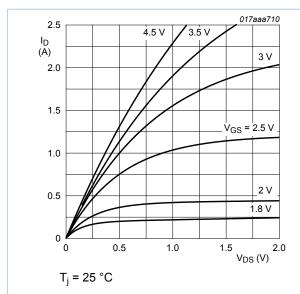


Fig. 7. Output characteristics: drain current as a function of drain-source voltage; typical values

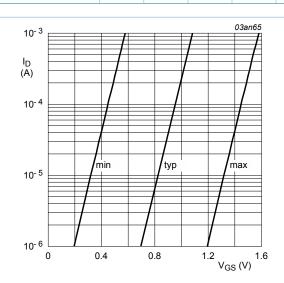


Fig. 8. Subthreshold drain current as a function of gate-source voltage

$$T_j = 25^{\circ}C; V_{DS} = 5V$$

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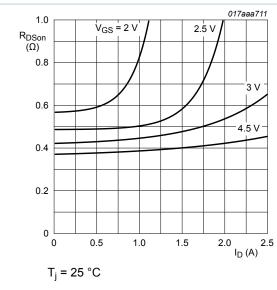


Fig. 9. Drain-source on-state resistance as a function of drain current; typical values

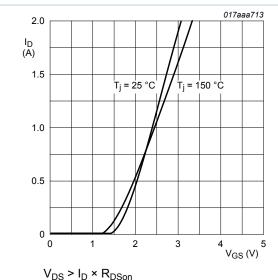


Fig. 11. Transfer characteristics: drain current as a function of gate-source voltage; typical values

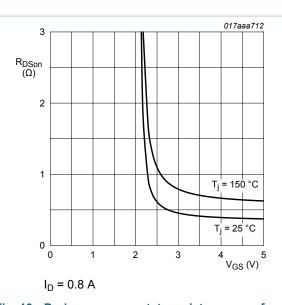


Fig. 10. Drain-source on-state resistance as a function of gate-source voltage; typical values

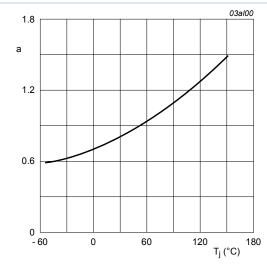


Fig. 12. Normalized drain-source on-state resistance factor as a function of junction temperature

$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$$

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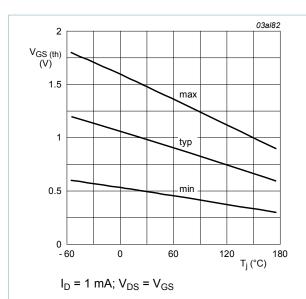


Fig. 13. Gate-source threshold voltage as a function of junction temperature

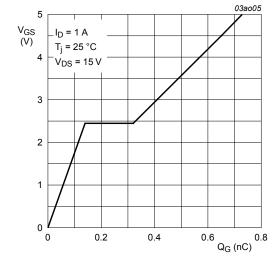


Fig. 15. Gate-source voltage as a function of gate charge; typical values

$$I_D=1A; V_{DS}=15V$$

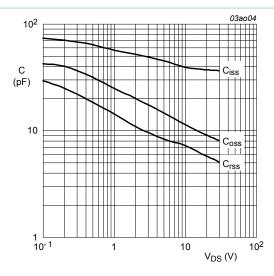


Fig. 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

$$V_{GS}=0V; f=1MHz$$

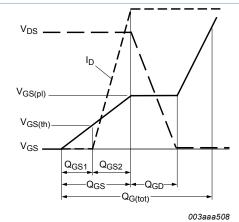
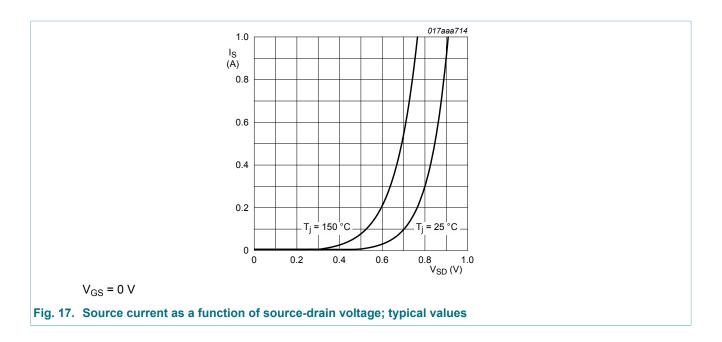


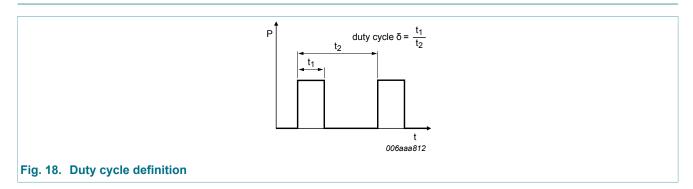
Fig. 16. MOSFET transistor: Gate charge waveform definitions

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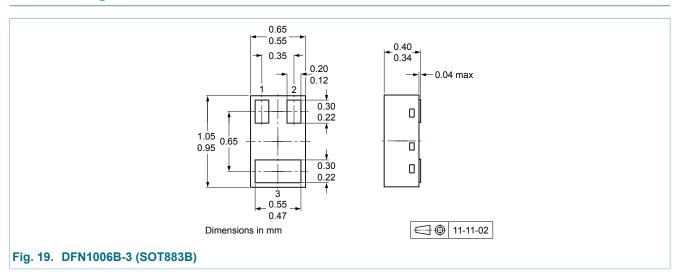
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8. Test information



9. Package outline

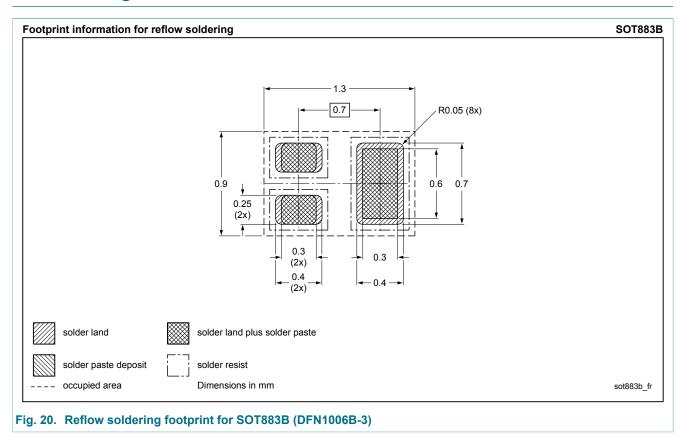


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10. Soldering



11. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMZB380XN v.1	20120801	Product data sheet	-	-

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12. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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