



PMF63UN

20 V, single N-channel Trench MOSFET

Rev. 1 — 22 March 2012

Product data sheet

1. Product profile

1.1 General description

N-channel enhancement mode Field-Effect Transistor (FET) in a SOT323 (SC-70) small Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

1.2 Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology

1.3 Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

1.4 Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|-------------------------------|----------------------------------|--|-----|-----|-----|------------|
| V_{DS} | drain-source voltage | $T_j = 25\text{ °C}$ | - | - | 20 | V |
| V_{GS} | gate-source voltage | | -8 | - | 8 | V |
| I_D | drain current | $V_{GS} = 4.5\text{ V}$; $T_{amb} = 25\text{ °C}$; $t \leq 5\text{ s}$ | [1] | - | 1.9 | A |
| Static characteristics | | | | | | |
| R_{DSon} | drain-source on-state resistance | $V_{GS} = 4.5\text{ V}$; $I_D = 1.8\text{ A}$; $T_j = 25\text{ °C}$ | - | 63 | 74 | m Ω |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

2. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------|-----------------------|------------------|
| 1 | G | gate | <p>SOT323 (SC-70)</p> | <p>017aaa253</p> |
| 2 | S | source | | |
| 3 | D | drain | | |



3. Ordering information

Table 3. Ordering information

| Type number | Package | | Version |
|-------------|---------|--|---------|
| | Name | Description | |
| PMF63UN | SC-70 | plastic surface-mounted package; 3 leads | SOT323 |

4. Marking

Table 4. Marking codes

| Type number | Marking code ^[1] |
|-------------|-----------------------------|
| PMF63UN | V8% |

[1] % = placeholder for manufacturing site code

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

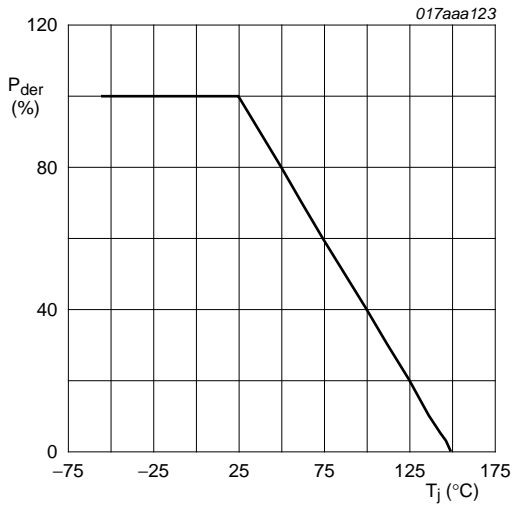
| Symbol | Parameter | Conditions | Min | Max | Unit |
|-----------|-------------------------|--|-----|------|------|
| V_{DS} | drain-source voltage | $T_j = 25\text{ °C}$ | - | 20 | V |
| V_{GS} | gate-source voltage | | -8 | 8 | V |
| I_D | drain current | $V_{GS} = 4.5\text{ V}; T_{amb} = 25\text{ °C}; t \leq 5\text{ s}$ | [1] | 1.9 | A |
| | | $V_{GS} = 4.5\text{ V}; T_{amb} = 25\text{ °C}$ | [1] | 1.8 | A |
| | | $V_{GS} = 4.5\text{ V}; T_{amb} = 100\text{ °C}$ | [1] | 1.1 | A |
| I_{DM} | peak drain current | $T_{amb} = 25\text{ °C};$ single pulse; $t_p \leq 10\text{ }\mu\text{s}$ | - | 7.2 | A |
| P_{tot} | total power dissipation | $T_{amb} = 25\text{ °C}$ | [2] | 275 | mW |
| | | | [1] | 350 | mW |
| | | $T_{sp} = 25\text{ °C}$ | | 1785 | mW |
| T_j | junction temperature | | -55 | 150 | °C |
| T_{amb} | ambient temperature | | -55 | 150 | °C |
| T_{stg} | storage temperature | | -65 | 150 | °C |

Source-drain diode

| | | | | | |
|-------|----------------|--------------------------|-----|-----|---|
| I_S | source current | $T_{amb} = 25\text{ °C}$ | [1] | 0.8 | A |
|-------|----------------|--------------------------|-----|-----|---|

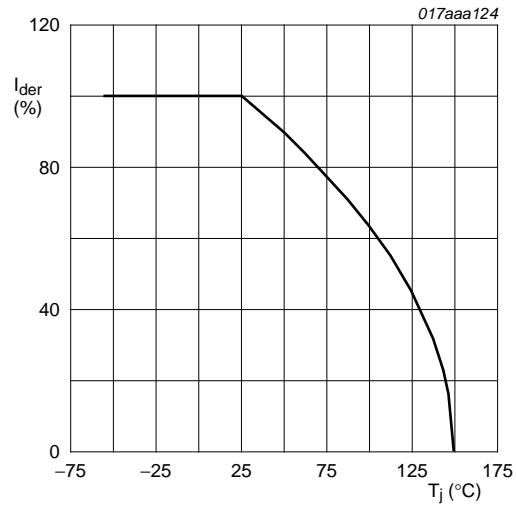
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



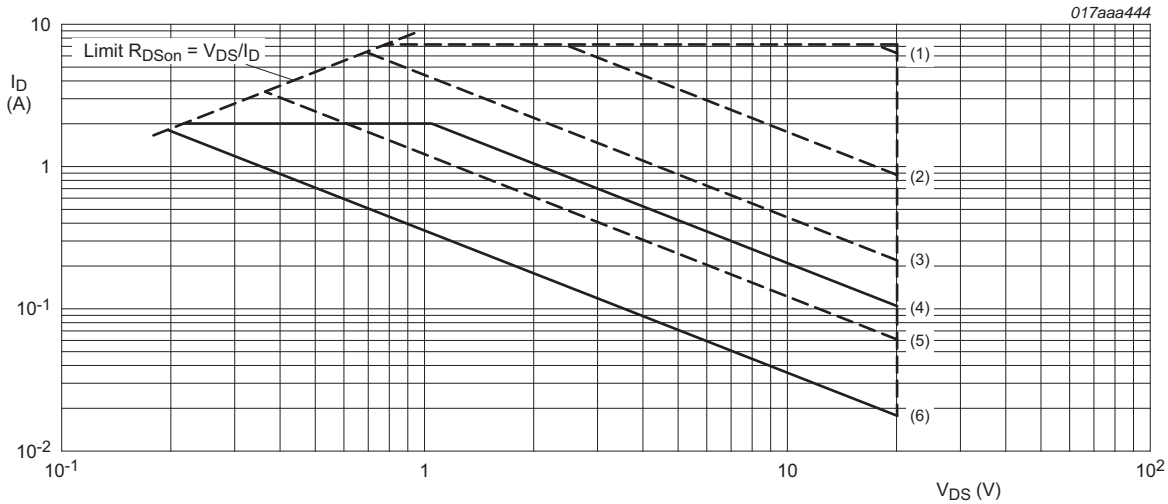
$$P_{der} = \frac{P_{tot}}{P_{tot(25^\circ\text{C})}} \times 100 \%$$

Fig 1. Normalized total power dissipation as a function of junction temperature



$$I_{der} = \frac{I_D}{I_{D(25^\circ\text{C})}} \times 100 \%$$

Fig 2. Normalized continuous drain current as a function of junction temperature



I_{DM} = single pulse

(1) $t_p = 100 \mu\text{s}$

(2) $t_p = 1 \text{ ms}$

(3) $t_p = 10 \text{ ms}$

(4) DC; $T_{sp} = 25^\circ\text{C}$

(5) $t_p = 100 \text{ ms}$

(6) DC; $T_{amb} = 25^\circ\text{C}$; drain mounting pad 6 cm^2

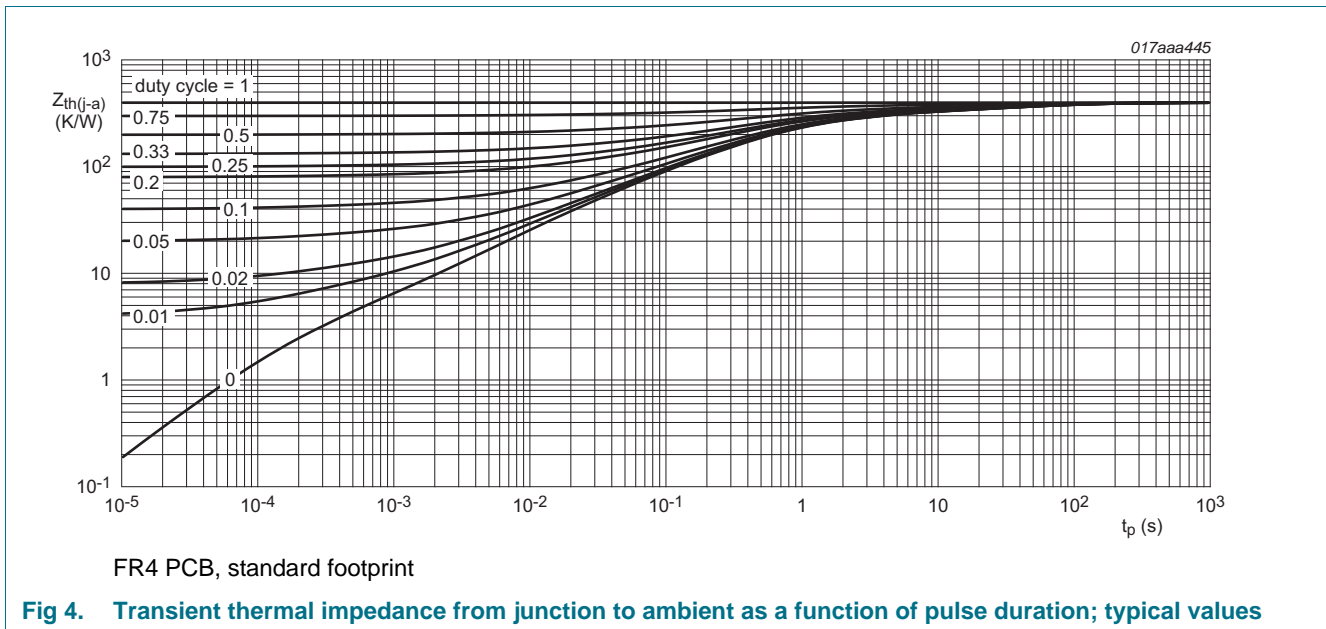
Fig 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

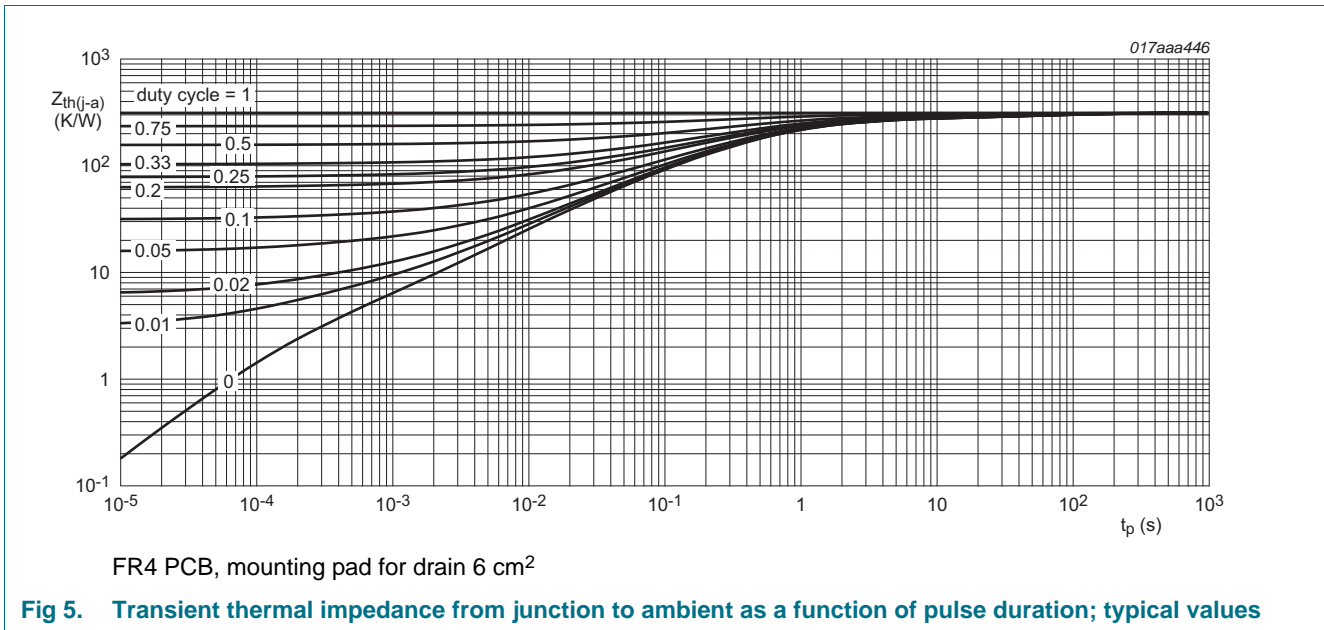
6. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit | |
|----------------|--|-------------|-----|-----|-----|------|-----|
| $R_{th(j-a)}$ | thermal resistance from junction to ambient | in free air | [1] | - | 395 | 455 | K/W |
| | | | [2] | - | 308 | 355 | K/W |
| | | | [3] | - | 263 | 305 | K/W |
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point | | - | 60 | 70 | K/W | |

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm², t ≤ 5 s.

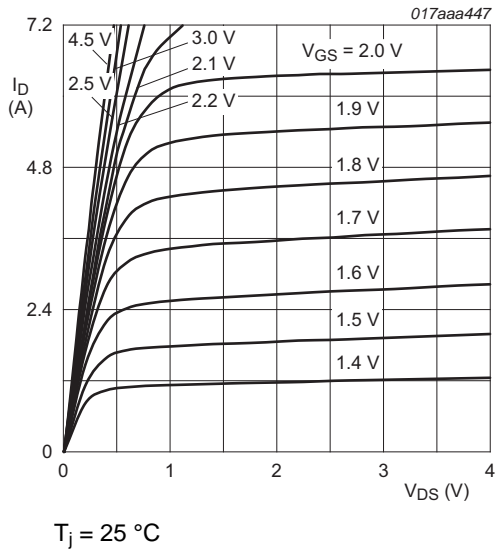




7. Characteristics

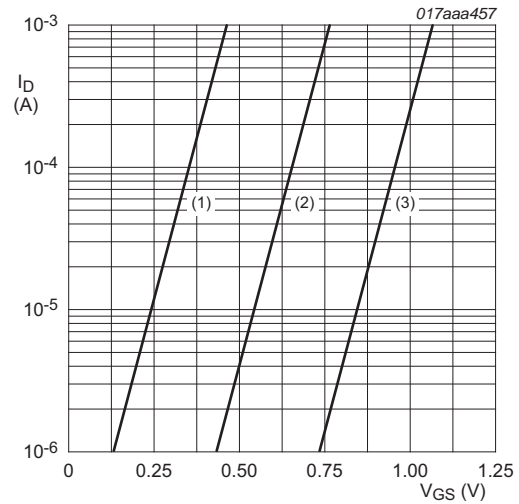
Table 7. Characteristics

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|--------------------------------|----------------------------------|--|-----|------|-----|---------------|
| Static characteristics | | | | | | |
| $V_{(BR)DSS}$ | drain-source breakdown voltage | $I_D = 250 \mu\text{A}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$ | 20 | - | - | V |
| V_{GSth} | gate-source threshold voltage | $I_D = 250 \mu\text{A}; V_{DS} = V_{GS}; T_j = 25 \text{ }^\circ\text{C}$ | 0.4 | 0.7 | 1 | V |
| I_{DSS} | drain leakage current | $V_{DS} = 20 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$ | - | - | 1 | μA |
| | | $V_{DS} = 20 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 150 \text{ }^\circ\text{C}$ | - | - | 20 | μA |
| I_{GSS} | gate leakage current | $V_{GS} = 8 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$ | - | - | 100 | nA |
| | | $V_{GS} = -8 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$ | - | - | 100 | nA |
| R_{DSon} | drain-source on-state resistance | $V_{GS} = 4.5 \text{ V}; I_D = 1.8 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$ | - | 63 | 74 | m Ω |
| | | $V_{GS} = 4.5 \text{ V}; I_D = 1.8 \text{ A}; T_j = 150 \text{ }^\circ\text{C}$ | - | 92 | 108 | m Ω |
| | | $V_{GS} = 2.5 \text{ V}; I_D = 1.6 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$ | - | 77 | 96 | m Ω |
| | | $V_{GS} = 1.8 \text{ V}; I_D = 0.8 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$ | - | 114 | 162 | m Ω |
| g_{fs} | forward transconductance | $V_{DS} = 10 \text{ V}; I_D = 1.8 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$ | - | 8 | - | S |
| Dynamic characteristics | | | | | | |
| $Q_{G(tot)}$ | total gate charge | $V_{DS} = 10 \text{ V}; I_D = 1.8 \text{ A}; V_{GS} = 4.5 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$ | - | 2.2 | 3.3 | nC |
| Q_{GS} | gate-source charge | | - | 0.36 | - | nC |
| Q_{GD} | gate-drain charge | | - | 0.55 | - | nC |
| C_{iss} | input capacitance | $V_{DS} = 10 \text{ V}; f = 1 \text{ MHz}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$ | - | 185 | - | pF |
| C_{oss} | output capacitance | | - | 53 | - | pF |
| C_{rss} | reverse transfer capacitance | | - | 27 | - | pF |
| $t_{d(on)}$ | turn-on delay time | $V_{DS} = 10 \text{ V}; I_D = 1.8 \text{ A}; V_{GS} = 4.5 \text{ V}; R_{G(ext)} = 6 \Omega; T_j = 25 \text{ }^\circ\text{C}$ | - | 8 | - | ns |
| t_r | rise time | | - | 27 | - | ns |
| $t_{d(off)}$ | turn-off delay time | | - | 31 | - | ns |
| t_f | fall time | | - | 17 | - | ns |
| Source-drain diode | | | | | | |
| V_{SD} | source-drain voltage | $I_S = 0.8 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$ | - | 0.8 | 1.2 | V |



$T_j = 25\text{ }^\circ\text{C}$

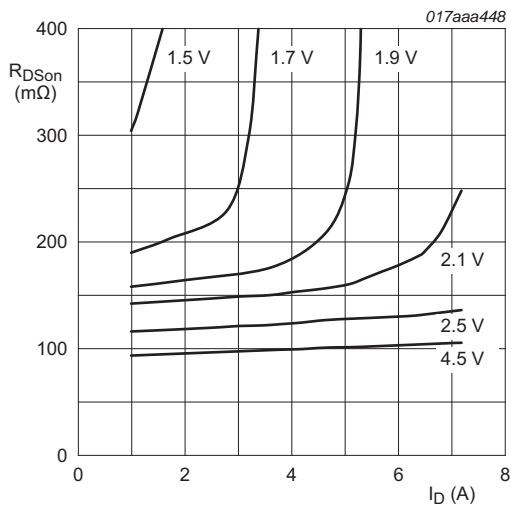
Fig 6. Output characteristics: drain current as a function of drain-source voltage; typical values



$T_j = 25\text{ }^\circ\text{C}; V_{DS} = 5\text{ V}$

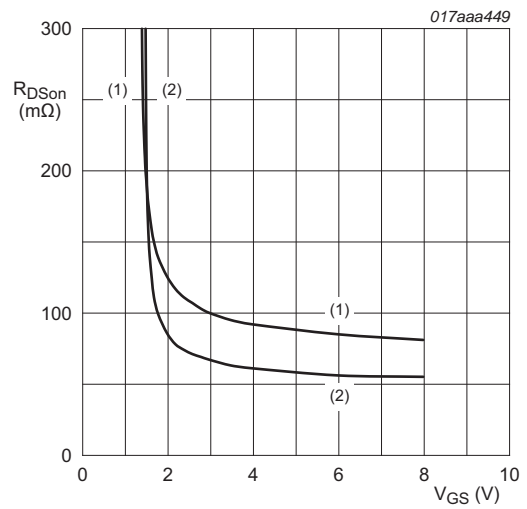
- (1) minimum values
- (2) typical values
- (3) maximum values

Fig 7. Sub-threshold drain current as a function of gate-source voltage



$T_j = 25\text{ }^\circ\text{C}$

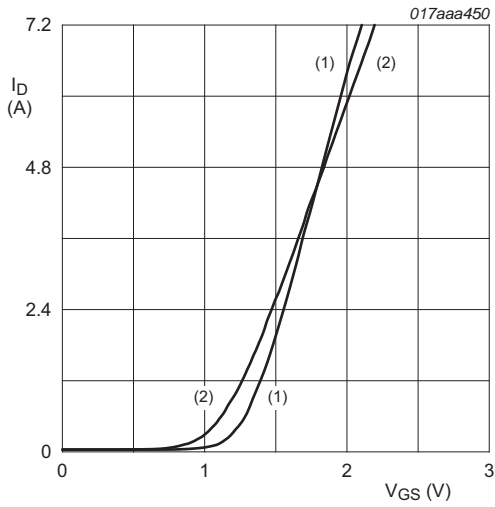
Fig 8. Drain-source on-state resistance as a function of drain current; typical values



$I_D = 1.8\text{ A}$

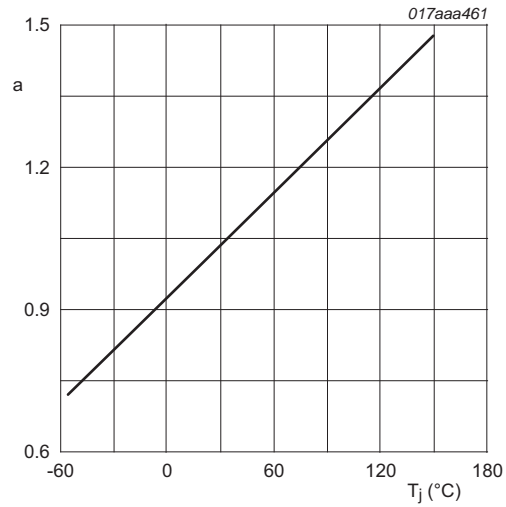
- (1) $T_j = 150\text{ }^\circ\text{C}$
- (2) $T_j = 25\text{ }^\circ\text{C}$

Fig 9. Drain-source on-state resistance as a function of gate-source voltage; typical values



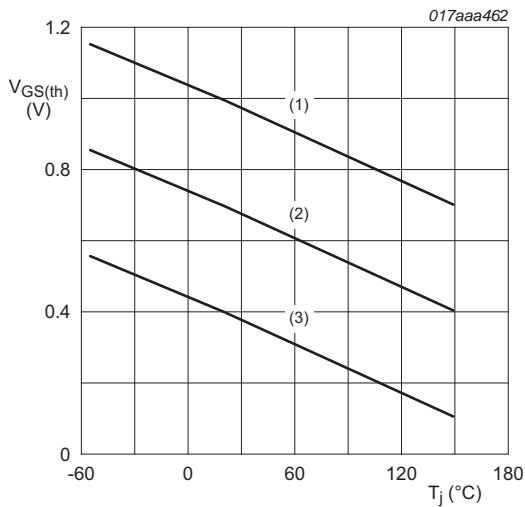
$V_{DS} > I_D \times R_{DS(on)}$
 (1) $T_j = 25\text{ °C}$
 (2) $T_j = 150\text{ °C}$

Fig 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values



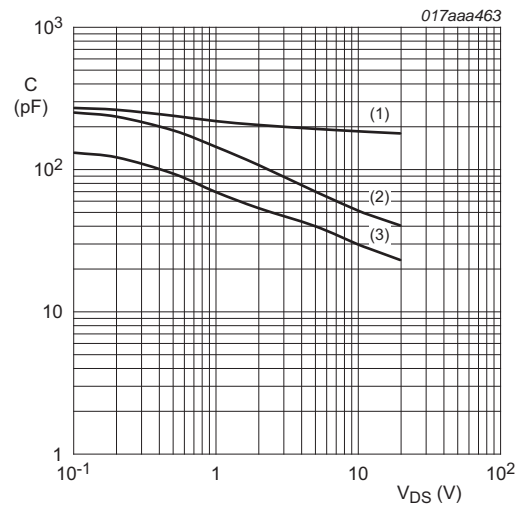
$$a = \frac{R_{DS(on)}}{R_{DS(on)@25^\circ\text{C}}}$$

Fig 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values



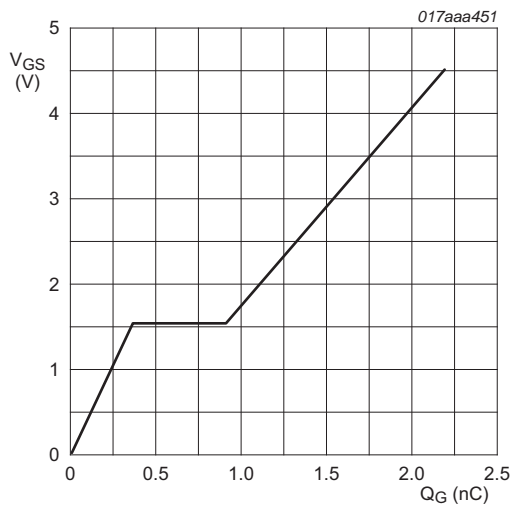
$I_D = 0.25\text{ mA}; V_{DS} = V_{GS}$
 (1) maximum values
 (2) typical values
 (3) minimum values

Fig 12. Gate-source threshold voltage as a function of junction temperature



$f = 1\text{ MHz}; V_{GS} = 0\text{ V}$
 (1) C_{iss}
 (2) C_{oss}
 (3) C_{rss}

Fig 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



$I_D = 1.8$ A; $V_{DS} = 10$ V; $T_{amb} = 25$ °C

Fig 14. Gate-source voltage as a function of gate charge; typical values

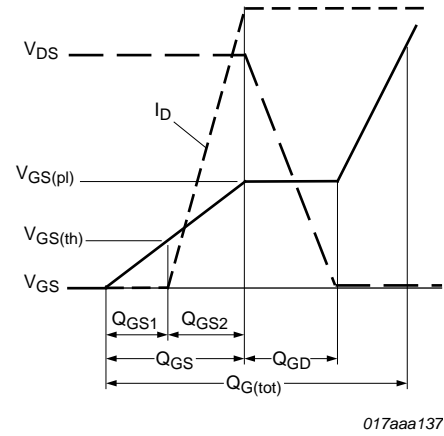
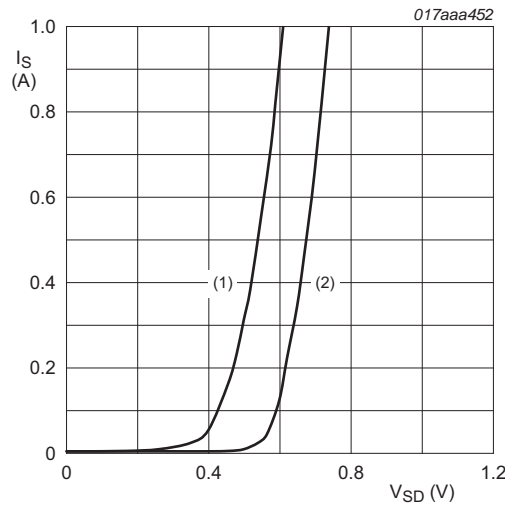


Fig 15. Gate charge waveform definitions



$V_{GS} = 0$ V
 (1) $T_j = 150$ °C
 (2) $T_j = 25$ °C

Fig 16. Source current as a function of source-drain voltage; typical values

8. Test information



Fig 17. Duty cycle definition

9. Package outline

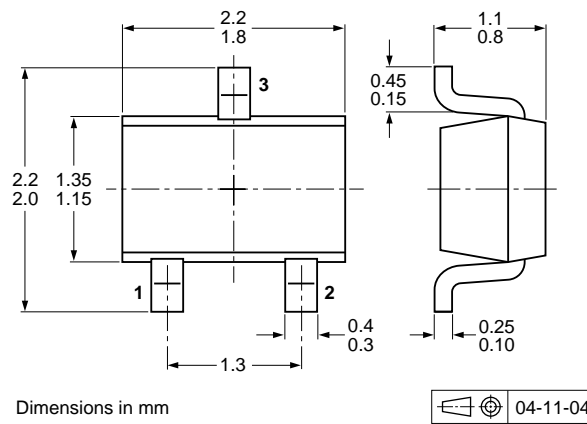


Fig 18. Package outline SOT323 (SC-70)

10. Soldering

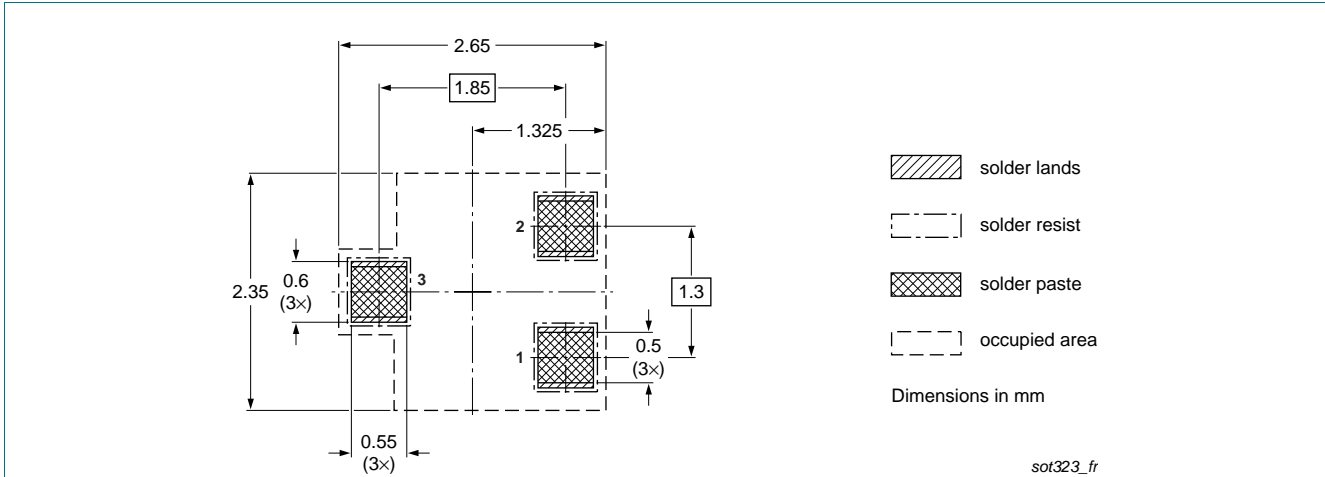


Fig 19. Reflow soldering footprint for SOT323 (SC-70)

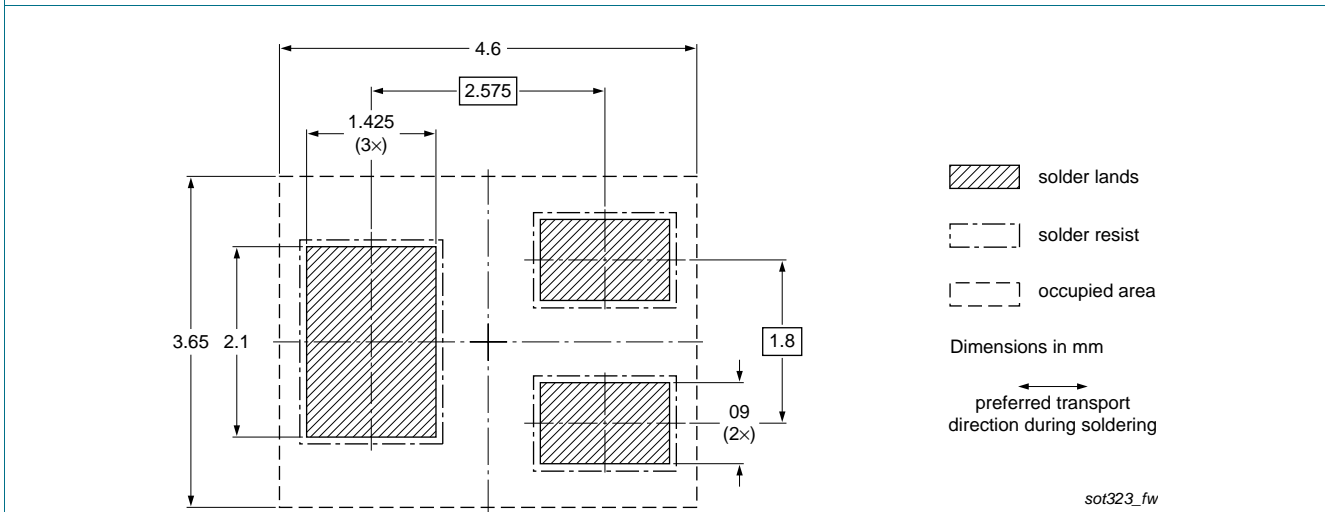


Fig 20. Wave soldering footprint for SOT323 (SC-70)

11. Revision history

Table 8. Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|-------------|--------------|--------------------|---------------|------------|
| PMF63UN v.1 | 20120322 | Product data sheet | - | - |

12. Legal information

12.1 Data sheet status

| Document status ^[1] [2] | Product status ^[3] | Definition |
|------------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
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14. Contents

| | | |
|-----------|--|-----------|
| 1 | Product profile | 1 |
| 1.1 | General description | 1 |
| 1.2 | Features and benefits | 1 |
| 1.3 | Applications | 1 |
| 1.4 | Quick reference data | 1 |
| 2 | Pinning information | 1 |
| 3 | Ordering information | 2 |
| 4 | Marking | 2 |
| 5 | Limiting values | 2 |
| 6 | Thermal characteristics | 4 |
| 7 | Characteristics | 6 |
| 8 | Test information | 10 |
| 9 | Package outline | 10 |
| 10 | Soldering | 11 |
| 11 | Revision history | 12 |
| 12 | Legal information | 13 |
| 12.1 | Data sheet status | 13 |
| 12.2 | Definitions | 13 |
| 12.3 | Disclaimers | 13 |
| 12.4 | Trademarks | 14 |
| 13 | Contact information | 14 |

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[MCIMX6SX-SDB](#) [74ALVC125BQ,115](#) [74HC4050N](#) [74HC4514N](#) [MK21FN1M0AVLQ12](#) [MKV30F128VFM10](#) [FRDM-K66F](#) [FRDM-](#)
[KW40Z](#) [FRDM-MC-LVBLDC](#) [PESD18VF1BSFYL](#) [PMF63UNEX](#) [PSMN4R0-60YS,115](#) [HEF4028BPN](#) [RAPPID-567XFSW](#)
[MPC565MVR56](#) [MPC574XG-176DS](#) [MPC8548VJAUJD](#) [MPC860PCVR66D4](#) [BCV61A,215](#) [BFU520XAR](#) [BT137-600E](#) [BT137S-600D.115](#)
[BT138-600E.127](#) [BT139X-600.127](#) [BT258-600R.127](#) [BUK7628-100A118](#) [BUK765R0-100E.118](#) [P5020NSE7VNB](#) [S12ZVML12EVBLIN](#)
[SCC2692AC1N40](#) [LPC1785FBD208K](#) [LPC2124FBD64/01](#) [LS1020ASN7KQB](#) [LS1020AXN7HNB](#) [LS1020AXN7KQB](#)