

100 V, 3 A low leakage current Schottky barrier rectifier7 May 2015Product data sheet

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

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- Extremely low leakage current I_R = 110 nA
- Reverse voltage: V_R ≤ 100 V
- Average forward current: $I_{F(AV)} \le 3 A$
- High power capability due to clip-bonding technology
- High temperature T_i ≤ 175 °C
- Small and flat lead SMD plastic package
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

4. Quick reference data

Table 1. Quie	ck reference data					
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; T _{sp} ≤ 160 °C; square wave	-	-	3	A
V _R	reverse voltage	T _j = 25 °C	-	-	100	V
V _F	forward voltage	I_F = 3 A; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C	-	710	770	mV
I _R	reverse current	V_R = 100 V; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; T_j = 25 °C	-	110	450	nA





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5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	К	cathode[1]		1 - 2
2	A	anode		sym001

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering inf	formation		
Type number	Package		
	Name	Description	Version
PMEG10030ELP	SOD128	plastic surface-mounted package; 2 leads	SOD128

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG10030ELP	DJ

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	100	V
l _F	forward current	T _{sp} = 155 °C; δ = 1		-	4.2	А
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; T _{amb} ≤ 55 °C; square wave	[1]	-	3	A
		δ = 0.5; f = 20 kHz; T _{sp} ≤ 160 °C; square wave		-	3	A
I _{FSM}	non-repetitive peak forward current	t_p = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	70	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[2]	-	750	mW
			[3]	-	1250	mW
			[1]	-	2500	mW
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

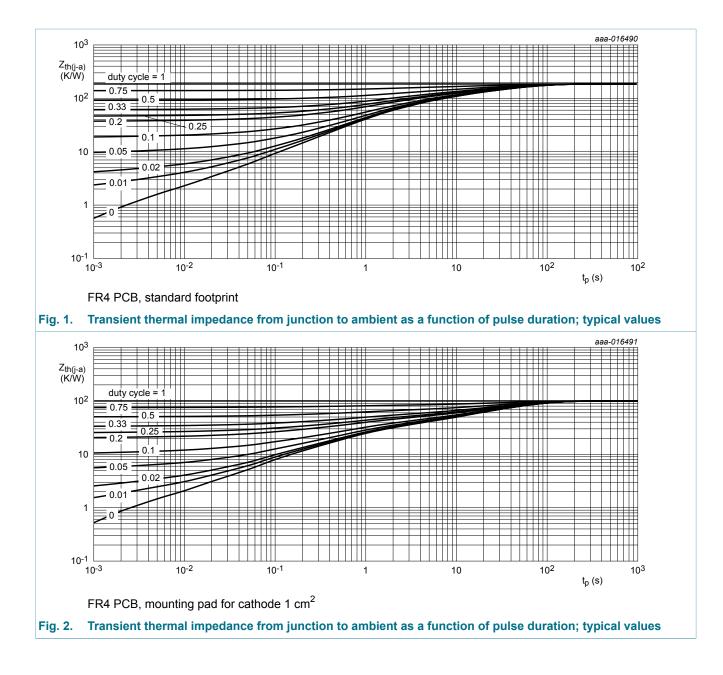
Table 6. Thermal characteristics							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1][2]	-	-	200	K/W
			[1][3]	-	-	120	K/W
			[1][4]	-	-	60	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[5]	-	-	12	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- ^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [4] Device mounted on a ceramic PCB, $\tilde{A}I_2O_3$, standard footprint.
- [5] Soldering point of cathode tab.

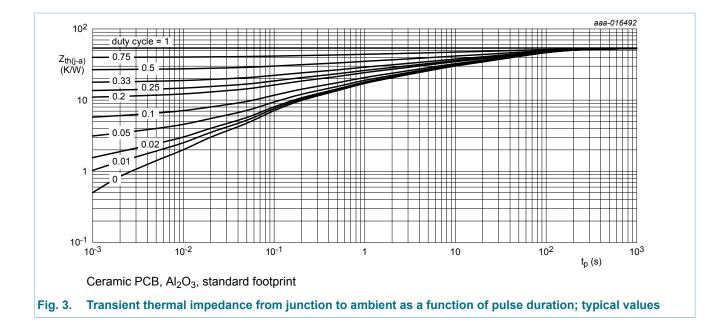
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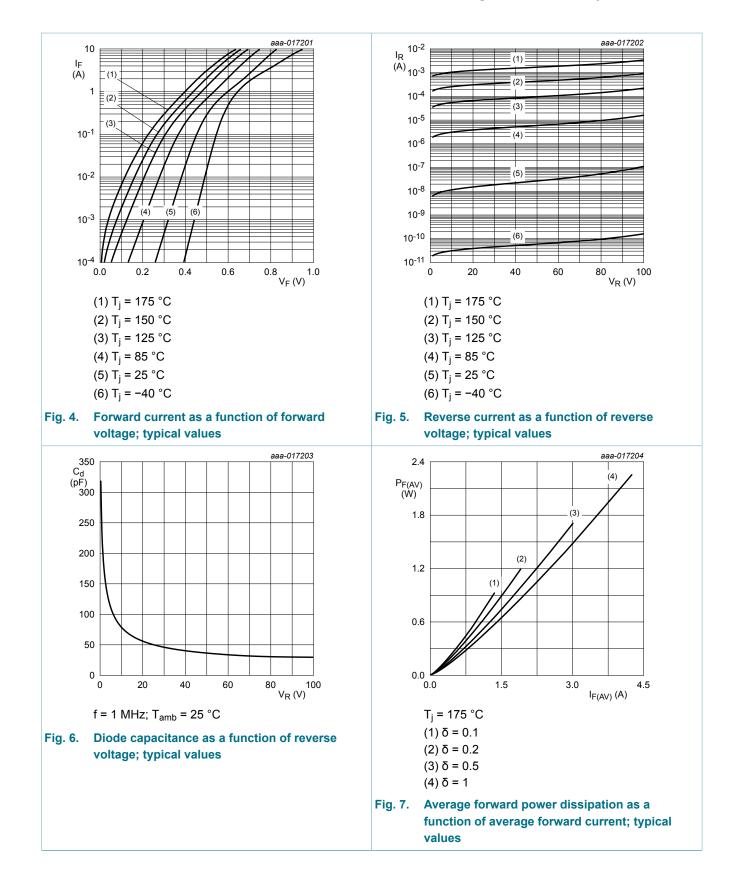
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10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	I _R = 1 mA; T _j = 25 °C; t _p = 300 μs; δ = 0.02	100	-	-	V
V _F	forward voltage	I_F = 0.1 A; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C	-	455	510	mV
		I _F = 0.5 A; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C	-	535	605	mV
		$I_F = 0.7$ A; $t_p \le 300$ μs; δ ≤ 0.02 ; $T_j = 25$ °C	-	565	640	mV
		I _F = 1 A; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C	-	600	670	mV
		I_F = 1.6 A; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C	-	645	720	mV
		I_F = 2 A; $t_p \le 300$ μs; δ ≤ 0.02 ; T_j = 25 °C	-	670	740	mV
		$I_F = 3 \text{ A}; t_p \le 300 \text{ μs}; \delta \le 0.02;$ $T_j = 25 \text{ °C}$	-	710	770	mV
		I _F = 3 A; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 125 °C	-	575	680	mV
I _R reverse curr	reverse current	V_R = 10 V; $t_p \le 300 \ \mu$ s; $\delta \le 0.02$; T _j = 25 °C	-	15	-	nA
		V_R = 60 V; $t_p \le 300 \ \mu$ s; $\delta \le 0.02$; T _j = 25 °C	-	35	-	nA
		V_R = 100 V; $t_p \le 300 \ \mu$ s; $\delta \le 0.02$; T _j = 25 °C	-	110	450	nA
		V_R = 100 V; $t_p \le 300 \ \mu$ s; $\delta \le 0.02$; T _j = 125 °C	-	220	1500	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	200	-	pF
		V _R = 4 V; f = 1 MHz; T _j = 25 °C	-	120	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C	-	78	-	pF
rr	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$	-	8	-	ns
V _{FRM}	peak forward recovery voltage	$I_F = 0.5 \text{ A}; T_j = 25 \text{ °C}; dI_F/dt = 20 \text{ A}/\mu\text{s}$	-	580	-	mV

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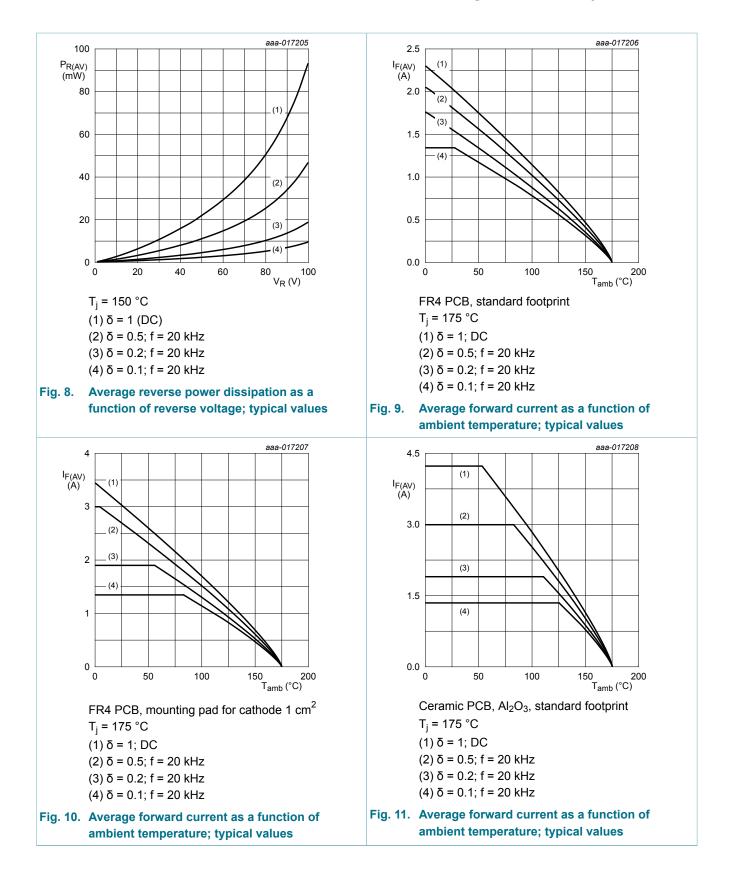


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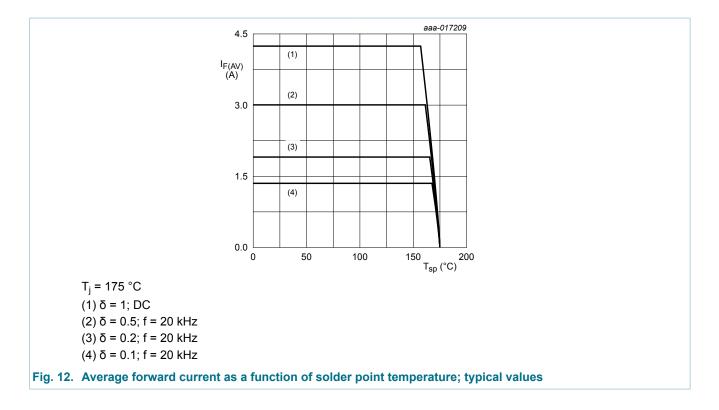
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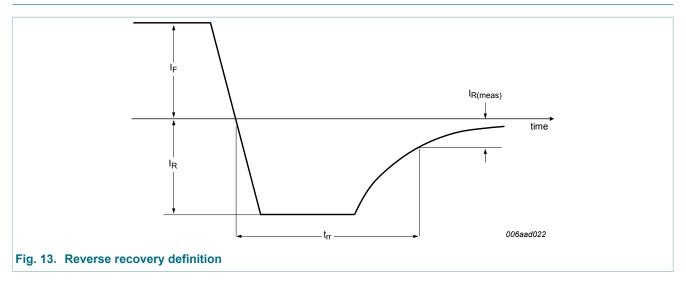


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11. Test information

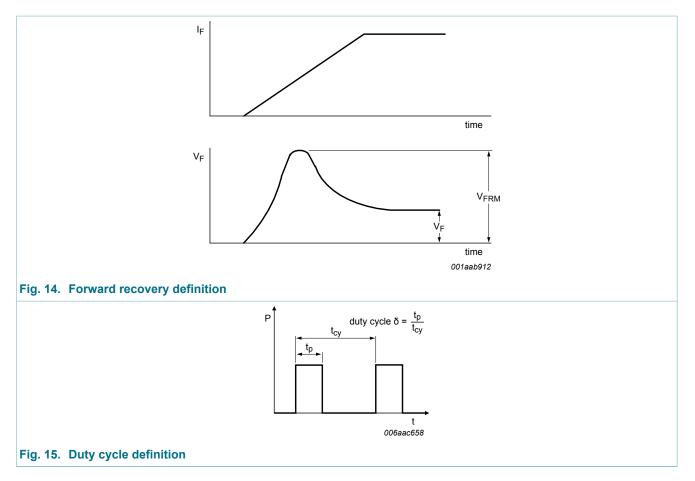


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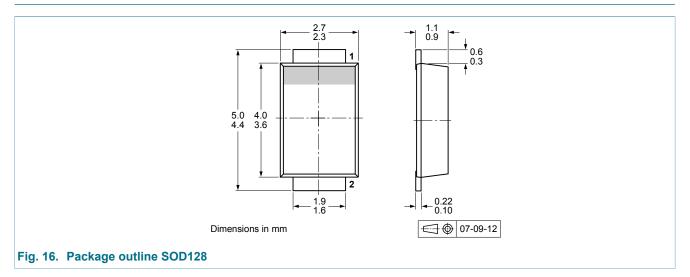
The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

11.1 Quality information

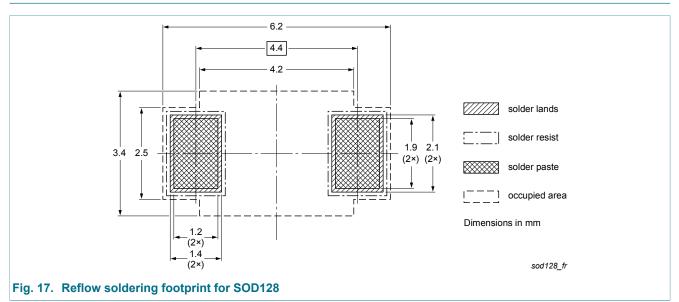
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

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12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision his	story			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG10030ELP v.2	20150507	Product data sheet	-	PMEG10030ELP v.1
Modifications:	Product status char	nged		
PMEG10030ELP v.1	20150323	Preliminary data sheet	-	-

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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