



Description

- Standard 7 x 5mm crystal oscillator
Ceramic package with a seam sealed metal lid, hermetically sealed
- Model CFPS-73
- Model Issue number 12

Frequency Parameters

- Frequency 48.0MHz
- Frequency Tolerance @ 25°C INCLUSIVE
- Frequency Stability ±50.00ppm
- Operating Temperature Range 0.00 to 70.00°C
- Ageing ±3ppm per year max

Electrical Parameters

- Supply Voltage 3.3V
- Supply Voltage Tolerance ±10%
- Current Draw 20mA max

Output Details

- Output Compatibility HCMOS
- Output Load 50pF max
- Rise and Fall time 10ns max
- Duty Cycle 45/55%

Output Control

- Logic '1' (>70% VS) to pad 1 enables oscillator output
- Logic '0' (<30% VS) to pad 1 disables oscillator output; the oscillator output goes to the high impedance state
- No connection to pad 1 enables oscillator output
- Standby Current: 10µA max

Noise Parameters

- Phase Noise @125MHz (typical):
 - 10Hz -54dBc
 - 100Hz -92dBc
 - 1kHz -130dBc
 - 10kHz -149dBc
 - 100kHz -159dBc
 - 1MHz -163dBc
- RMS Phase Jitter @125MHz: 52fs typical

Environmental Parameters

- Shock: MIL-STD-202, Method 213, Condition E
- Vibration: MIL-STD-883, Method 2007, Condition A
- Storage Temperature Range: -55 to 125°C

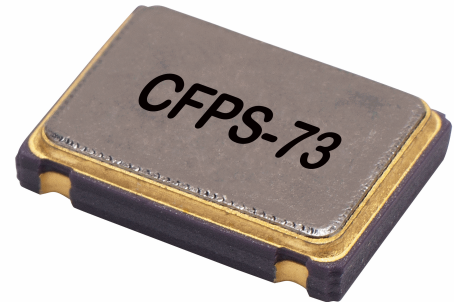
Compliance

- RoHS Status Compliant
- REACH Status Compliant
- MSL Rating (JDEC-STD-033): Not Applicable

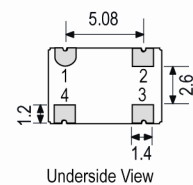
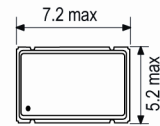
Packaging

- Pack Type: Cutt In tape, cut from a reel
- Pack Size 10
- Alternative packing option available

This document is correct at the time of printing; please contact your local office for the latest version.

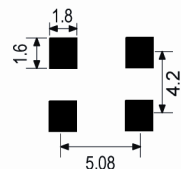


Outline (mm)

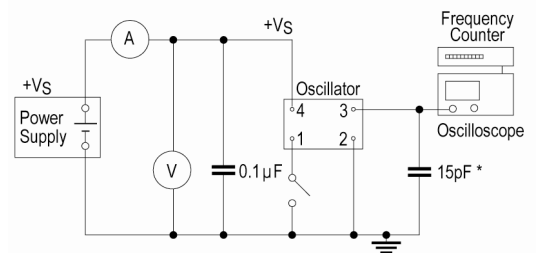


- Pad Connections
- Standby Operation
 - GND
 - Output
 - +Vs

Solder Pad Layout



Test Circuit



* Inclusive of jiggging and equipment capacitance

Sales Office Contact Details:

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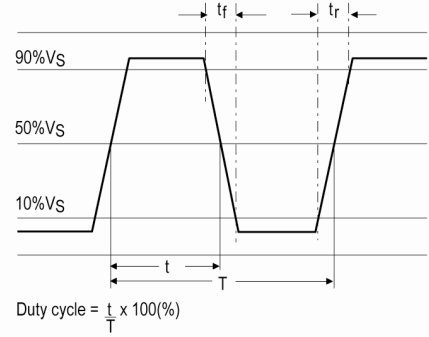
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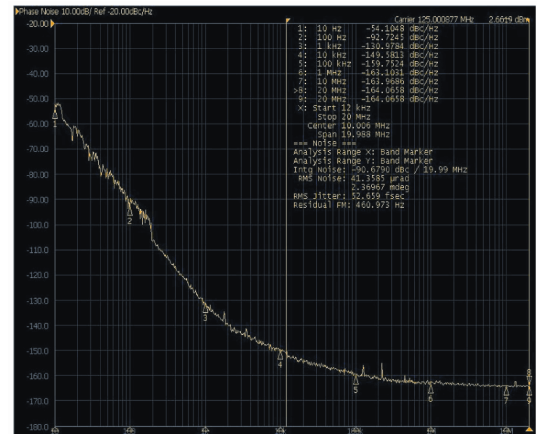
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Wave Form



Phase Noise / Jitter



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