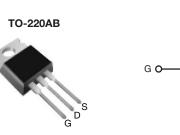


**Vishay Siliconix** 

### **Power MOSFET**

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	400				
R <sub>DS(on)</sub> (Ω)	V <sub>GS</sub> = 10 V 1.0				
Q <sub>g</sub> (Max.) (nC)	38				
Q <sub>gs</sub> (nC)	5.7				
Q <sub>gd</sub> (nC)	22				
Configuration	Single				



S N-Channel MOSFET

#### **FEATURES**

- Dynamic dV/dt Rating
- Repetitive Avalanche Rated
- · Fast Switching
- · Ease of Paralleling
- Simple Drive Requirements
- Compliant to RoHS Directive 2002/95/EC

#### DESCRIPTION

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION			
Package	TO-220AB		
Lead (Pb)-free	IRF730PbF		
	SiHF730-E3		
SnPb	IRF730		
	SiHF730		

ABSOLUTE MAXIMUM RATINGS ( $T_{\rm C}$	– 25 O, um					
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V <sub>DS</sub>	400	v	
Gate-Source Voltage			V <sub>GS</sub>	± 20	v	
Continuous Drain Current	V <sub>GS</sub> at 10 V	T <sub>C</sub> = 25 °C	l_	5.5		
	VGS at TO V	T <sub>C</sub> = 100 °C	I <sub>D</sub>	3.5	А	
Pulsed Drain Current <sup>a</sup>			I <sub>DM</sub>	22		
Linear Derating Factor				0.59	W/°C	
Single Pulse Avalanche Energy <sup>b</sup>			E <sub>AS</sub>	290	mJ	
Repetitive Avalanche Current <sup>a</sup>			I <sub>AR</sub>	5.5	A	
Repetitive Avalanche Energy <sup>a</sup>			E <sub>AR</sub>	7.4	mJ	
Maximum Power Dissipation $T_{C} = 25 \text{ °C}$			P <sub>D</sub>	74	W	
Peak Diode Recovery dV/dt <sup>c</sup>			dV/dt	4.0	V/ns	
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 150	°C	
Soldering Recommendations (Peak Temperature)	for <sup>-</sup>	10 s		300 <sup>d</sup>		
Mounting Torque	6-32 or M3 screw			10	lbf ∙ in	
Mounting Torque				1.1	N · m	

#### Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

k. V<sub>DD</sub> = 50 V, starting T<sub>J</sub> = 25 °C, L = 16 mH, R<sub>g</sub> = 25  $\Omega$ , I<sub>AS</sub> = 5.5 A (see fig. 12). c. I<sub>SD</sub>  $\leq$  5.5 A, dI/dt  $\leq$  90 A/µs, V<sub>DD</sub>  $\leq$  V<sub>DS</sub>, T<sub>J</sub>  $\leq$  150 °C. d. 1.6 mm from case.

\* Pb containing terminations are not RoHS compliant, exemptions may apply

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THERMAL RESISTANCE RATI	NGS							
PARAMETER	SYMBOL	TYP.		MAX.		UNIT		
Maximum Junction-to-Ambient	R <sub>thJA</sub>	- 62 0.50 - - 1.7						
Case-to-Sink, Flat, Greased Surface	R <sub>thCS</sub>					°C/W		
Maximum Junction-to-Case (Drain)	R <sub>thJC</sub>							
<b>SPECIFICATIONS</b> ( $T_J = 25 \ ^{\circ}C$ , u	Inless otherw	ise noted)						
PARAMETER	SYMBOL	TEST	CONDITI	ONS	MIN.	TYP.	MAX.	UNIT
Static							•	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0	0 V, I <sub>D</sub> = 2	50 µA	400	-	-	V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	to 25 °C,	l <sub>D</sub> = 1 mA	-	0.54	-	V/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V$	/ <sub>GS</sub> , I <sub>D</sub> = 2	50 µA	2.0	-	4.0	V
Gate-Source Leakage	I <sub>GSS</sub>	Vo	GS = ± 20 \	/	-	-	± 100	nA
7		V <sub>DS</sub> = 400 V, V <sub>GS</sub> = 0 V		-	-	25		
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 320 V,	V <sub>DS</sub> = 320 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C		-	-	250	μA
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub>	= 3.3 A <sup>b</sup>	-	-	1.0	Ω
Forward Transconductance	9 <sub>fs</sub>	V <sub>DS</sub> = 5	50 V, I <sub>D</sub> = 3	3.3 A <sup>b</sup>	2.9	-	-	S
Dynamic		•						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1.0 MHz, see fig. 5		-	700	-		
Output Capacitance	C <sub>oss</sub>			-	170	-	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			-	64	-		
Total Gate Charge	Qg				-	-	38	<u> </u>
Gate-Source Charge	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	$V_{GS} = 10 \text{ V} \qquad \begin{array}{c} I_D = 3.5 \text{ A}, V_{DS} = 320 \text{ V},\\ \text{see fig. 6 and } 13^{\text{b}} \end{array}$		-	-	5.7	nC
Gate-Drain Charge	Q <sub>gd</sub>				-	-	22	
Turn-On Delay Time	t <sub>d(on)</sub>		1		-	10	-	1
Rise Time	t <sub>r</sub>	$V_{DD}$ = 200 V, I <sub>D</sub> = 3.5 A R <sub>g</sub> = 12 $\Omega$ , R <sub>D</sub> = 57 $\Omega$ , see fig. 10 <sup>b</sup>		-	15	-	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>			-	38	-		
Fall Time	t <sub>f</sub>			-	14	-		
Internal Drain Inductance	L <sub>D</sub>	Between lead, 6 mm (0.25") from package and center of die contact		-	4.5	-		
Internal Source Inductance	L <sub>S</sub>			-	7.5	-	nH	
Drain-Source Body Diode Characteristic	cs							
Continuous Source-Drain Diode Current	١ <sub>S</sub>	MOSFET symbol showing the integral reverse p - n junction diode		-	-	5.5	А	
Pulsed Diode Forward Current <sup>a</sup>	I <sub>SM</sub>			-	-	22		
Body Diode Voltage	$V_{SD}$	T <sub>J</sub> = 25 °C, I	<sub>S</sub> = 5.5 A,	$V_{GS} = 0 V^{b}$	-	-	1.6	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	$T_{\rm J} = 25 \ ^{\circ}\text{C}, I_{\rm F} = 3.5 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}^{ m b}$		-	270	530	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			-	1.8	2.2	μC	
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn			-on is dor	ninated b	$v L_s and$	L <sub>D</sub> )

#### Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b. Pulse width  $\leq$  300 µs; duty cycle  $\leq$  2 %.

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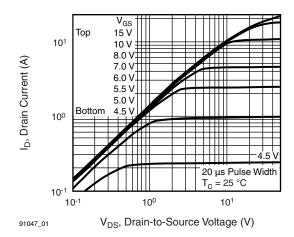


Fig. 1 - Typical Output Characteristics,  $T_C = 25 \ ^{\circ}C$ 

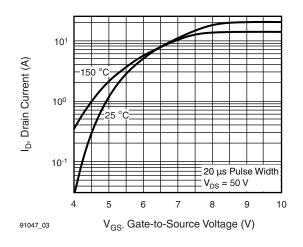


Fig. 3 - Typical Transfer Characteristics

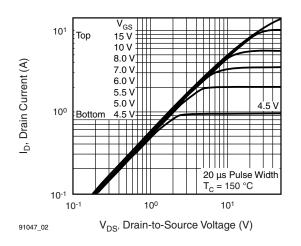


Fig. 2 - Typical Output Characteristics,  $T_C = 150 \ ^{\circ}C$ 

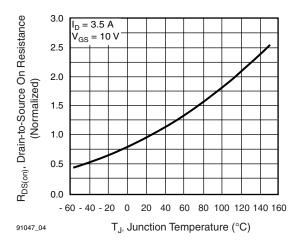


Fig. 4 - Normalized On-Resistance vs. Temperature

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3

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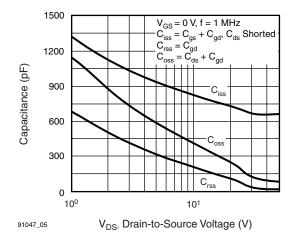
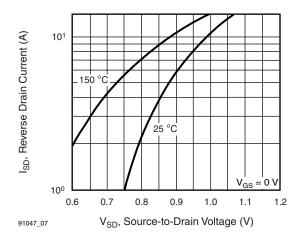
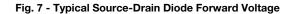


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage





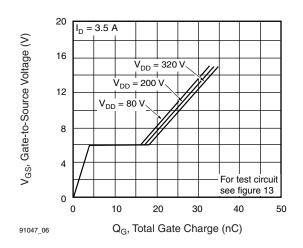


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

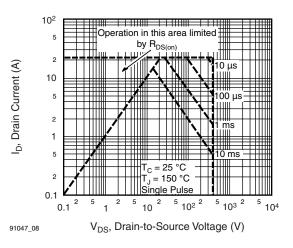


Fig. 8 - Maximum Safe Operating Area

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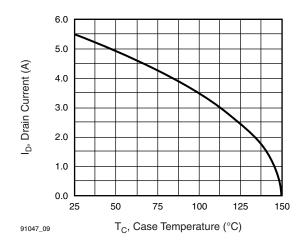


Fig. 9 - Maximum Drain Current vs. Case Temperature

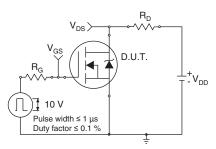


Fig. 10a - Switching Time Test Circuit

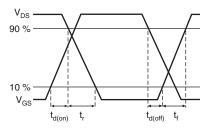


Fig. 10b - Switching Time Waveforms

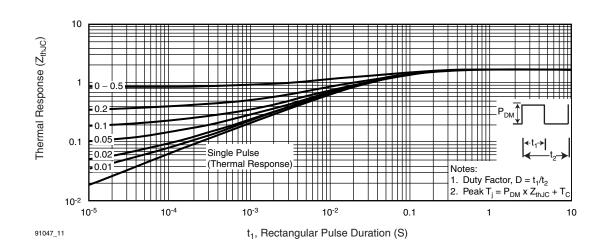


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

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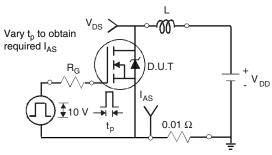


Fig. 12a - Unclamped Inductive Test Circuit

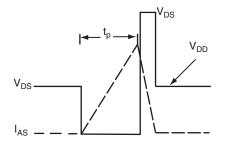
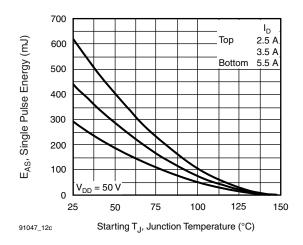
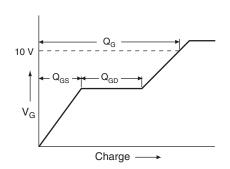


Fig. 12b - Unclamped Inductive Waveforms









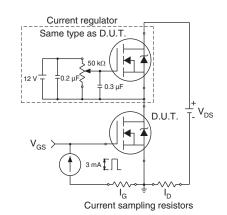
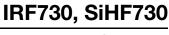


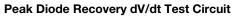
Fig. 13b - Gate Charge Test Circuit

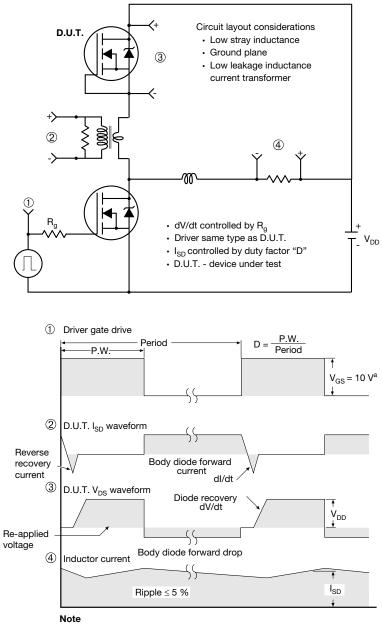
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a.  $V_{GS} = 5 V$  for logic level devices

Fig. 14 - For N-Channel

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg291047">www.vishay.com/ppg291047</a>.

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TO-220-1



DIM.	MILLIN	IETERS	INCHES		
DIN.	MIN.	MAX.	MIN.	MAX.	
А	4.24	4.65	0.167	0.183	
b	0.69	1.02	0.027	0.040	
b(1)	1.14	1.78	0.045	0.070	
С	0.36	0.61	0.014	0.024	
D	14.33	15.85	0.564	0.624	
E	9.96	10.52	0.392	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.10	6.71	0.240	0.264	
J(1)	2.41	2.92	0.095	0.115	
L	13.36	14.40	0.526	0.567	
L(1)	3.33	4.04	0.131	0.159	
ØР	3.53	3.94	0.139	0.155	
Q	2.54	3.00	0.100	0.118	
ECN: X15-0364-Rev. C, 14-Dec-15 DWG: 6031					

Note

-  $M^{\star}$  = 0.052 inches to 0.064 inches (dimension including protrusion), heatsink hole for HVM

Package Picture						
ASE		Xi'an				
		IRF 9510 744K AB				

Revison: 14-Dec-15

1 For technical questions, contact: <u>hvm@vishay.com</u> Document Number: 66542

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