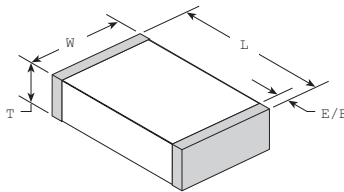


SURFACE MOUNT MLCCs 10 - 200 VDC

CASE SIZE			Voltage	AVAILABLE CAPACITANCE CODE																									
JDI	Inches	(mm)		0R5	XR5	100	120	150	180	220	270	330	390	470	560	680	820	101	121	151	181	221	271	331	391	471	561	681	821
R05	0201	(0603) L .024 ±.001 W .012 ±.001 T .012 ±.001 EB .006 ±.002 (0.60 ±.03) (0.30 ±.03) (0.30 ±.03) (0.15±.05)	25V	█																									
			16V	█											█														
			10V	█											█							█							
				█											█														
R07	0402	(1005) L .040 ±.004 W .020 ±.004 T .025 Max. EB .008 ±.004 (1.02 ±.10) (0.51 ±.10) (0.64) (0.20±.10)	50V	█											█														
			25V	█											█														
			16V	█											█														
			10V	█											█														
R14	0603	(1608) L .063 ±.008 W .032 ±.008 T .035 Max. EB .010±.005 (1.60 ±.20) (0.81 ±.20) (0.89) (.25±.13)	200V	█											█														
			100V	█											█														
			50V	█											█														
			25V	█											█														
			16V	█											█														
R15	0805	(2012) L .080 ±.010 W .050 ±.010 T .050 Max. EB .020±.010 (2.03 ±.25) (1.27 ±.25) (1.27) (0.51±.25)	200V	█											█														
			100V	█											█														
			50V	█											█														
			25V	█											█														
R18	1206	(3216) L .125 ±.010 W .062 ±.010 T .050 Max. EB .020 ±.010 (3.17 ±.25) (1.57 ±.25) (1.27) (0.51 ±.25)	200V	█																									
			100V	█																									
			50V	█																									
			25V	█																									
S41	1210	(3224) L .125 ±.010 W .095 ±.010 T .065 Max. EB .020 ±.010 (3.18 ±.25) (2.41 ±.25) (1.65) (0.51 ±.25)	200V	█											█														
			100V	█											█														
			50V	█											█														
			25V	█											█														
			16V	█											█														
S43	1812	(4532) L .175 ±.010 W .125 ±.010 T .085 Max. EB .025 ±.015 (4.45 ±.25) (3.17 ±.25) (2.16) (0.64 ±.38)	200V	█											█														
			100V	█											█														
			50V	█											█														
			25V	█											█														



NP0
X7R
X5R

How To ORDER - SURFACE MOUNT MLCC

Part number written: 100R07W104KV4E

100	R 07	W	104	K	V	4	E
VOLTAGE	SERIES/SIZE	DIELECTRIC	CAPACITANCE	TOLERANCE	TERMINATION	MARKING	PACKING
100 = 10 V DC 160 = 16 V DC 250 = 25 V DC 500 = 50 V DC 101 = 100 V DC 201 = 200 V DC	R05 = 0201 R07 = 0402 R14 = 0603 R15 = 0805 R18 = 1206 S41 = 1210 S43 = 1812	N = NP0 W = X7R X = X5R	1st two digits are significant; third digit denotes number of zeros, R = decimal. 5R6 = 5.6 pF 100 = 10 pF 102 = 1,000 pF 474 = 0.47 μF	* B = ± 0.10 pF * C = ± 0.25 pF * D = ± 0.50 pF F = ± 1 % G = ± 2 % J = ± 5 % K = ± 10 % M = ± 20 % *Values < 10 pF only	V = Nickel Barrier with 100% Tin Plating (Matte) T = SnPb	3 = Special 4 = Unmarked 6 = EIA Code* *Not available on sizes ≤ 0402	E = Embossed 7" T = Punched 7" U = Embossed 13" R = Punched 13" No code = bulk Tape specifications on page 48. Not all tape styles are available on all parts.



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[1210J2K00102KXT](#) [1210J5000103KXT](#) [1210J5000223KXT](#) [D55342E07B379BR-TR](#) [D55342E07B523DR-T/R](#) [1812J1K00103KXT](#)
[1812J1K00473KXT](#) [1812J2K00680JCT](#) [1812J4K00102MXT](#) [1812J5000102JCT](#) [1812J5000103JCT](#) [1812J5000682JCT](#) [NIN-FB391JTRF](#)
[NIN-FC2R7JTRF](#) [NPIS27H102MTRF](#) [C1206C101J1GAC](#) [C1608C0G1E472JT000N](#) [C2012C0G2A472J](#) [2220J2K00101JCT](#)
[KHC201E225M76N0T00](#) [LRC-LRF1206LF-01R025FTR1K](#) [1812J1K00222JCT](#) [1812J2K00102KXT](#) [1812J2K00222KXT](#)
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[CGA2B2C0G1H040C](#) [CGA2B2C0G1H050C](#) [CGA2B2C0G1H060D](#) [CGA2B2C0G1H070D](#) [CGA2B2C0G1H151J](#) [CGA2B2C0G1H1R5C](#)
[CGA2B2C0G1H2R2C](#) [CGA2B2C0G1H3R3C](#) [CGA2B2C0G1H680J](#) [CGA2B2C0G1H6R8D](#) [CGA2B2X8R1H221K](#) [CGA2B2X8R1H472K](#)
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