500 WATT MULTI-LINE LOW CAPACITANCE TVS ARRAY



DESCRIPTION

The PLCDAxxC-6 Series are low capacitance multi-line transient voltage suppressor arrays that provides board level protection for standard TTL and CMOS bus line applications against the damaging effects of ESD, tertiary lightning and switching transients.

The PLCDAxxC-6 Series has a peak pulse power rating of 500 Watts for an $8/20\mu s$ waveshape. This device series meets the IEC 61000-4-2, IEC 61000-4-4 and IEC 61000-4-5 requirements.

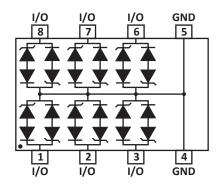
FEATURES

- Compatible with IEC 61000-4-2 (ESD): Air 15kV, Contact 8kV
- Compatible with IEC 61000-4-4 (EFT): 40A 5/50ns
- Compatible with IEC 61000-4-5 (Surge): 24A, 8/20µs Level 2(Line-Gnd) & Level 3(Line-Line)
- 500 Watts Peak Pulse Power per Line (tp = 8/20μs)
- Bidirectional Configuration
- Available in Multiple Voltages Ranging from 3V to 15V
- Protects Up to Six Lines
- Low Capacitance: 8pF
- RoHS Compliant
- REACH Compliant

MECHANICAL CHARACTERISTICS

- Molded JEDEC SO-8 Package
- Approximate Weight: 70 milligrams
- Lead-Free Pure-Tin Plating (Annealed)
- Solder Reflow Temperature:
- Pure-Tin Sn, 100: 260-270°C
- 12mm Tape and Reel Per EIA Standard 481
- Flammability Rating UL 94V-0

PIN CONFIGURATION



APPLICATIONS

- Computer Interface Protection
- Ethernet 10/100/1000 Base T
- Bluetooth & RF

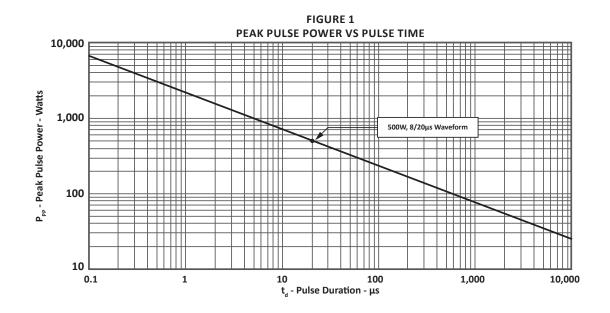
TYPICAL DEVICE CHARACTERISTICS

05102

MAXIMUM RATINGS @ 25°C Unless Otherwise Specified							
PARAMETER SYMBOL VALUE UNITS							
Operating Temperature	Τ _ι	-55 to 150	°C				
Storage Temperature	T _{stg}	-55 to 150	°C				
Peak Pulse Power (tp = 8/20µs) - See Figure 1	P _{pp}	500	Watts				

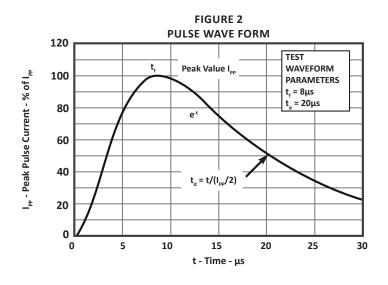
PART NUMBER	DEVICE MARKING	RATED STAND-OFF VOLTAGE	MINIMUM BREAKDOWN VOLTAGE	MAXIMUM CLAMPING VOLTAGE (Fig. 2)	MAXIMUM LEAKAGE CURRENT	MAXIMUM CAPACITANCI (Note 1)
		V _{WM} VOLTS	@1mA V _(BR) VOLTS	@I _p = 1A V _c VOLTS	@V _{wm} Ι _D μΑ	@0V, 1MHz C pF
PLCDA03C-6	PRS	3.3	4.5	7.0	125	8
PLCDA05C-6	PRT	5.0	6.0	9.8	20	8
PLCDA08C-6	PRW	8.0	8.5	13.4	10	8
PLCDA12C-6	PRV	12.0	13.3	19.0	2	8
PLCDA15C-6	PRU	15.0	16.7	24.0	2	8

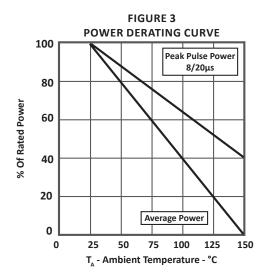
1. Capacitance between I/O pins and ground (pins 4 and 5) is typically 8pF. Capacitance between I/O pins is typically 4pF.

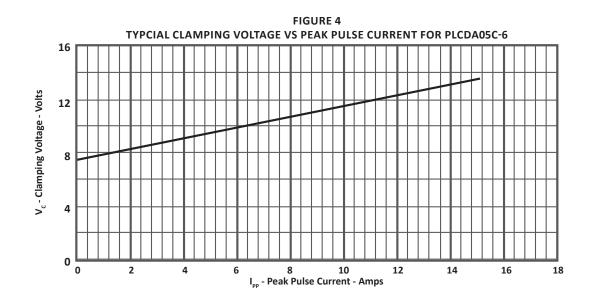


PLCDA03C-6 - PLCDA15C-6

TYPICAL DEVICE CHARACTERISTICS



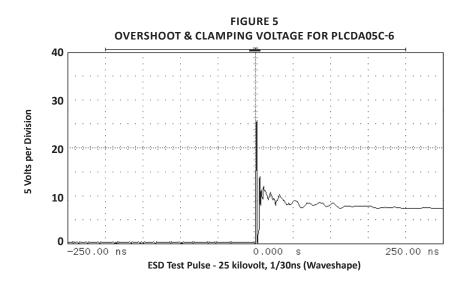




TYPICAL DEVICE CHARACTERISTICS

PROJEK DEVICES

Only One Name Means ProTek'Tion™



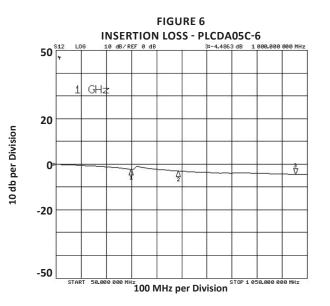


FIGURE 7 RETURN LOSS - PLCDA05C-6 50 [<u>.</u> 367.700 012 MHz 2:-15.579 dB REF Ø d 367.700012 MHz 20 10 db per Division **0**° A 3 ₩ -20 -50 START 58.88 STOP 1 050.000 000 MHz 100 MHz per Division

SPICE MODEL

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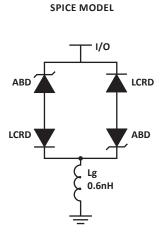


FIGURE 1

ABD - Avalanche Breakdown Diode (TVS) LCRD: Low Capacitance Rectifier Diode Lg - Lead Inductance

	TABLE 1 - SPICE PARAMETERS								
PARAMETER	UNIT	ABD(TVS)	LCRD						
BV	V	See Table 2	200						
IBV	μΑ	1	0.01						
C _{jo}	pF	See Table 2	5						
I _s	А	See Table 2	1E-13						
Vj	V	0.6	0.6						
м	-	0.33	0.33						
N	-	1	1						
R _s	Ohms	See Table 2	0.31						
TT	S	1E-8	1E-9						
EG	eV	1.11	1.11						

TABLE 2 - ABD SPECIFIC SPICE PARAMETERS							
PART NUMBER	B _v (VOLTS)	C _{io} (pF)	I _s (AMPS)	Rs(OHMS)			
PLCDA03	4.5	438	1E-11	0.21			
PLCDA05	6.0	284	1E-11	0.14			
PLCDA15	16.7	102	1E-13	0.52			

APPLICATION INFORMATION

P PROIEK DEVICES

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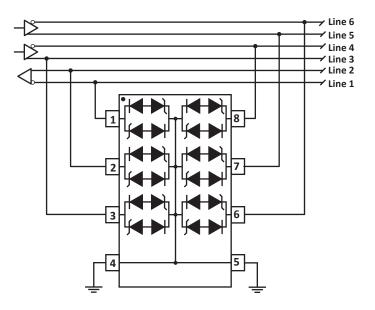


FIGURE 1 - BIDIRECTIONAL COMMON-MODE PROTECTION FOR A TRANSCEIVER

- Circuit connectivity is as follows:
- Line 1 connected to Pin 1.
- Line 2 connected to Pin 2.
- Line 3 connected to Pin 3.
- Line 4 connected to Pin 8.
- Line 5 connected to Pin 7.
- Line 6 connected to Pin 6.
- Pins 4 and 5 connected to ground.

CIRCUIT BOARD RECOMMENDATIONS

Circuit board layout is critical for electromagnetic compatibility protection. The following guidelines are recommended:

- The protection device should be placed near the input terminals or connectors, the device will divert the transient current immediately before it can be coupled into the nearby traces.
- The path length between the TVS device and the protected line should be minimized.
- All conductive loops including power and ground loops should be minimized.
- The transient current return path to ground should be kept as short as possible to reduce parasitic inductance.
- Ground planes should be used whenever possible. For multilayer PCBs, use ground vias.

SO-8 PACKAGE INFORMATION

OUTLINE DIMENSIONS								
DIM	MILLIN	IETERS	INCHES					
DIIVI	MIN	MAX	MIN	MAX				
А	4.80	5.00	0.189	0.196				
В	3.80	4.00	0.150	0.157				
С	1.35	1.75	0.054	0.068				
D	0.35	0.49	0.014	0.019				
F	0.40	1.25	0.016	0.049				
G	1.27 BSC		0.05 BSC					
J	0.18	0.25	0.007	0.009				
К	0.10	0.25	0.004	0.008				
Р	5.80	6.20	0.229	0.244				
R	0.25	0.50	0.010	0.019				



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1. -T- = Seating plane and datum surface.

2. Dimensions "A" and "B" are datum.

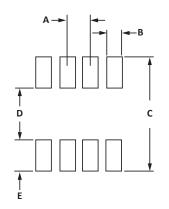
3. Dimensions "A" and "B" do not include mold protrusion.

Maximum mold protrusion is 0.015" (0.380mm) per side.
 Dimensioning and tolerances per ANSI Y14.5M, 1982.

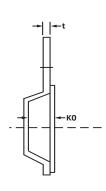
Dimensions are exclusive of mold flash and metal burrs.

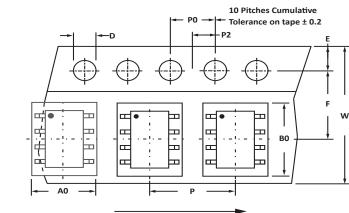
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$
$G \rightarrow = - + = D - R \times 45^{\circ}$
(→ 0.010" (0.25mm) (M) T B (S) A (S) 8 PL

DIM	MILLIN	IETERS	INCHES				
DIIVI	MIN	MAX	MIN	MAX			
А	1.14	1.40	0.045	0.055			
В	0.64	0.89	0.025	0.035			
С	6.22	-	0.245	-			
D	3.94	4.17	0.155	0.165			
E	1.02	1.27	0.040	0.050			
D 3.94 4.17 0.155 0.165							



TAPE AND REEL





User Direction of Feed

SPECIFICATIONS												
REEL DIA.	TAPE WIDTH	A0	В0	ко	D	E	F	w	PO	P2	Р	tmax
178mm (7")	12mm	6.50 ± 0.10	5.40 ± 0.10	2.00 ± 0.10	1.50 ± 0.10	1.75 ± 0.10	5.50 ± 0.05	12.00 ± 0.30	4.00 ± 0.12	2.00 ± 0.10	4.00 ± 0.10	0.25
NOTES	ro io millino o	tors										

1. Dimensions are in millimeters.

2. Surface mount product is taped and reeled in accordance with EIA-481.

3. Suffix - T7 = 7" Reel - 1,000 pieces per 12mm tape.

4. Suffix - T13 = 13" Reel - 2,500 pieces per 12mm tape.

5. Bulk product shipped in tubes of 98 pieces per tube.

6. Marking on Part - marking code (see page 2), date code, logo and pin one defined by dot on top of package.

Package outline, pad layout and tape specifications per document number 06009.R3 9/10.

ORDERING INFORMATION

BASE PART NUMBER (xx = Voltage)	LEADFREE SUFFIX	TAPE SUFFIX	QTY/REEL	REEL SIZE	TUBE QTY		
PLCDAxxC-6	-LF	-T7	1,000	7"	98		
PLCDAxxC-6	-LF	-T13	2,500	13″	98		
This device is only available in a lead-Free configuration							

This device is only available in a Lead-Free configuration.

COMPANY INFORMATION

COMPANY PROFILE

In business more than 20 years, ProTek Devices[™] is a privately-held company located in Tempe, Arizona, that offers a product line of transient voltage suppressors (TVS); avalanche breakdown diodes; steering diode TVS arrays and other surge suppressor component products. These TVS devices protect electronic systems from the effects of lightning, electrostatic discharge (ESD), nuclear electromagnetic pulses (NEMP), inductive switching and EMI / RFI. ProTek Devices also offers high performance interface and linear products that include analog switches; multiplexers; LED drivers; audio control ICs; RF and related high frequency products. The analog devices work in a host of consumer; industrial; automotive and other applications.

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