

RJK0652DPB

 $60V,\,35A,\,7.0m\Omega$ max. Silicon N Channel Power MOS FET Power Switching

Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting

R07DS0077EJ0200

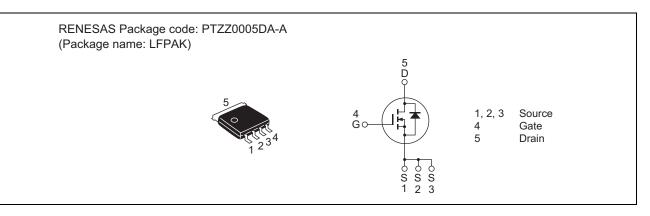
Rev.2.00

Apr 09, 2013

• Low on-resistance

- $R_{DS(on)} = 5.5 \text{ m}\Omega \text{ typ.}$ (at $V_{GS} = 10 \text{ V}$)
- Pb-free
- Halogen-free

Outline



Application

• Switching Mode Power Supply

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	60	V	
Gate to source voltage	V _{GSS}	±20	V	
Drain current	I _D	35	А	
Drain peak current	Note1 I _{D(pulse)}	140	А	
Body-drain diode reverse drain current	I _{DR}	35	А	
Avalanche current	I _{AP} Note 2	17.5	А	
Avalanche energy	E _{AS} Note 2	23	mJ	
Channel dissipation	Pch Note3	55	W	
Channel to Case Thermal Resistance	θch-C	2.27	°C/W	
Channel temperature	Tch	150	٥C	
Storage temperature	Tstg	-55 to +150	٥C	

Notes: 1. $PW \le 10 \ \mu s$, duty cycle $\le 1\%$

2. Value at Tch = 25°C, Rg \geq 50 Ω

3. Tc = 25°C

This product is for the low voltage drive (≤ 10 V).

If the driving voltage is over 10 V under normal conditions, please use the product for high gate to source cutoff voltage $(V_{GS(off)})$ which characteristics has been improved.



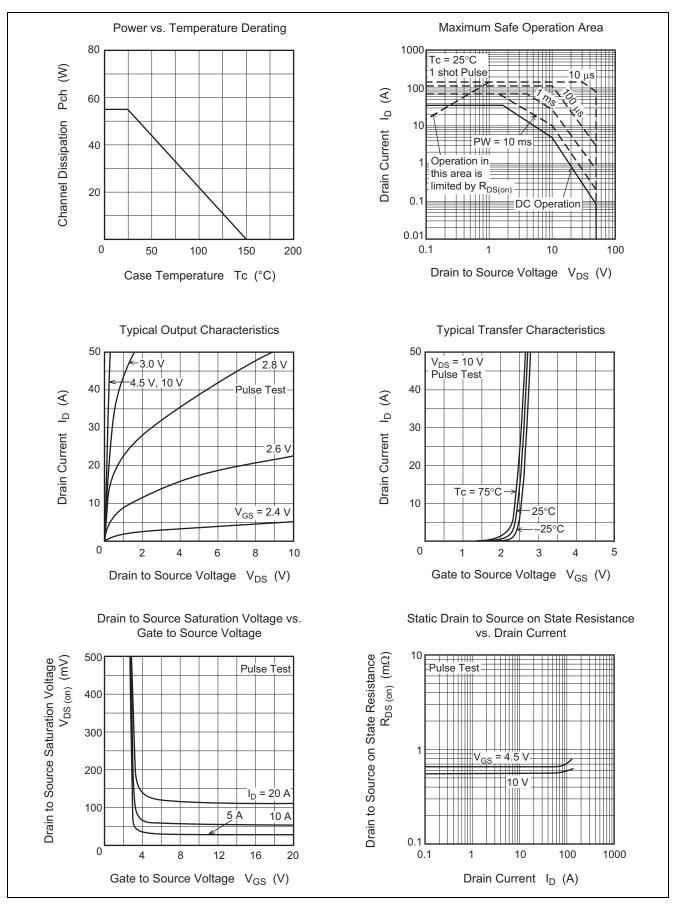
Electrical Characteristics

ltem	Symbol	Min	Tun	Мах	Unit	(Ta = 25°C) Test Conditions	
	Symbol		Тур	wax			
Drain to source breakdown voltage	V _{(BR)DSS}	60	—		V	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	
Gate to source leak current	I _{GSS}	_	—	±0.1	μA	$V_{GS}=\pm 20~V,~V_{DS}=0~V$	
Zero gate voltage drain current	I _{DSS}	_	—	1	μA	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$	
Gate to source cutoff voltage	V _{GS(off)}	1.2	—	2.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	
Static drain to source on state	R _{DS(on)}	—	5.5	7.0	mΩ	$I_D = 17.5 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$	
resistance	R _{DS(on)}	_	6.5	9.0	mΩ	$I_D = 17.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{Note4}$	
Forward transfer admittance	y _{fs}	_	54	_	S	$I_D = 17.5 \text{ A}, V_{DS} = 10 \text{ V}^{Note4}$	
Input capacitance	Ciss	_	4100	_	pF	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0 \text{ V},$ f = 1 MHz	
Output capacitance	Coss	_	485	_	pF		
Reverse transfer capacitance	Crss	_	200	_	pF		
Gate Resistance	Rg		0.4	_	Ω		
Total gate charge	Qg		29	_	nC	$V_{DD} = 25 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V},$ $I_D = 35 \text{ A}$	
Gate to source charge	Qgs		13	_	nC		
Gate to drain charge	Qgd		8.8	_	nC		
Turn-on delay time	t _{d(on)}		11	_	ns		
Rise time	tr	_	7.0	_	ns		
Turn-off delay time	t _{d(off)}		54	_	ns		
Fall time	t _f		10	_	ns		
Body-drain diode forward voltage	V_{DF}		0.83	1.1	V	$I_F = 35 \text{ A}, V_{GS} = 0 \text{ V}^{Note4}$	
Body–drain diode reverse recovery	t _{rr}	_	35		ns	$I_F = 35 \text{ A}, V_{GS} = 0 \text{ V}$	
time						di _F / dt = 100 A/ μs	

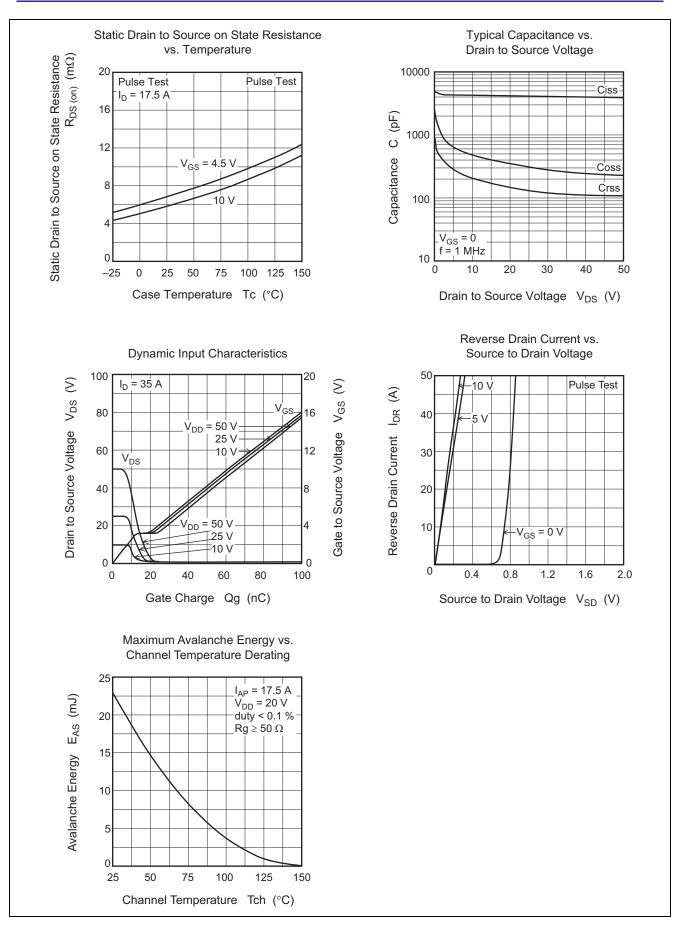
Notes: 4. Pulse test



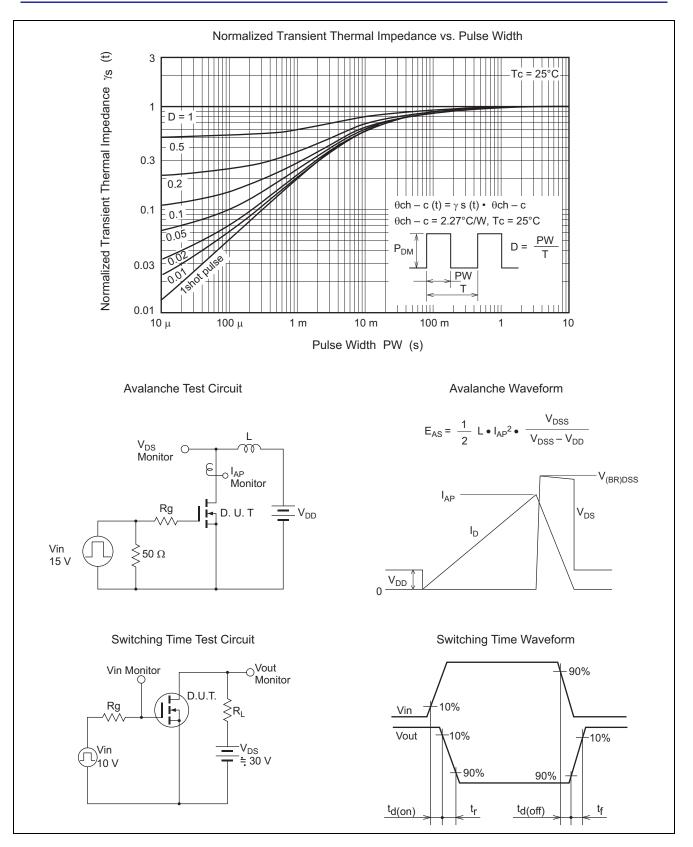
Main Characteristics





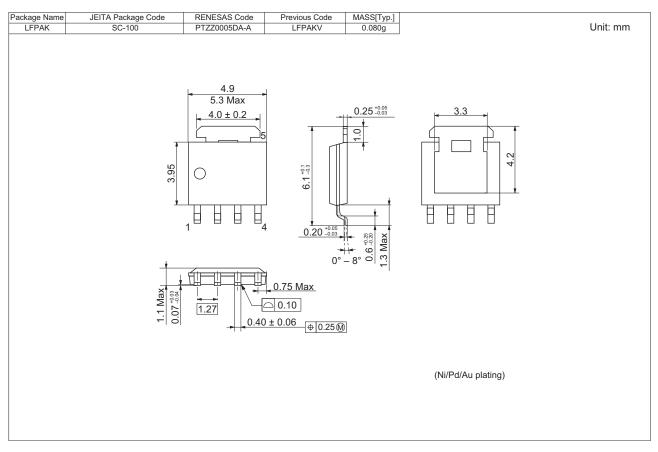








Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK0652DPB-00-J5	2500 pcs	Taping



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