Vishay Semiconductors

Schottky Rectifier, 5.5 A



- Popular D-PAK outline
- Small foot print, surface mountable
- · Low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

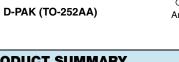
DESCRIPTION

The VS-50WQ04FNPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	5.5	А						
V _{RRM}		40	V						
I _{FSM}	t _p = 5 μs sine	340	A						
V _F	5 Apk, T _J = 125 °C	0.44	V						
TJ	Range	- 40 to 150	°C						

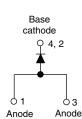
VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-50WQ04FNPbF	UNITS					
Maximum DC reverse voltage	V _R	40	V					
Maximum working peak reverse voltage	V _{RWM}	40	v					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONE	VALUES	UNITS				
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at $T_C = 135$ °C,	5.5					
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	550	А			
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	90				
Non-repetitive avalanche energy	E _{AS}	$T_{J} = 25 \text{ °C}, I_{AS} = 1.5 \text{ A}, L = 8 \text{ mH}$		9	mJ			
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical		1.2	А			



PRODUCT SUMMARY							
Package	D-PAK (TO-252AA)						
I _{F(AV)}	5.5 A						
V _R	40 V						
V _F at I _F	See Electrical table						
I _{RM}	40 mA at 125 °C						
T _J max.	150 °C						
Diode variation	Single die						
E _{AS}	9 mJ						











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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS			
		5 A	T.I = 25 °C	0.51	v		
Maximum forward voltage drop	V (1)	10 A	1j=25 C	0.63			
See fig. 1	V _{FM} ⁽¹⁾	5 A	T 105 %C	0.44			
		10 A	T _J = 125 °C	0.59			
Maximum reverse leakage current	I _{RM} ⁽¹⁾	$T_J = 25 \text{ °C}$		3	mA		
See fig. 2		T _J = 125 °C	V _R = Rated V _R	40	ША		
Thereshold voltage			0.27	V			
Forward slope resistance	r _t	T _J =T _J maximum	26.77	mΩ			
Typical junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range 1	405	pF			
Typical series inductance	L _S	Measured lead to lead 5 mm fi	5.0	nH			

Note

⁽¹⁾ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 40 to 150	°C			
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.0	°C/W			
Approximate weight			0.3	g			
Approximate weight			0.01	oz.			
Marking device		Case style D-PAK (similar to TO-252AA)	50WQ04FN				

Note

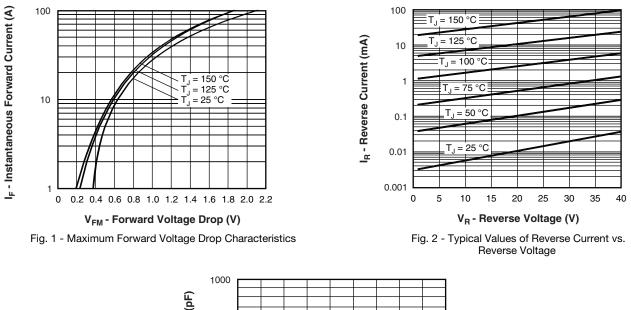
(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



VS-50WQ04FNPbF

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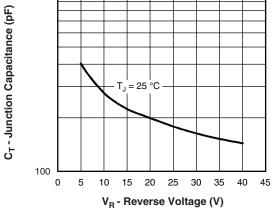


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

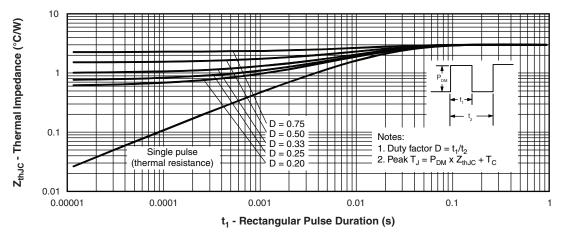


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

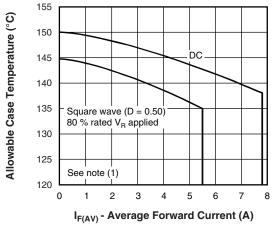
VS-50WQ04FNPbF

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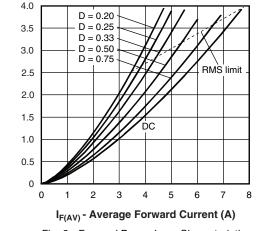
Schottky Rectifier, 5.5 A

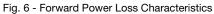
Average Power Loss (W)

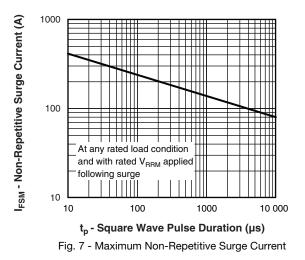












Note

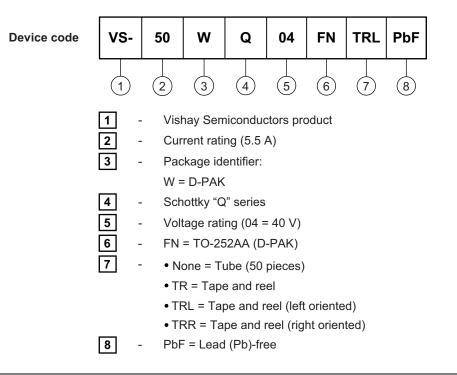
- (1)
- Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 D); I_R at V_{R1} = 80 % rated V_R



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ORDERING INFORMATION TABLE



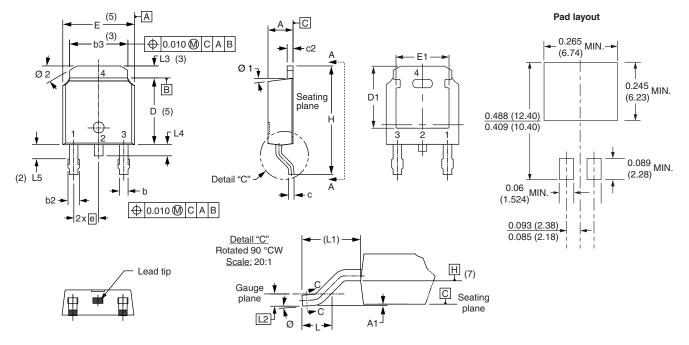
LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95016					
Part marking information	www.vishay.com/doc?95059					
Packaging information	www.vishay.com/doc?95033					





D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES		NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		
А	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC			
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410			
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070			
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.			
b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC			
с	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3		
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040			
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2		
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°			
E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°			
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°			

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip

(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁶⁾ Dimension b1 and c1 applied to base metal only

⁽⁷⁾ Datum A and B to be determined at datum plane H

⁽⁸⁾ Outline conforms to JEDEC outline TO-252AA

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