

PNP 500 mA, 50 V resistor-equipped transistor; R1 = 1 k $\Omega$ , R2 = 10 k $\Omega$ 

Rev. 3 — 23 September 2010

**Product data sheet** 

# 1. Product profile

### 1.1 General description

500 mA PNP Resistor-Equipped Transistor (RET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

NPN complement: PDTD113ZT.

### 1.2 Features and benefits

- 500 mA output current capability
- Built-in bias resistors
- Simplifies circuit design
- Reduces component count

### 1.3 Applications

- Digital application in automotive and industrial segments
- Control of IC inputs

### 1.4 Quick reference data

#### Table 1. Quick reference data

Symbol Conditions Unit Parameter Min Тур Max VCEO collector-emitter voltage open base ---50 V  $I_{O}$ output current --500 mΑ -R1 0.7 1.0 bias resistor 1 (input) 1.3 kΩ R2/R1 bias resistor ratio 9 10 11

- Reduces pick and place costs
- ±10 % resistor ratio tolerance
- AEC-Q101 qualified
- Cost-saving alternative for BC807 series in digital applications
- Switching loads



PNP 500 mA resistor-equipped transistor; R1 = 1 k $\Omega$ , R2 = 10 k $\Omega$ 

# 2. Pinning information

Table 2.	Pinning		
Pin	Description	Simplified outline Graphic symbol	
1	input (base)		
2	GND (emitter)	3	- 3
3	output (collector)	1 2 006aaa144 sym003	- 2

# 3. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
PDTB113ZT	-	plastic surface-mounted package; 3 leads	SOT23		

## 4. Marking

Table 4. Marking codes	
Type number	Marking code <sup>[1]</sup>
PDTB113ZT	*7W

- [1] \* = -: made in Hong Kong
  - \* = p: made in Hong Kong
  - \* = t: made in Malaysia
  - \* = W: made in China

## 5. Limiting values

#### Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter	-	-50	V
$V_{CEO}$	collector-emitter voltage	open base	-	-50	V
$V_{\text{EBO}}$	emitter-base voltage	open collector	-	-5	V
VI	input voltage				
	positive		-	+5	V
	negative		-	-10	V
lo	output current		-	-500	mA

#### **PNP 500 mA resistor-equipped transistor;** $R1 = 1 k\Omega$ , $R2 = 10 k\Omega$

#### Limiting values ... continued Table 5.

In accordance with the Absolute Maximum Rating System (IEC 60134).

			,		
Symbol	Parameter	Conditions	Min	Max	Unit
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	<u>[1]</u> -	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

#### **Thermal characteristics** 6.

Table 6.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	<u>[1]</u> -	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

#### **Characteristics** 7.

#### $T_{amb} = 25$ °C unless otherwise specified. Symbol Parameter Conditions Unit Min Тур Max collector-base $V_{CB} = -40 \text{ V}; I_E = 0 \text{ A}$ \_ -100 nA I<sub>CBO</sub> cut-off current $V_{CB} = -50 \text{ V}; I_E = 0 \text{ A}$ -100 -nA $V_{CE} = -50 \text{ V}; I_B = 0 \text{ A}$ **I**CEO collector-emitter -\_ -0.5 μΑ cut-off current $V_{EB} = -5 \text{ V}; I_{C} = 0 \text{ A}$ emitter-base -0.8 mΑ $I_{EBO}$ -cut-off current DC current gain $V_{CE} = -5 V;$ 70 $h_{FE}$ -- $I_{\rm C} = -50 \, {\rm mA}$ collector-emitter $I_{C} = -50 \text{ mA};$ V V<sub>CEsat</sub> -0.3-saturation voltage $I_B = -2.5 \text{ mA}$ off-state input voltage $V_{CE} = -5 V;$ V<sub>I(off)</sub> -0.3 -0.6 -1.0٧ $I_{\rm C} = -100 \ \mu A$ V<sub>I(on)</sub> $V_{CE} = -0.3 V;$ -0.4 -0.8 -1.4V on-state input voltage $I_C = -20 \text{ mA}$ 1.3 R1 bias resistor 1 (input) 0.7 1.0 kΩ R2/R1 bias resistor ratio 9 10 11 collector capacitance pF $C_{c}$ $V_{CB} = -10 V;$ 11 - $I_E = i_e = 0 A;$ f = 100 MHz

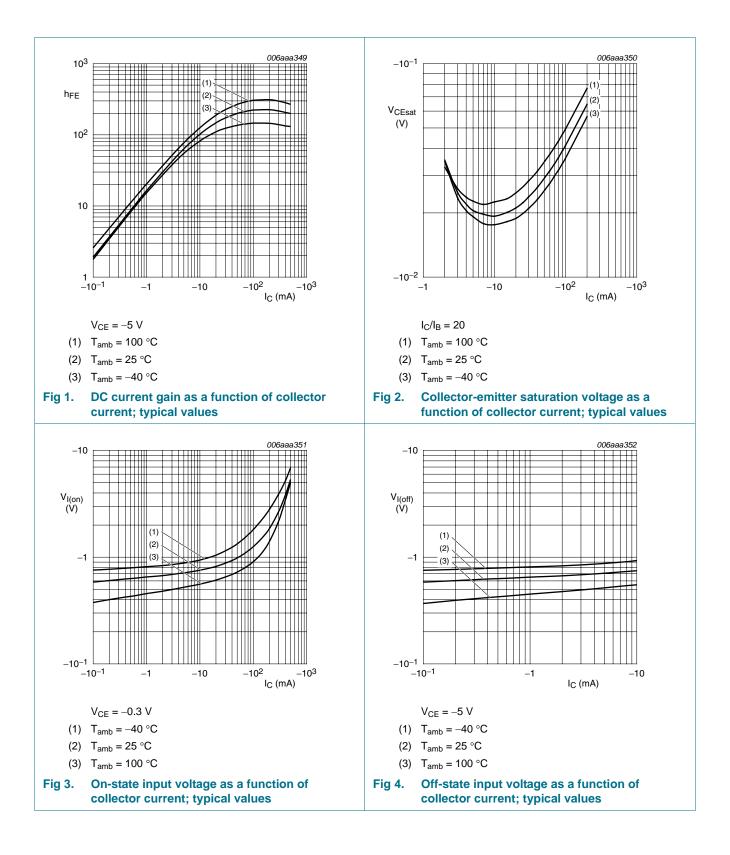
Table 7. **Characteristics** 

PDTB113ZT Product data sheet

### **NXP Semiconductors**

# PDTB113ZT

#### PNP 500 mA resistor-equipped transistor; R1 = 1 k $\Omega$ , R2 = 10 k $\Omega$



PDTB113ZT

4 of 10

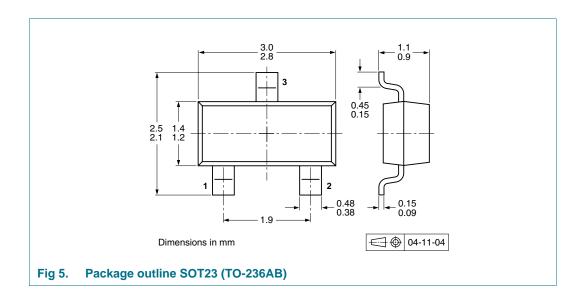
**PNP 500 mA resistor-equipped transistor;**  $R1 = 1 k\Omega$ ,  $R2 = 10 k\Omega$ 

## 8. Test information

#### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

### 9. Package outline



# **10. Packing information**

#### Table 8. Packing methods

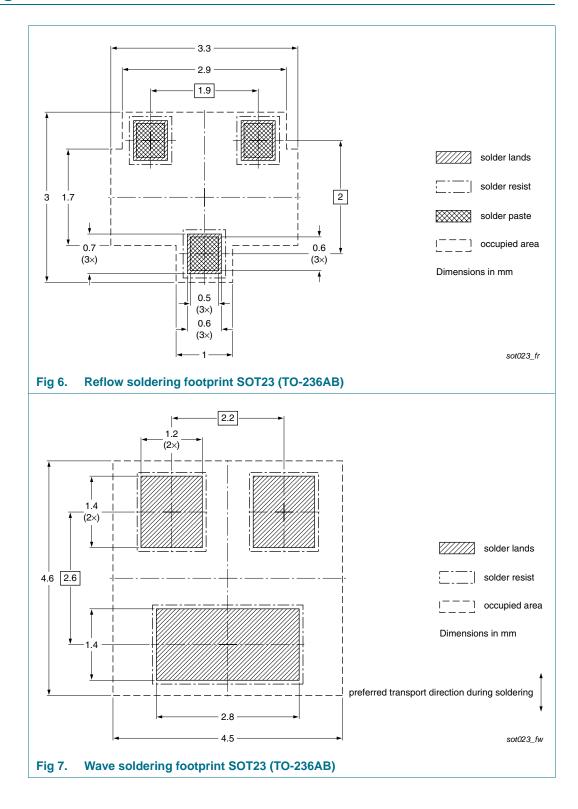
The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing qu	antity
			3000	10000
PDTB113ZT	SOT23	4 mm pitch, 8 mm tape and reel	-215	-235

[1] For further information and the availability of packing methods, see Section 14.

PNP 500 mA resistor-equipped transistor; R1 = 1 k $\Omega$ , R2 = 10 k $\Omega$ 

## 11. Soldering



# 12. Revision history

Table 9. Revision h	istory			
Document ID	Release date	Data sheet status	Change notice	Supersedes
PDTB113ZT v.3	20100923	Product data sheet	-	PDTB113Z_SER_2
Modifications:	<ul> <li>Type number</li> </ul>	ers PDTB113ZK and PDTB	113ZS deleted.	
	<ul> <li>Table 7 "Characteristic Table 7"</li> </ul>	aracteristics": unit for V <sub>CEsat</sub>	changed from mV to V	Ι.
	<ul> <li>Section 8 "T</li> </ul>	est information": added.		
	Section 11 "	Soldering": added.		
	<ul> <li>Section 13 "</li> </ul>	Legal information": updated	1.	
PDTB113Z_SER_2	20091116	Product data sheet	-	PDTB113Z_SER_1
PDTB113Z_SER_1	20050427	Product data sheet	-	-

# 13. Legal information

#### 13.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <a href="http://www.nxp.com">http://www.nxp.com</a>.

#### 13.2 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

### 13.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <a href="http://www.nxp.com/profile/terms">http://www.nxp.com/profile/terms</a>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

8 of 10

#### **PNP 500 mA resistor-equipped transistor;** $R1 = 1 k\Omega$ , $R2 = 10 k\Omega$

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

#### 13.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

# 14. Contact information

For more information, please visit: http://www.nxp.com

For sales office addresses, please send an email to: salesaddresses@nxp.com

#### **NXP Semiconductors**

# PDTB113ZT

PNP 500 mA resistor-equipped transistor; R1 = 1 k $\Omega$ , R2 = 10 k $\Omega$ 

## **15. Contents**

1	Product profile 1
1.1	General description 1
1.2	Features and benefits 1
1.3	Applications 1
1.4	Quick reference data 1
2	Pinning information 2
3	Ordering information 2
4	Marking 2
5	Limiting values 2
6	Thermal characteristics 3
7	Characteristics 3
8	Test information 5
8.1	Quality information 5
9	Package outline 5
10	Packing information 5
11	Soldering 6
12	Revision history 7
13	Legal information 8
13.1	Data sheet status 8
13.2	Definitions
13.3	Disclaimers
13.4	Trademarks9
14	Contact information 9
15	Contents 10

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

#### © NXP B.V. 2010.

All rights reserved.

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 23 September 2010 Document identifier: PDTB113ZT

# **X-ON Electronics**

Largest Supplier of Electrical and Electronic Components

Click to view similar products for nxp manufacturer:

Other Similar products are found below :

MC13211R2 LFSTBEB865X MC33399PEFR2 PCA9551PW,112 MC34825EPR2 CBTW28DD14AETJ PCF8583P MC68340AB16E MC8640DTVJ1250HE EVBCRTOUCH MC9S08PT8AVTG MC9S08SH32CTL MCF54415CMJ250 MCIMX6Q-SDB MCIMX6SX-SDB 74ALVC125BQ,115 74HC4050N 74HC4514N MK21FN1M0AVLQ12 MKV30F128VFM10 FRDM-K66F FRDM-KW40Z FRDM-MC-LVBLDC PESD18VF1BSFYL PMF63UNEX PSMN4R0-60YS,115 HEF4028BPN RAPPID-567XFSW MPC565MVR56 MPC574XG-176DS MPC8548VJAUJD MPC860PCVR66D4 BCV61A,215 BFU520XAR BT137-600E BT137S-600D.115 BT138-600E.127 BT139X-600.127 BT258-600R.127 BUK7628-100A118 BUK765R0-100E.118 P5020NSE7VNB S12ZVML12EVBLIN SCC2692AC1N40 LPC1785FBD208K LPC2124FBD64/01 LS1020ASN7KQB LS1020AXN7HNB LS1020AXN7KQB LS1043ASE7PQA