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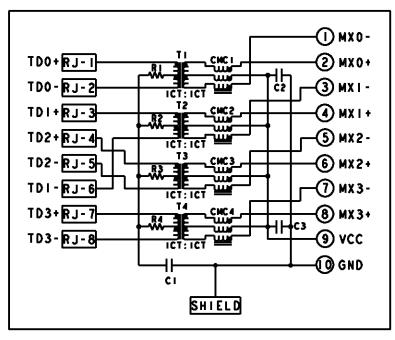
5

S8G56 GIGABIT CIRCUIT A

7

8

Top and Bottom Ports



Cl = 1000pF, 2kV Capacitor C2-C3 = 10nF, 50V Capacitors R1-R4 = 75 Ohms, 1/16W, Resistors

<u>Pin Designations</u> (Repeat for each vertical pair of ports.)								
PCB Pins (Top View, Comp. Side)	4 00100 5005 000 5005 000 5004 005 0 5004 005 0 1002 0001 5 1002 0001 5							
RJ Cable Coniacis	Top Ports RJ-8							

<u> </u>	OI010	

RJ-I RJ-8

Botiom Poris

LEDS ARE DRIVEN WI LED COLOR: DOMINANT WAVEL FORWARD VOL DOMINANT WAVEL FORWARD VOL DOMINANT WAVEL FORWARD VOL

DOMINANT WAVE FORWARD VO DOMINANT WAVEL

3		2							
		AA 00 P	LTR	REVISIONS DESCRIPTION	DATE DWN APVD				
			D1 REVISED PER ECO-1	1-005140	29APR11 RK HMR				
: BLACK, THER PREPLATED WIT H 2.54um MIN SELECT 1.27ur MATTE TIN ON POXY LENS, CAI ITH 2.03um MIN I.02um MIN CC E TIN AND/OR HIGH TEMP PCB	H 0.76um MI SAC SOLDER .27um MIN O MIN GOLD SOLDER TAIL RBON STEEL N SILVER OV OPPER UNDER SAC SOLD <u>E</u> R	N SEMI-BRIG ON SOLDER T. VERALL NICK AT MATING I S. EAD FRAME ER I.02um M PLATE. POST	HT NICKEL, AILS, EL NTERFACE TAILS OF LE IN NICKEL -PLATED WIT	D		D			
I00/I000 BASE HMS P:CABLE): I:I UCTANCE (OCL) FROM 0°C TO 7(I-DIRECTIONAL °C: OSS (IL): I.I	ALL FOUR P. : 350uH MIN 0°C, ALL FO	@IOOkHz, O JR PAIRS							
(RL): 18dB M 12-2010 TTENUATION: 33 33-2010 REJECTION RA E: 2250VDC (MA WITH ALL PO LOGO, PART NU	IN FROM 0.51 DG(f/80)dB 1 5dB MIN FRO DG(f/50)dB 1 TIO (CMRR): AX) FOR 60 DRTS CONNEC MBER, DATE	MHZ TO 40MH MIN FROM 40 MIN FROM 40 MIN FROM 40 30dB MIN SECONDS WIT TED. CODE AND CC	z .IMHz TO IO 40MHz .IMHz TO IO FROM 0.5MHz H A RISE TI DUNTRY OF OF	0MHz TO IOOMHz ME OF 500V/ RIGIN	SEC AND				
VAL LOGO TO BI COMMENDED FOR				WN.		с			
260°C MAX, IC			υ,						
FROM 0°C TO + DRMS TO FCC RU C CONNECTIONS /MDIX.	LES AND REG		RT 68 SUBPAF	RT F.					
DIMENSION ESTA									
ESTABLISHED BY 8mm.	CUSIOMER,	BUI MAY NUI	DE						
VITH CONSTANT	CURRENT AT	APPROX 20mA	l.						
ELENGTH (λ.D): GREEN 568 nm TYP. @ IF=20mA OLTAGE (VF): GREEN 2.2V TYP. @ IF=20mA ELENGTH (λ.D): LOW WAVELENGTH GREEN (LWG) 557 nm TYP. @ IF=20mA OLTAGE (VF): LOW WAVELENGTH GREEN 2.2V TYP. @ IF=20mA ELENGTH (λ.D): ULTRA LOW WAVELENGTH GREEN (ULWG) 525 nm TYP @ IF=20mA OLTAGE (VF): ULTRA LOW WAVELENGTH GREEN (ULWG) 2.8V TYP @ IF=20 mA ELENGTH (λ.D): YELLOW 588 nm TYP. @ IF=20mA OLTAGE (VF): YELLOW 2.1V TYP. @ IF=20mA ELENGTH (λ.D): ORANGE 605 nm TYP. @ IF=20mA OLTAGE (VF): ORANGE 2.1V TYP. @ IF=20mA									
3.04	ULWG/YEL LWG/YEL	ULWG LWG/YEL	ULWG LWG/YEL	ULWG/YEL LWG/YEL	9-1840332-7 9-1840332-4				
3.04	ORANGE	GRN/ORA	ORANGE	GRN/ORA	4-1840332-4				
3.04	YELLOW GRN/YEL	GRN/ORA GRN/YEL	YELLOW GRN/YEL	GRN/ORA GRN/YEL	4-1840332-3				
4.06	GRN/YEL	GRN/YEL	GRN/YEL	GRN/YEL	1-1840332-4				
3.04	GREEN	GRN/ORA GRN/YEL	GREEN	GRN/ORA GRN/YEL	840332-9 840332-8				
3.04	GRN/ORA	GREEN	GRN/ORA	GREEN	1840332-6				
3.04	GRN/YEL GRN/YEL	GREEN GRN/YEL	GRN/YEL GRN/YEL	GREEN GRN/YEL	840332-5 840332-4				
3.04	GREEN	YELLOW	GREEN	YELLOW	1840332-3				
3.04	YELLOW GREEN	GREEN GREEN	GREEN GREEN	YELLOW GREEN	840332-2 840332-				
DIM A	BOTTOM LED	BOTTOM LED		TOP LED	PART NUMBER	A			
		DWH TOMMY RE CHK KEITH ZH	04FEB2009	с ТЕ те	Connectivity				
3	mm D PLC	SE SP COTFTED: AP VD ±0.25 PRODUCT SPEC ±0.25 PRODUCT SPEC	10 04FEB2009 NAME 2XI M. 4004 WAVE	AG45(TM),GIGABI PANEL, GROUND					
NATERIAL	2 PLC 3 PLC 4 PLC 4 PLC 4 NCL ES	±0.25 108-10 ±0.25 APPLICATION ±	SPEC SIZE CAG	be code drawing no)779 C - 840332	RESTRICTED TO				
	A		2 1 1		TS SHEET OF 3 PEV D1				

		2				I						
		AA 00	P LTR		SIONS	DATE DWN	APVD					
			D1 REVISED	PER ECO-11-0051	40	29APR11 RK	HMR					
D WITH MIN S ZE, I.	OPLASTIC F 0.76um MI AC SOLDER 27um MIN O	N SEMI-BR ON SOLDER VERALL NI	IGHT NICK TAILS, CKEL	EL,	- 0							
N ON S S, CAR Jm MIN MIN CO D/OR S	MIN GOLD OLDER TAIL BON STEEL SILVER OV PPER UNDER AC SOLDER TG>I70°C.	S. LEAD FRAM ER I.02um PLATE. PO	E TAILS O MIN NICK ST-PLATED	F LED EL WITH	P		D					
BASE-T I:I ALL FOUR PAIRS												
(OCL):	.): 350uH MIN @I00kHz, 0.IVRMS, 70°C, ALL FOUR PAIRS											
BdB MI 2-20LO	B MAX FROM N FROM 0.5 G(f/80)dB dB MIN FRO	MHz TO 40 MIN FROM	MHz 40.IMHz T	о тоомн	Z							
B-20LO DN RAT DC (MA	G(f/50)dB IO (CMRR): X) FOR 60 RTS CONNEC	MIN FROM 30dB MI SECONDS W	40.IMHz T N FROM 0.	5MHz TO	100MHz	SEC AND						
TO BE D FOR	MBER, DATE LOCATED I WAVE SOLDE	N APPROXI RING PROC	MATE AREA		IN		с					
X, 10 : TO +	SECONDS MA	ΥΧ .										
CC RUL	LES AND REG ARE SYMMET			JBPART F	. .							
	BLISHED BY CUSTOMER,											
TANT (CURRENT AT	APPROX 20)mA									
/Ε): G (λ.D): /Ε): L	REEN 2.2V LOW WAVELE OW WAVELEN	GTH GREEN	F=20mA IN (LWG) 5 2.2V TYP	57 nm T . @ IF=	20mA							
/F): U (入D): /F): Y	ULTRA LOW LTRA LOW W YELLOW 588 ELLOW 2.IV	AVELENGTH nm TYP. TYP. @ I	GREEN (U @ IF=20m4 F=20mA	LWG) 2.			В					
/F): 0	ORANGE 605 RANGE 2.IV	TYP. @ I					_					
) 4	ULWG/YEL LWG/YEL	ULWG LWG/YEL	ULW . LWG/Y		LWG/YEL .WG/YEL	9-1840332 9-1840332						
) 4	ORANGE YELLOW	GRN/ORA GRN/ORA			GRN/ORA	4-1840332						
9	GRN/YEL	GRN/YEL			GRN/YEL	2-1840332						
) 6	GRN/YEL GREEN	GRN/YEL GRN/ORA			GRN/YEL GRN/ORA	- 840332 840332-						
) 4	GREEN	GRN/YEL			GRN/YEL	1840332-						
) 4	GRN/ORA	GREEN	GRN/C		GREEN	840332-						
) 4	GRN/YEL GRN/YEL	GREEN	<u> </u>		GREEN	1840332-						
) 4	GREEN	GRN/YEL YELLOW	GREE		<u>YELLOW</u>	1840332-						
) 4	YELLOW	GREEN	GREE	N	YELLOW	840332-	2					
- A	GREEN BOTTOM LED NO. 2	GREEN BOTTOM LI NO. I		р 🗸 то	GREEN PLED	1840332- PART NUMBER	I A					
	7103	DWH TOMM CHA	04FEB2009 4Y REN 04FEB2009		TE TE	Connectivity	\neg					
DI	MERSIDAS: mm 0 PLC 1 PLC 2 PLC 3 PLC	3E SPECIFIED: APVD ±0.25 PRODUC ±0.25 0 ±0.25 0	H ZHU 04FEB2009 NETH LAL	WAVE PAN	EL, GROUND S	T \$8656 CIRCUI Ghield, W/Leds						
NATERIAL	4 PLC ANGLES FINISH	Щ.25 АРРЕІСІ ± А WEIBHT	-	SIZE CAGE CODE	©⊂ 840332	PESTRI	CTED TO -					
	\triangle	CUSTO	MER DRAWING	P 1 1 2 2 1 3		S SHEET OF 3 PE	^V D1					

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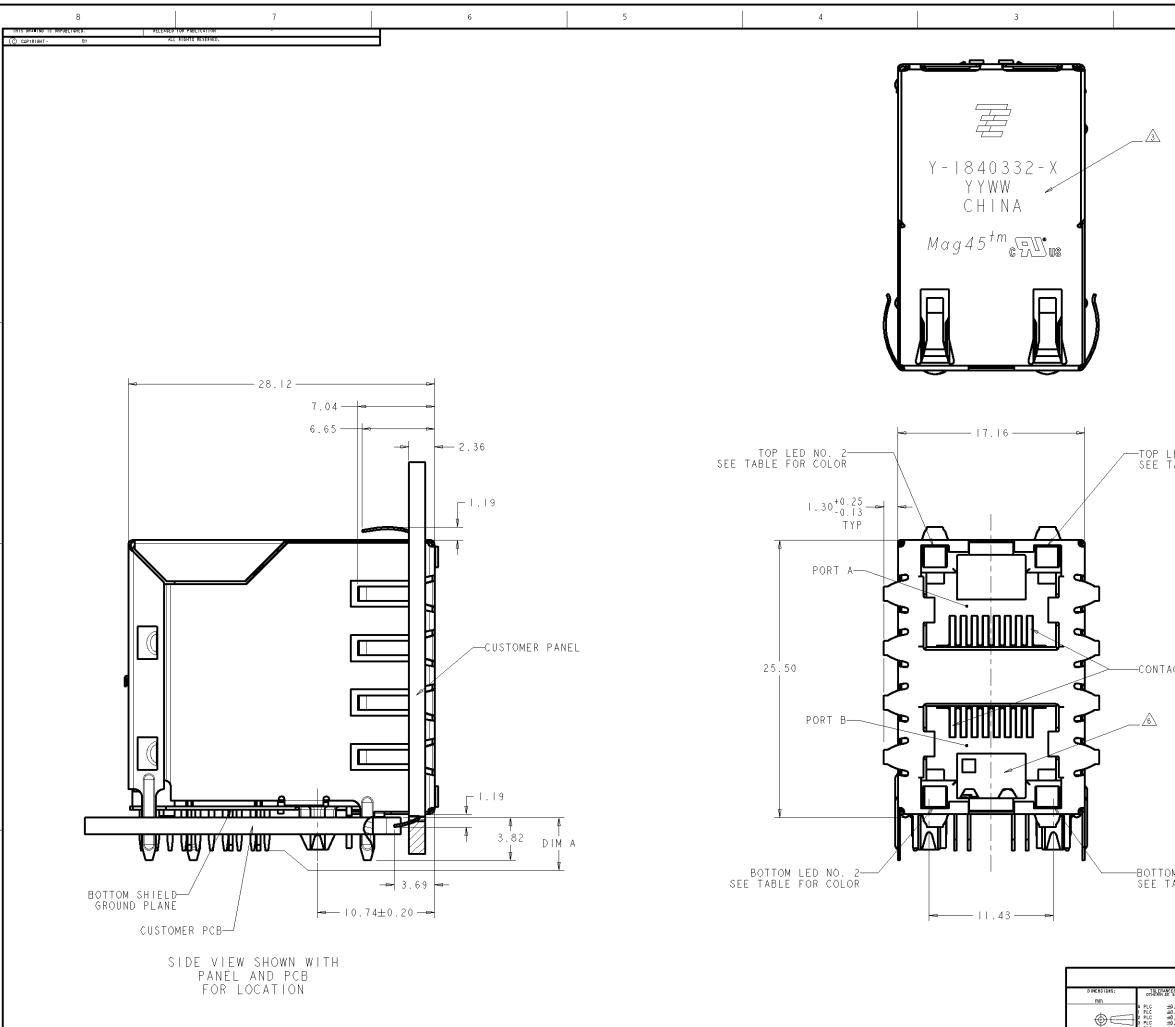
▲ MATERIALS: PLASTIC HOUSING: SHIELD: BRASS, PF POST DIPPED WITH CONTACTS: PHOSPHC UNDERPLATE WITH S AND 2.54um MIN MA LED: DIFFUSED EPC ARE PREPLATED WIT UNDERPLATE OVER I 2.54um MIN MATTE ALL PC BOARDS: HI

AMAGNETICS APPLICATION: 10/1 IMPEDANCE: 100 OHI TURNS RATIO (CHIP OPEN CIRCUIT INDU 8mADC BIAS F

- ALL FOUR PAIRS BI-PERFORMANCE @ 25*(INSERTION LOS RETURN LOSS
 - CROSSTALK AT

COMMON MODE I ISOLATION VOLTAGE

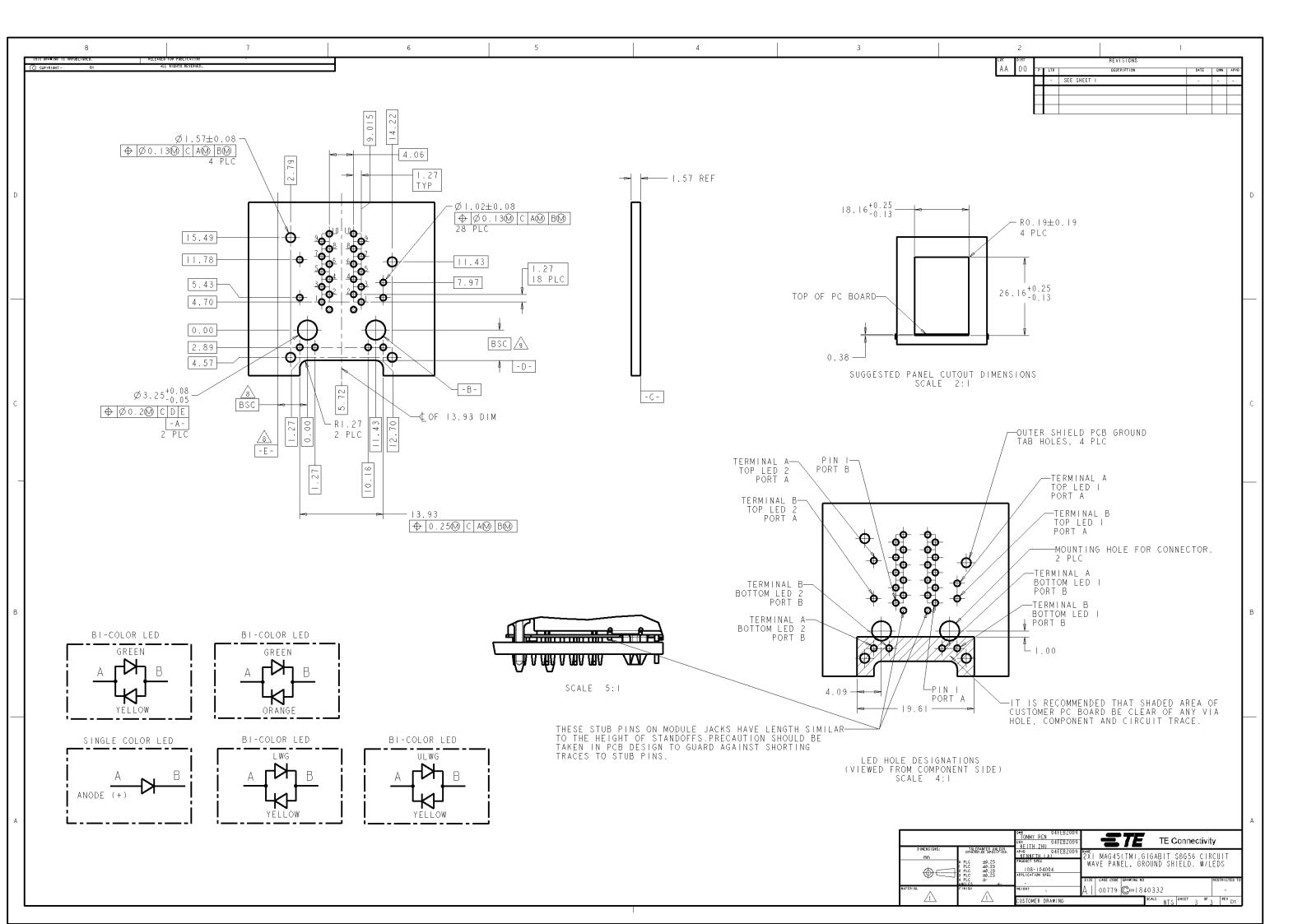
- AND AGENCY APPROV
- 4. THE PARTS ARE RECO PEAK TEMPERATURE
- 5. OPERATING TEMP:
- ▲ RJ45 CAVITY CONFO
- A INDICATED MAGNETIC SUPPORT AUTO-MDI/M
- ▲ DATUM AND BASIC DI
- BASIC DIMENSION ES GREATER THAN 5.08m
- LEDS ARE DRIVEN W



LOC	2 Dist					F	REVIS	IONS			I				1
AA	00	P	LTP -	SEE SI	HEET		DESCRIP					DATE -	DWN -	APVD -	
															5
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ED NO TABLE). I For	С	OL(DR											Ŭ
ACT NC).														
															В
															D
)M LED Able	NO. FOR	Ci	I Olc)R											
															A
	DWH TOMM1 CHK	r Re	04 N	EB2009		-	- 7	F		TE Co	1000	ctivit			
CES UNLESS SPECIFIED:	CHK KEITH APVD KENNE PRODUCT	I ZH		FEB2009 FEB2009	nahe 2 X I	MAG	64 5(1	TMD.G	ilga	BIT	S865	6 CII	, RCUIT	T	
H0.25 H0.25 H0.25 H0.25 H0.25 H	PRODUCT 118- APPLICAT		1004 SPEC							ID SH	IEĹĎ	, W/			
<u>+</u>	- WEIBHT		-		A				403:	32			-		
17	CUSTOM	IE R	DRAWI	NG					SCALE	NTS	SHEET	2 OF	3 PE	^v D1	

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