# **TLP2768A**

#### 1. Applications

- Factory Networking
- High-Speed Digital Interfacing for Instrumentation and Control Devices
- I/O Interface Boards

#### 2. General

The Toshiba TLP2768A consists of a high-output GaAlAs light-emitting diode coupled with integrated high gain, high-speed photodetectors. It is housed in a thin SO6L package of 2.3 mm(max). The TLP2768A has guaranteed the isolation voltage 5 kVrms and compliant with international safety standards for reinforced insulation. It guarantees operation at up to 125 °C and on supplies from 2.7 V to 5.5 V.

The TLP2768A has an internal Faraday shield that provides a guaranteed common-mode transient immunity of  $\pm 20~kV/\mu s.$ 

#### 3. Features

- (1) Inverter logic type (open collector output)
- (2) Package: SO6L
- (3) Operating temperature: -40 to 125 °C
- (4) Data transfer rate: 20 MBd (typ.) (NRZ)
- (5) Threshold input current: 5.0 mA (max)
- (6) Supply current: 4 mA (max)
- (7) Common-mode transient immunity:  $\pm 20 \text{ kV/}\mu\text{s}$  (min)
- (8) Isolation voltage: 5000 Vrms (min)
- (9) Safety standards

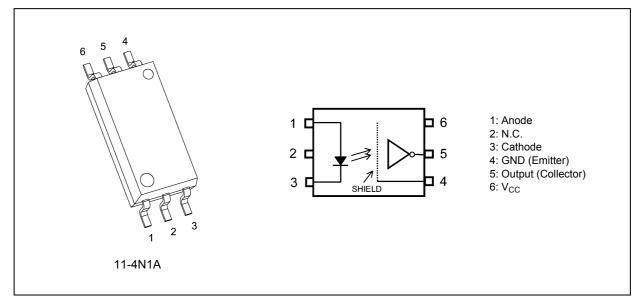
UL-approved: UL1577, File No.E67349

cUL-approved: CSA Component Acceptance Service No.5A File No.E67349

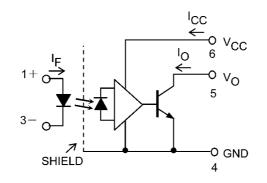
VDE-approved: EN60747-5-5, EN60065 or EN60950-1, EN62368-1 (Pending) (Note 1)

Note 1: When a VDE approved type is needed, please designate the Option (D4).

#### 4. Packaging and Pin Assignment



#### 5. Internal Circuit



#### 6. Principle of Operation

#### 6.1. Truth Table

Input	LED	Output
Н	ON	L
L	OFF	Н

#### 6.2. Mechanical Parameters

Characteristics	Dimension	Unit
Package height	2.3 (max)	mm
Creepage distances	8.0 (min)	
Clearance distances	8.0 (min)	
Internal isolation thickness	0.4 (min)	

#### 7. Absolute Maximum Ratings (Note) (Unless otherwise specified, T<sub>a</sub> = 25 °C)

	Characteristics		Symbol	Note	Rating	Unit
LED	Input forward current		I <sub>F</sub>		25	mA
	Input forward current derating	$(T_a \ge 110 \ ^\circ C)$	$\Delta I_F / \Delta T_a$		-0.67	mA/°C
	Input forward current (pulsed)		I <sub>FP</sub>	(Note 1)	50	mA
	Input forward current derating (pulsed)	$(T_a \ge 110 \ ^\circ C)$	$\Delta I_{FP} / \Delta T_a$		-1	mA/°C
	Input power dissipation		PD		40	mW
	Input power dissipation derating	$(T_a \ge 110 \ ^\circ C)$	$\Delta P_D / \Delta T_a$		-1.0	mW/°C
	Input reverse voltage		V <sub>R</sub>		5	V
Detector	Output current		Ι <sub>Ο</sub>		25	mA
	Output voltage		Vo		6	V
	Supply voltage		V <sub>CC</sub>		6	
	Output power dissipation		Po		60	mW
	Output power dissipation derating	$(T_a \ge 110 \ ^\circ C)$	$\Delta P_0 / \Delta T_a$		-1.5	mW/°C
Common	Operating temperature		T <sub>opr</sub>		-40 to 125	°C
	Storage temperature		T <sub>stg</sub>		-55 to 125	
	Lead soldering temperature	(10 s)	T <sub>sol</sub>		260	
	Isolation voltage (A	AC, 60 s, R.H. ≤ 60 %)	BV <sub>S</sub>	(Note 2)	5000	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: This device is considered as a two-terminal device: Pins 1, 2 and 3 are shorted together, and pins 4, 5 and 6 are shorted together.

#### 8. Recommended Operating Conditions (Note)

Characteristics	Symbol	Note	Min	Тур.	Max	Unit
Input on-state current	I <sub>F(ON)</sub>	(Note 1)	7.5	_	14	mA
Input off-state voltage	V <sub>F(OFF)</sub>		0	_	0.8	V
Supply voltage	V <sub>CC</sub>	(Note 2)	2.7	3.3 / 5.0	5.5	
Operating temperature	T <sub>opr</sub>	(Note 2)	-40	—	125	°C

Note: The recommended operating conditions are given as a design guide necessary to obtain the intended performance of the device. Each parameter is an independent value. When creating a system design using this device, the electrical characteristics specified in this datasheet should also be considered.

Note: A ceramic capacitor (0.1 µF) should be connected between pin 4 and pin 6 to stabilize the operation of a highgain linear amplifier. Otherwise, this photocoupler may not switch properly. The bypass capacitor should be placed within 1 cm of each pin.

Note 1: The rise and fall times of the input on-current should be less than 0.5  $\mu s.$ 

Note 2: Denotes the operating range, not the recommended operating condition.

Note 1: Pulse width (PW)  $\leq$  1 ms, duty = 50 %

#### Electrical Characteristics (Note) (Unless otherwise specified, T<sub>a</sub> = -40 to 125 °C, V<sub>CC</sub> = 2.7 to 5.5 V)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input forward voltage	V <sub>F</sub>		I <sub>F</sub> = 10 mA, T <sub>a</sub> = 25 °C	1.45	1.55	1.70	V
Input forward voltage temperature coefficient	$\Delta V_F / \Delta T_a$		I <sub>F</sub> = 10 mA	—	-2.0	—	mV/°C
Input reverse current	I <sub>R</sub>		V <sub>R</sub> = 5 V, T <sub>a</sub> = 25 °C	—	_	10	μA
Input capacitance	Ct		V <sub>S</sub> = 0 V, f = 1 MHz, T <sub>a</sub> = 25 °C	_	20	_	pF
High-level output current	I <sub>OH</sub>	Fig.	$V_{CC} = V_{O} = 5.5 \text{ V}, \text{ V}_{F} = 0.8 \text{ V}$	—	—	50	μA
		12.1.1	V <sub>CC</sub> = V <sub>O</sub> = 5.5 V, V <sub>F</sub> = 0.8 V, Ta = 25 ℃	_	_	10	
Low-level output voltage	V <sub>OL</sub>	Fig. 12.1.2	I <sub>F</sub> = 10 mA, I <sub>O</sub> = 13 mA	—	—	0.6	V
High-level supply current	I <sub>CCH</sub>	Fig. 12.1.3	I <sub>F</sub> = 0 mA	—	—	4	mA
Low-level supply current	I <sub>CCL</sub>	Fig. 12.1.4	I <sub>F</sub> = 10 mA	_	-	4	
Threshold input current (H/L)	I <sub>FHL</sub>		I <sub>O</sub> = 13 mA, V <sub>O</sub> < 0.6 V	_	_	5.0	

Note: All typical values are at  $T_a = 25$  °C.

#### 10. Isolation Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Total capacitance (input to output)	CS	(Note 1)	V <sub>S</sub> = 0 V, f = 1 MHz	_	1.0	—	pF
Isolation resistance	R <sub>S</sub>	(Note 1)	$V_S$ = 500 V, R.H. $\leq$ 60 %	1×10 <sup>12</sup>	1014	_	Ω
Isolation voltage	BVS	(Note 1)	AC, 60 s	5000	_		Vrms
			AC, 1 s in oil	_	10000		
			DC, 60 s in oil	_	10000	_	Vdc

Note 1: This device is considered as a two-terminal device: Pins 1, 2 and 3 are shorted together, and pins 4, 5 and 6 are shorted together.

## 11. Switching Characteristics (Unless otherwise specified, $T_a = -40$ to 125 °C, $V_{CC} = 2.7$ to 5.5 V)

Characteristics	Symbol	Note	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Propagation delay time (H/L)	t <sub>pHL</sub>	(Note 1)	Fig. 12.1.5	$I_F$ = 7.5 mA, C <sub>L</sub> = 15 pF, R <sub>L</sub> = 350 Ω	_	_	60	ns
Propagation delay time (L/H)	t <sub>pLH</sub>				—	_	60	
Pulse width distortion	t <sub>pHL</sub> -t <sub>pLH</sub>				_		35	
Propagation delay skew (device to device)	t <sub>psk</sub>	(Note 1), (Note 2)			-40	_	40	
Fall time	t <sub>f</sub>	(Note 1)			_	30	_	
Rise time	tr				_	30	_	
Common-mode transient immunity at output high	CM <sub>H</sub>		Fig. 12.1.6	$I_F = 0 \text{ mA}, V_{CC} = 5 \text{ V},$ $V_{CM} = 1000 \text{ V}_{p-p}, T_a = 25 \text{ °C}$	±20	±25	_	kV/μs
Common-mode transient immunity at output low	CML			I <sub>F</sub> = 10 mA, V <sub>CC</sub> = 5 V, V <sub>CM</sub> = 1000 V <sub>p-p</sub> , T <sub>a</sub> = 25 °C	±20	±25		

Note: All typical values are at  $T_a = 25$  °C.

Note 1: f = 5 MHz, duty = 50 %, input current  $t_r = t_f = 5$  ns,  $C_L$  is approximately 15 pF which includes probe and stray wiring capacitance.

Note 2: The propagation delay skew, t<sub>psk</sub>, is equal to the magnitude of the worst-case difference in t<sub>pHL</sub> and/or t<sub>pLH</sub> that will be seen between units at the same given conditions (supply voltage, input current, temperature, etc).

#### 12. Test Circuits and Characteristics Curves

#### 12.1. Test Circuits

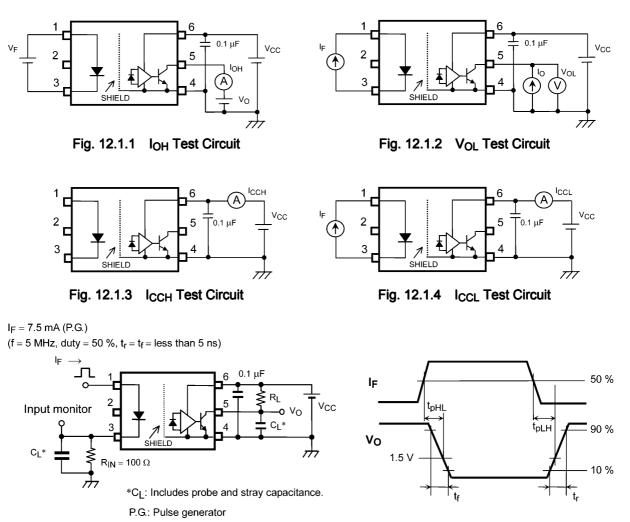


Fig. 12.1.5 Switching Time Test Circuit and Waveform

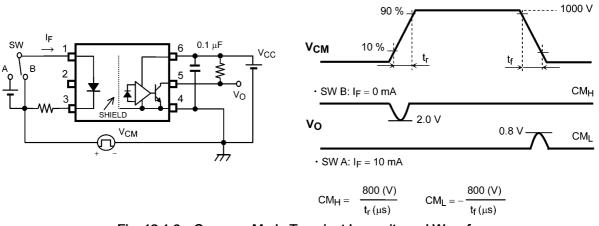


Fig. 12.1.6 Common-Mode Transient Immunity and Waveform

#### 13. Soldering and Storage

#### 13.1. Precautions for Soldering

The soldering temperature should be controlled as closely as possible to the conditions shown below, irrespective of whether a soldering iron or a reflow soldering method is used.

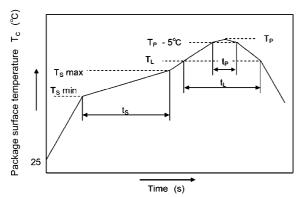
• When using soldering reflow.

The soldering temperature profile is based on the package surface temperature.

(See the figure shown below, which is based on the package surface temperature.)

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.



	Symbol	Min	Max	Unit
Preheat temperature	Ts	150	200	°C
Preheat time	ts	60	120	s
Ramp-up rate ( $T_L$ to $T_P$ )			3	°C/s
Liquidus temperature	TL	217		°C
Time above $T_L$	tL	60	150	s
Peak temperature	Τ <sub>Ρ</sub>		260	°C
Time during which $T_c$ is between ( $T_P - 5$ ) and $T_P$	t <sub>P</sub>		30	s
Ramp-down rate $(T_P \text{ to } T_L)$			6	°C/s

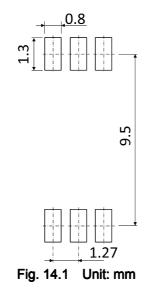
Fig. 13.1.1 An Example of a Temperature Profile When Lead(Pb)-Free Solder Is Used

- When using soldering flow Preheat the device at a temperature of 150 °C (package surface temperature) for 60 to 120 seconds. Mounting condition of 260 °C within 10 seconds is recommended. Flow soldering must be performed once.
- When using soldering Iron (Applicable to both eutectic solder and Lead(Pb)-Free solder) Complete soldering within 10 seconds for lead temperature not exceeding 260 °C or within 3 seconds not exceeding 350 °C Heating by soldering iron must be done only once per lead.

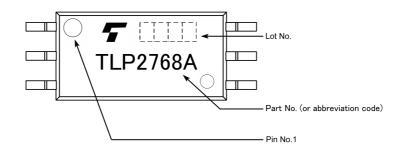
#### 13.2. Precautions for General Storage

- Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- Follow the precautions printed on the packing label of the device for transportation and storage.
- Keep the storage location temperature and humidity within a range of 5  $^{\circ}\mathrm{C}$  to 35  $^{\circ}\mathrm{C}$  and 45 % to 75 %, respectively.
- Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- When restoring devices after removal from their packing, use anti-static containers.
- Do not allow loads to be applied directly to devices while they are in storage.
- If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

14. Land Pattern Dimensions (for reference only)



#### 15. Marking



#### 16. EN60747-5-5 Option (D4) Specification

- Part number: TLP2768A (Note 1)
  - The following part naming conventions are used for the devices that have been qualified according to option (D4) of EN60747.

Example: TLP2768A(D4-TP,E(T

D4: EN60747 option

TP: Tape type

E: [[G]]/RoHS COMPATIBLE (Note 2)

(T: Domestic ID (Country / Region of origin: Thailand)

Note 1: Use TOSHIBA standard type number for safety standard application.

e.g., TLP2768A(D4-TP,E(T  $\rightarrow$  TLP2768A

Note 2: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.

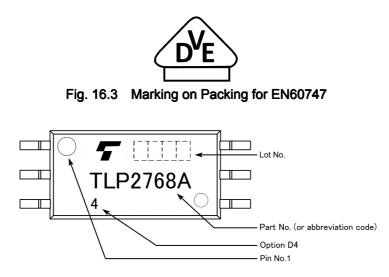
Description	Symbol	Rating	Unit
Application classification			
for rated mains voltage $\leq$ 600 Vrms for rated mains voltage $\leq$ 1000 Vrms		I-IV I-III	-
Climatic classification		55 / 125 / 21	_
Pollution degree		2	_
Maximum operating insulation voltage	VIORM	1230	Vpeak
Input to output test voltage, Method A $V_{pr}$ = 1.6 × $V_{IORM}$ , type and sample test $t_p$ = 10 s, partial discharge < 5 pC	Vpr	1970	Vpeak
Input to output test voltage, Method B $V_{pr}$ =1.875 × $V_{IORM}$ , 100 % production test $t_p$ = 1 s, partial discharge < 5 pC	Vpr	2310	Vpeak
Highest permissible overvoltage (transient overvoltage, t <sub>pr</sub> = 60 s)	V <sub>TR</sub>	8000	Vpeak
Safety limiting values (max. permissible ratings in case of fault, also refer to thermal derating curve) current (input current I <sub>F</sub> , P <sub>SO</sub> = 0) power (output or total power dissipation) temperature	I <sub>si</sub> P <sub>so</sub> T <sub>s</sub>	250 400 150	mA mW °C
Insulation resistance $V_{IO} = 500 \text{ V}, \text{ T}_a = 25 \text{ °C}$ $V_{IO} = 500 \text{ V}, \text{ T}_a = 100 \text{ °C}$ $V_{IO} = 500 \text{ V}, \text{ T}_a = \text{ T}_s$	R <sub>si</sub>	≥ 10 <sup>12</sup> ≥ 10 <sup>11</sup> ≥ 10 <sup>9</sup>	Ω

Fig. 16.1	EN60747 Isol	ation Characteristics
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Minimum creepage distance	Cr	8.0 mm
Minimum clearance	CI	8.0 mm
Minimum insulation thickness	ti	0.4 mm
Comparative tracking index	CTI	175

Fig. 16.2 Insulation Related Sp	pecifications (Note)
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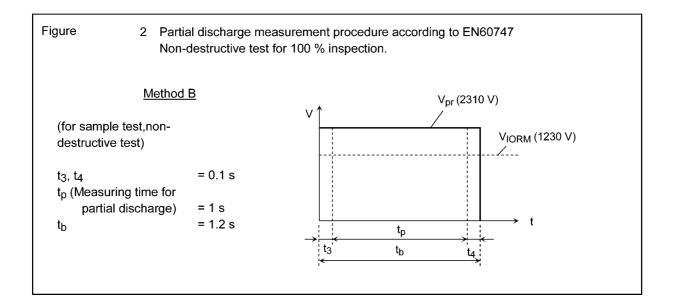
Note: This photocoupler is suitable for **safe electrical isolation** only within the safety limit data. Maintenance of the safety data shall be ensured by means of protective circuits.

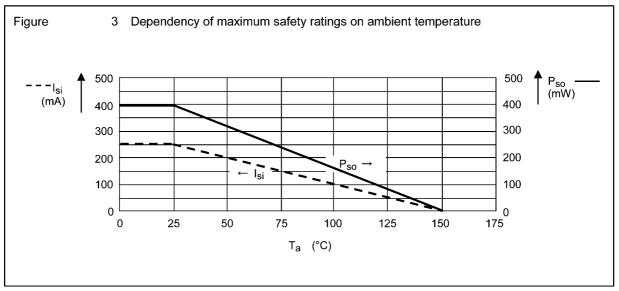




Note: When an EN60747-5-5 approved type is needed, please designate the Option (D4).

5	-	asurement procedure according to EN60747 alification and sampling tests.
Metho	<u>d A</u>	VINITIAL(8 kV)
(for type and sampling destructive tests)	tests,	V V <sub>pr</sub> (1970 V)
t <sub>1</sub> , t <sub>2</sub>	= 1 to 10 s	V <sub>IORM</sub> (1230 V)
t3, t4	= 1 s	
t <sub>p</sub> (Measuring time for		
partial discharge)	= 10 s	$0 \xrightarrow{\mu} t$
t <sub>b</sub>	= 12 s	$t_3$ $t_p$ $t_4$
t <sub>ini</sub>	= 60 s	$t_1$ $t_{ini}$ $t_2$ $t_b$







#### 17. Specifications for Embossed-Tape Packing (TP)

#### 17.1. Applicable Package

Package Name	Product Type
SO6L	long creepage distance mini-flat coupler

#### **17.2. Product Naming Conventions**

Type of package used for shipment is denoted by a symbol suffix after a part number. The method of classification is as below.

Example: TLP2768A(TP,E(T

Part number: TLP2768A

Tape type: TP

[[G]]/RoHS COMPATIBLE: E (Note)

Domestic ID (Country / Region of origin: Thailand): (T

Note: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

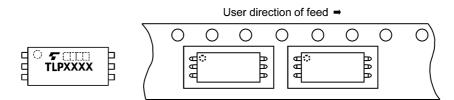
RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.

#### 17.3. Tape Specifications

Таре Туре	Division	Packing Amount (A unit per reel)
TP	—	1500

#### 17.3.1. Orientation of Device in Relation to Direction of Feed

Device orientation in the carrier cavities as shown in the following figure.





#### 17.3.2. Empty Cavities

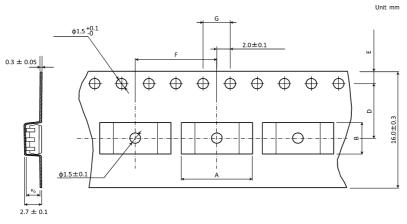
Characteristics	Criterion	Remarks
Occurrences of 2 or more successive empty cavities	0 device	Within any given 40-mm section of tape, not including leader and trailer
Single empty cavity	6 devices (max) per reel	Not including leader and trailer

#### 17.3.3. Tape Leader and Trailer

The start end of the tape has 14 or more empty cavities. The hub end of the tape has 34 or more empty cavities and a cover tape is attached 30 mm or more.

#### 17.3.4. Tape Dimensions

Tape material: Plastic (for protection against static electricity)



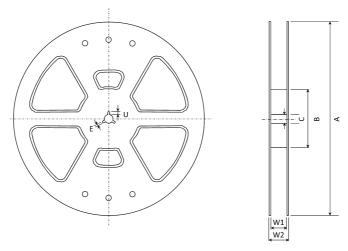


Symbol	Dimension	Remark
А	10.4	—
В	4.24	—
D	7.5	Center line of embossed cavity and sprocket hole
E	1.75	Distance between tape edge and sprocket hole center
F	12.0	Cumulative error +0.1/10 pitch, -0.3/10 pitch
G	4.0	Cumulative error +0.1/10 pitch, -0.3/10 pitch
K <sub>0</sub>	2.4	Internal space

### 17.3.5. Reel Specification

Material: Plastic

TOSHIBA





Symbol	Dimension
А	φ330±2.0
В	φ100±1.0
С	φ13±0.5
E	2.0±0.5
U	4.0±0.5
W1	17.4±0.5
W2	21.4±1.0

#### 17.4. Packing (Note)

Either one reel or ten reels (max) of photorelays are packed in a shipping carton.

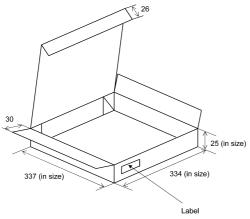
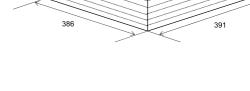


Fig. 17.4.1 1 reel/carton (unit: mm)





[10 reel]



#### 17.5. Label Format

(1) Carton: The label provides the part number, quantity, lot number, the Toshiba logo, etc.

(2) Reel: The label provides the part number, the taping name, quantity, lot number, etc.

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#### 17.6. Ordering Information

When placing an order, please specify the part number, tape type and quantity as shown in the following example.

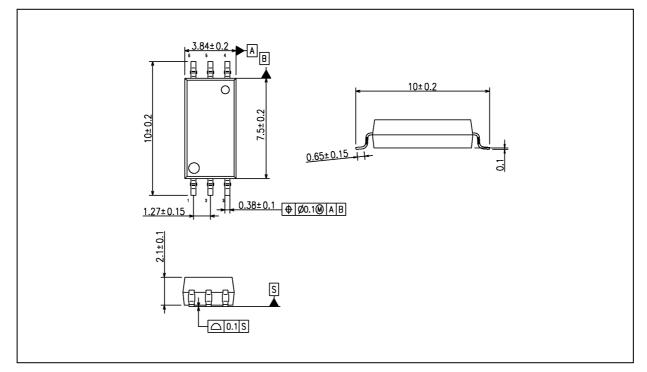
Example: TLP2768A(TP,E(T 1500 pcs

Part number: TLP2768A Tape type: TP(12 mm pitch) [[G]]/RoHS COMPATIBLE: E **(Note)** Quantity (must be a multiple of 1500): 1500 pcs Domestic ID (Country / Region of origin: Thailand): (T

Note: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility. RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.

### Package Dimensions

Unit: mm



Weight: 0.126 g (typ.)

	Package Name(s)
TOSHIBA: 11-4N1A	

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