Specification

Part Number:	
Version:	
Date:	

Revision

VERSION	DATE	REVISED PAGE NO.	Note
0	2016/05/25		First issue

Contents

2.Summary

This technical specification applies to 3.45' color TFT-LCD panel. The 3.45' color TFT-LCD panel is designed for camcorder, digital camera application and other electronic products which require high quality flat panel displays. This module follows RoHS.

Midas Active Matrix Display Part Number System

MC	T	057	A	6	*	W	320240	L	M	L	*	*
1	2	3	4	5	6	7	8	9	10	11	12	13

1   =   **MC:** Midas Components

2   =   **T:** TFT **A:** Active Matrix OLED **M:** Monitor

3   =   **Size**

4   =   **Series**

5   =   **Viewing Angle: 6:** 6 O'clock **12:** 12 O'clock **O:** All Round Viewing Angle

6   =   **Blank:** No Touch **T:** Resistive Touchscreen **C:** Capacitive Touchscreen

7   =   **Operating Temp Range:**   **S:** 0+50Deg C   **B:** -20+60Deg C
   W: -20+70Deg C   **E:** -30+85Deg C
   X: -30+80Deg C

8   =   **No of Pixels**

9   =   **Orientation:**   **P:** Portrait   **L:** Landscape

10   =   **Mode:**   **R:** Reflective   **M:** Transmissive   **T:** Transflective
  S: Sunlight Readable (Transmissive) **W:** White on Black (Monochrome)

11   =   **Backlight:**   **Blank:** None **L:** LED **C:** CCFL

12   =   **Blank:** No Module/board **C:** Controller board module (E-Tech)

13   =   **Blank:** None **OB:** Optically Bonded **IPS:** In-plane switching

3.General Specifications

- Size: 3.5 inch

- Dot Matrix: 320 x RGBx240(TFT) dots

- Module dimension: 76.9x 63.9x 4.52 mm

- Active area: 70.08 x 52.56 mm

- Dot pitch: 0.073 x 0.219 mm

- LCD type: TFT, Normally White, Transmissive

- View Direction: 12o'clock

- Gray Scale Inversion Direction: 6 o'clock

- Backlight Type: LED ,Normally White

- CTP FW Version: 0A

- With /Without TP: With CTP

- Surface: Glare

*Color tone slight changed by temperature and driving voltage.

4.Interface

4.1. LCM PIN Definition

Pin	Symbol	Function	Remark
1	VLED-	Power for LED backlight cathode	
2	VLED+	Power for LED backlight anode	
3	DGND	System ground pin of the IC. Connect to system ground.	
4	VCC	Power Supply	
5	R0	Red Data bit(LSB)	
6	R1	Red Data bit	
7	R2	Red Data bit	
8	R3	Red Data bit	
9	R4	Red Data bit	
10	R5	Red Data bit	
11	R6	Red Data bit	
12	R7	Red Data bit (MSB)	
13	G0	Green Data bit(LSB)	
14	G1	Green Data bit	
15	G2	Green Data bit	
16	G3	Green Data bit	
17	G4	Green Data bit	
18	G5	Green Data bit	
19	G6	Green Data bit	
20	G7	Green Data bit (MSB)	
21	B0	Blue Data bit(LSB)	
22	B1	Blue Data bit	
23	B2	Blue Data bit	
24	B3	Blue Data bit	
25	B4	Blue Data bit	
26	B5	Blue Data bit	
27	B6	Blue Data bit	
28	B7	Blue Data bit (MSB)	
29	AVSS	Grounding for analog circuit Connect to system ground	
30	CLK	Dot-clock signal and oscillator source	
31	NC	No connect	
32	HSYNC	Horizontal sync signal	Note1
33	VSYNC	Vertical sync signal	Note1
34	DE	Data Enable signal	Note1
35	NC	No connect	
36	RESET	Hardware reset	
37	NC	No connect	
38	NC	No connect	
39	NC	No connect	
40	NC	No connect	

Note1:
For digital 24Bit RGB input data format, both SYNC mode and DE mode are supported. If DE signal is fixed low, SYNC mode is used. Otherwise,DE mode is used. Suggest used SYNC mode!!

Mode	D[23:16]	D[15:8]	D[7:0]	IHS	IVS	DEN
24 bit RGB	R[7:0]	G[7:0]	B[7:0]	HSYNC	VSYNC	DE signal is fixed low for SYNC mode
				Floating if not used	Floating if not used	DE for DE Mode

4.2. Basic Display Color and Gray Scale

							Input Color Data																		
Color		Red (MSB ... LSB)								Green (MSB ... LSB)								Blue (MSB ... LSB)								
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Red(0) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255) Bright	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Green	Green(0) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)Bright	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Blue	Blue(0) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255) Bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

4.3. CTP PIN Definition

Pin	Symbol	Function	Remark
1	VSS	System ground pin of the IC. Connect to system ground.	
2	VDDT	Power Supply : +3.3V	
3	SCL	SPI Slave mode, chip select, active low / I2C clock input	
4	NC	No connect	
5	SDA	SPI Slave mode, data input / I2C data input and output	
6	NC	No connect	
7	/RST	External Reset, Low is active	
8	/WAKE	External interrupt from the host	
9	/INT	External interrupt to the host	
10	VSS	System ground pin of the IC. Connect to system ground.	

5.Contour Drawing

PIN NO.	SYMBOL	PIN NO.	SYMBOL
1	VLED-	21	B0
2	VLED+	22	B1
3	DGND	23	B2
4	VCC	24	B3
5	R0	25	B4
6	R1	26	B5
7	R2	27	B6
8	R3	28	B7
9	R4	29	AVSS
10	R5	30	CLK
11	R6	31	NC
12	R7	32	Hsync
13	G0	33	Vsync
14	G1	34	DE
15	G2	35	NC
16	G3	36	RESET
17	G4	37	NC
18	G5	38	NC
19	G6	39	NC
20	G7	40	NC

PIN NO.	SYMBOL
1	VSS
2	VDDT
3	SCL
4	NC
5	SDA
6	NC
7	/RST
8	/WAKE
9	/INT
10	VSS

The non-specified tolerance of dimension is ±0.3mm.
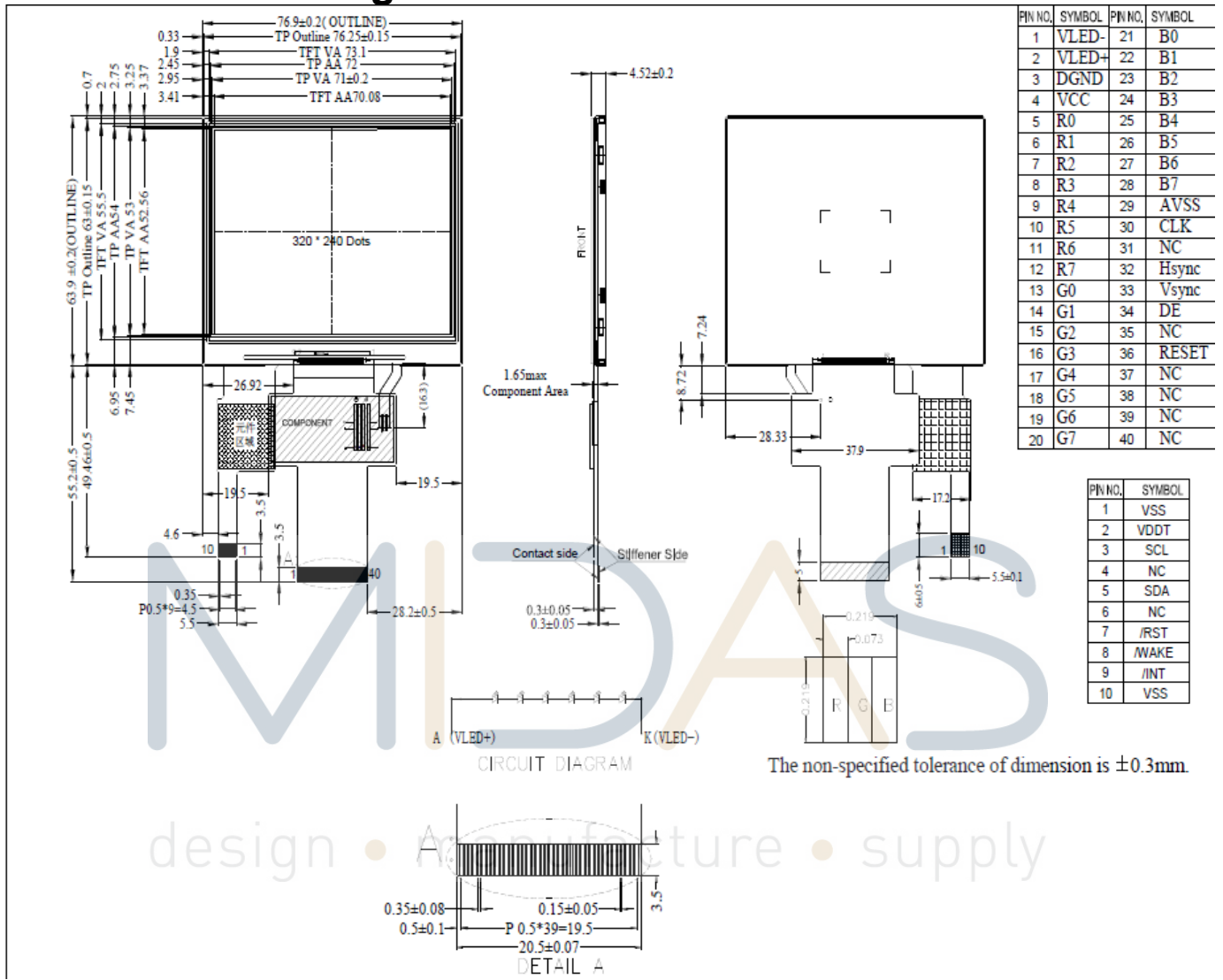
6.Block Diagram

LCD Panel
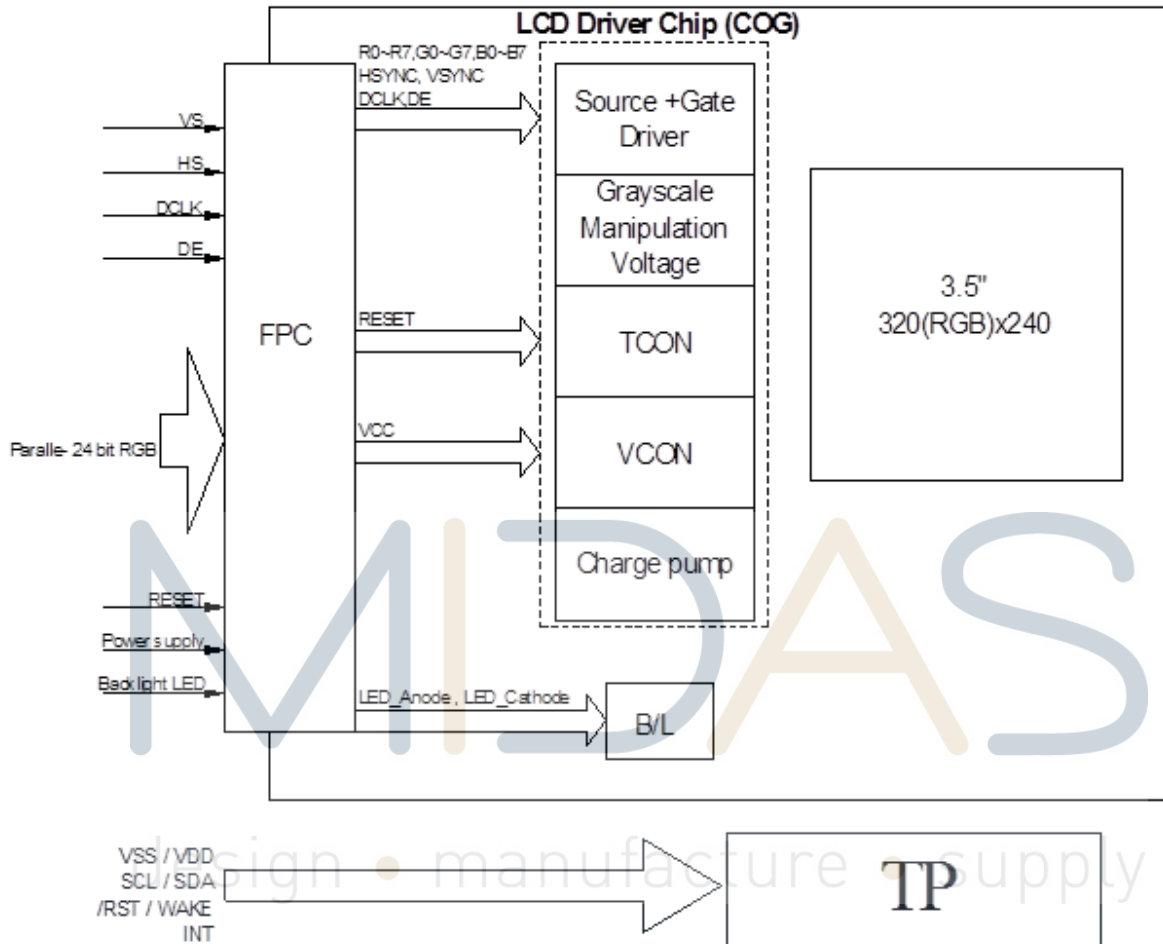
7.Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	
Storage Temperature	TST	-30	—	+80	

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above
1.   Temp. ≦60℃, 90% RH MAX. Temp.＞60 , Absolute humidity shall be less than 90% RH at 60
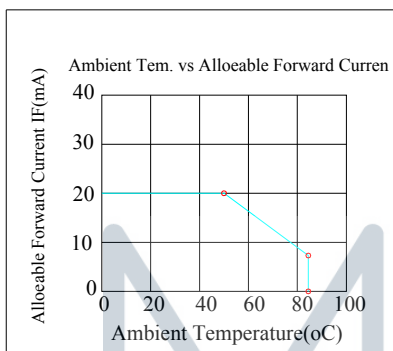
8.Electrical Characteristics

8.1. Operating conditions:

Item	Symbol	Condition	Min	Typ	Max	Unit	Remark
Supply Voltage For LCM	VCC	—	3.0	3.3	3.6	V	
Supply Current For LCM	ICC	—	—	12	18	mA	Note 1
Supply Voltage For Touch Logic	VDDT	—	2.8	-	3.3	V	

Note 1 : This value is test for VCC =3.3V , Ta=25 ℃ only

8.2. LED driving conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
LED current		-	20	-	mA	
Power Consumption		348	384	408	mW	
LED voltage	LED+	17.4	19.2	20.4	V	Note 1
LED Life Time		-	50,000	-	Hr	Note 2,3,4

Note 1 : There are 1 Groups LED
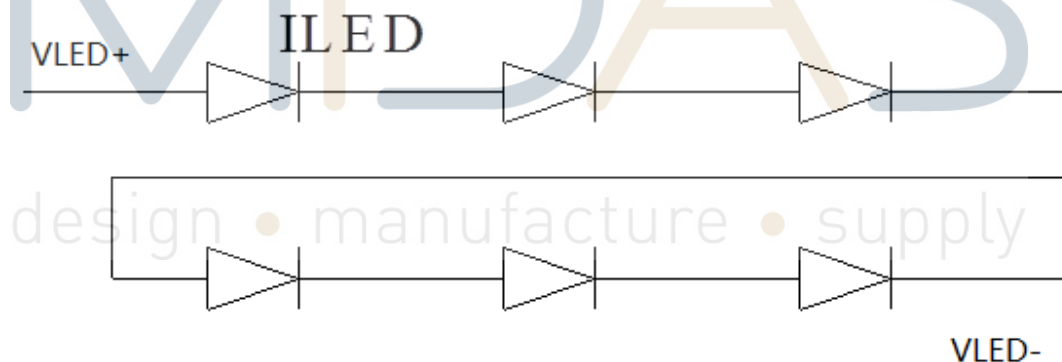
Note 2 : Ta = 25 ℃
Note 3 : Brightness to be decreased to 50% of the initial value
Note 4 : The single LED lamp case

9.DC CHARATERISTICS

Parameter	Symbol	Rating			Unit	Condition
		Min	Typ	Max		
Low level input voltage	V_{IL}	0	-	0.3VCC	V	
High level input voltage	V_{IH}	0.7VCC	-	VCC	V	

10.AC Characteristics

Digital Parallel RGB interface

Signal	Item	Symbol	Min	Typ	Max	Unit
Dclk	Frequency	Tosc	-	6.5	10	MHz
	High Time	Tch	-	77	-	ns
	Low Time	Tcl	-	77	-	ns
Data	Setup Time	Tsu	12	-	-	ns
	Hold Time	Thd	12	-	-	ns
Hsync	Period	TH	-	408		Tosc
	Pulse Width	THS	5	30	-	Tosc
	Back-Porch	Thb	-	38	-	Tosc
	Display Period	TEP	-	320	-	Tosc
	Hsync-den time	THE	36	68	88	-
	Front-Porch	Thf	-	20	-	Tosc
Vsync	Period	Tv	-	262	-	TH
	Pulse Width	Tvs	1	3	5	TH
	Back-Porch	Tvb	-	15	-	TH
	Display Period	Tvd	-	240	-	TH
	Front-Porch	Tvf	2	4	-	TH

Note:

1. Thp + Thb = 68, the user is make up by yourself.

2. Tv = Tvs + Tvb + Tvd + Tvf , the user is make up by yourself.

3.When SYNC mode is used,1st data start from 68th Dclk after Hsync falling

10.1. Waveform
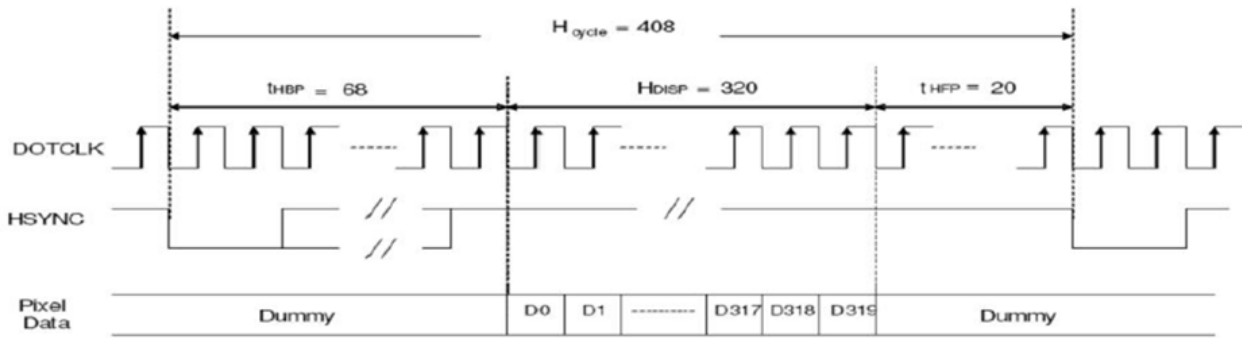
a) Horizontal Data Transaction Timing
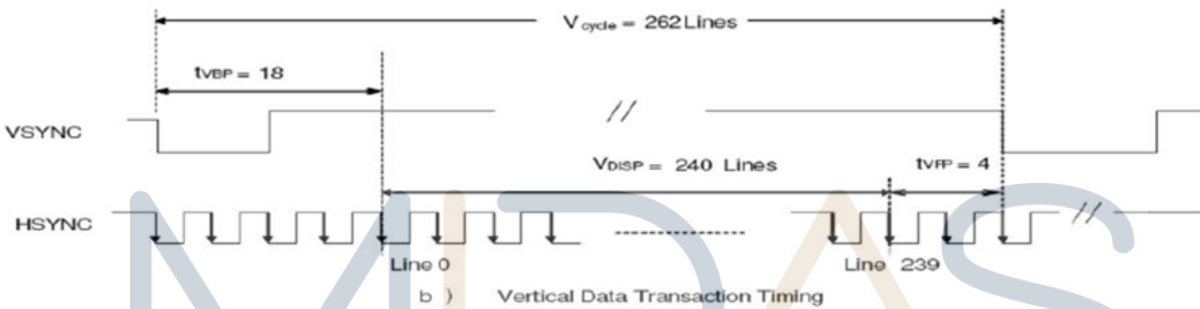
b) Vertical Data Transaction Timing

Figure 7.1.1 Data Transaction Timing in Parallel RGB (24 bit) Interface (SYNC Mode)
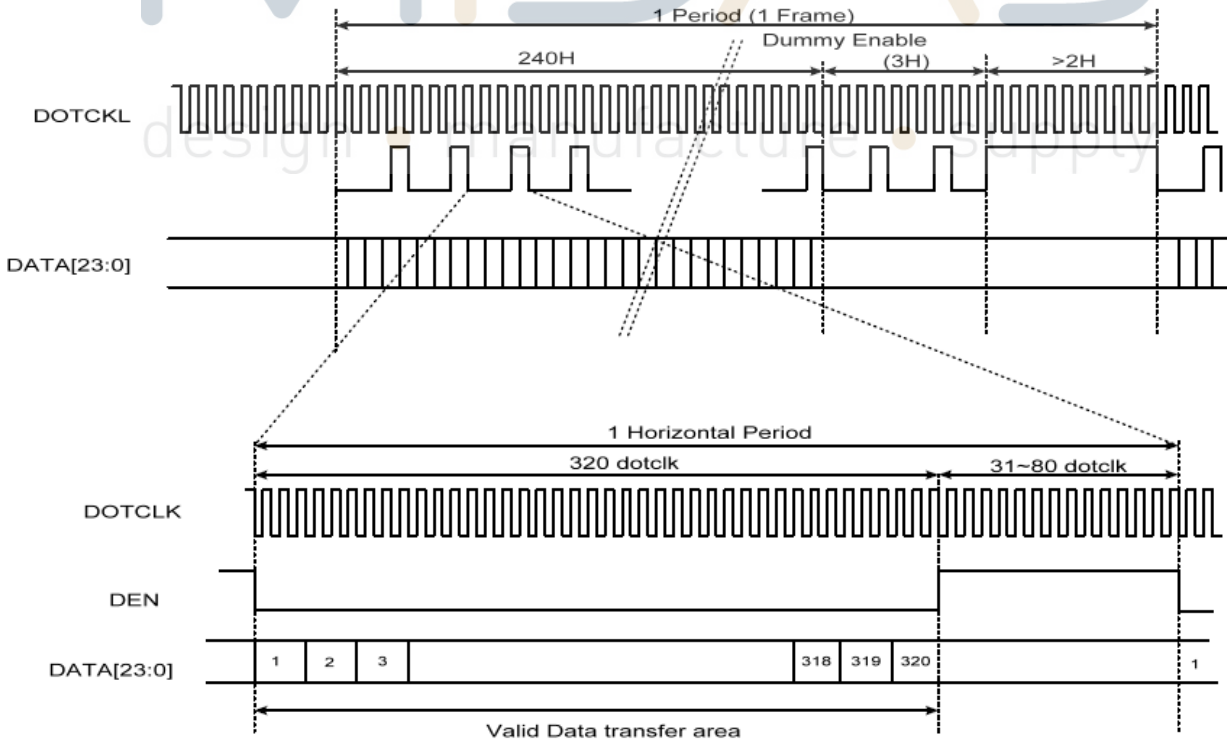
Figure 7.1.2 Data Transaction Timing in Parallel RGB (24 bit) Interface (DE Mode)

10.2. Clock and Sync waveforms
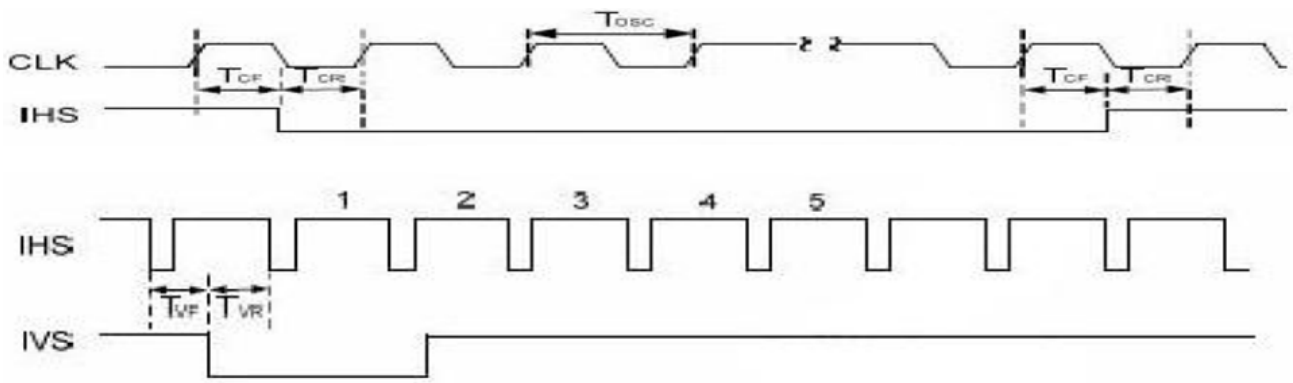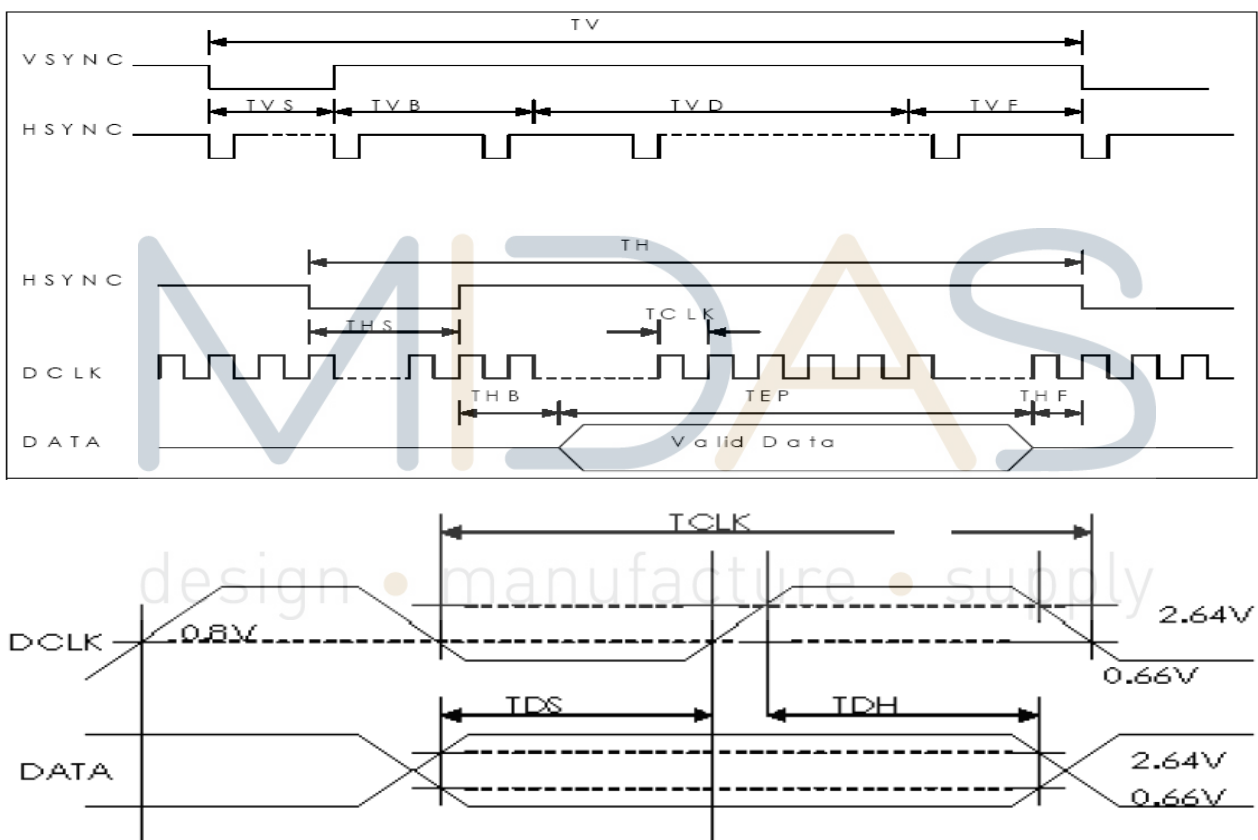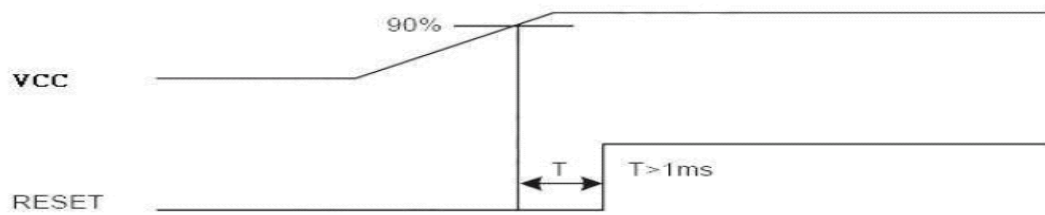
Figure 7.2.1 IHS and IVS timing waveforms

Figure 7.2.2 TV and TH timing waveforms

10.3. Reset Timing Chart
The RESET input must be held at least 1ms after power is stable

Reset timing

11.Optical Characteristics

Item		Symbol	Condition.	Min	Typ	Max	Unit	Remark
Response time		Tr	θ=0°、Φ=0°	-	10	-	ms	Note 3,5
		Tf		-	15	-	ms	
Contrast ratio		CR	At optimized viewing angle	300	350	-	-	Note 4,5
Color Chromaticity	White	Wx	θ=0°、Φ=0	0.26	0.31	0.36	-	Note 2,6,7
		Wy		0.28	0.33	0.38	-	
Viewing angle (Gray Scale Inversion Direction)	Hor.	ΘR	CR≧10	-	75	-	Deg.	Note 1
		ΘL		-	75	-		
	Ver.	ΦT		-	75	-		
		ΦB		-	75	-		
Brightness		-	-	200	250	-	cd/m²	Center of display

Ta=25±2 , IL=20mA

Note 1: Definition of viewing angle range
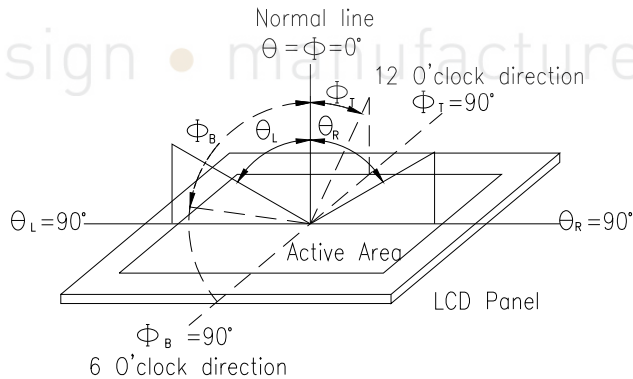
Fig. 11.1. Definition of viewing angle

Note 2: Test equipment setup:
After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7orBM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.
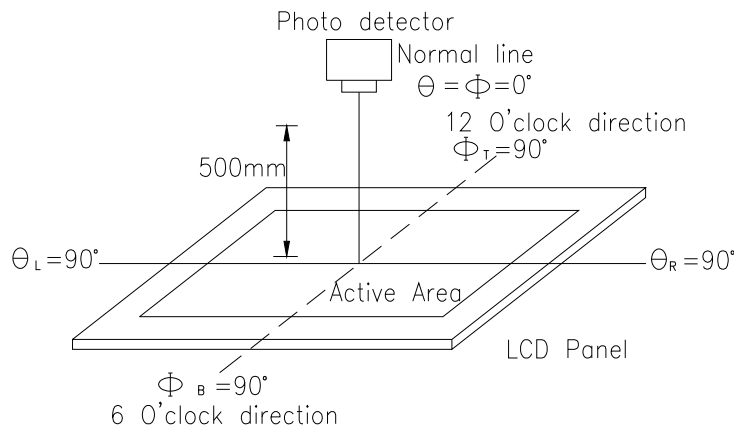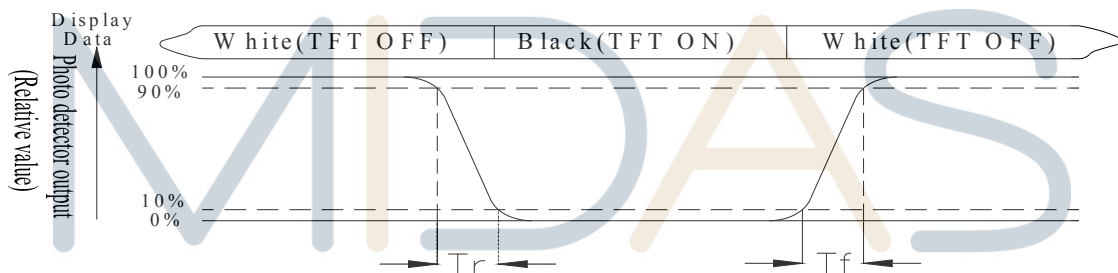
Fig. 11.2. Optical measurement system setup

Note 3: Definition of Response time:

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time, Tr, is the time between photo detector output intensity changed from 90%to 10%. And fall time, Tf, is the time between photo detector output intensity changed from 10%to 90%

Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: White Vi = Vi50 ± 1.5V

Black Vi = Vi50 ± 2.0V

"±" means that the analog input signal swings in phase with VCOM signal.

"±" means that the analog input signal swings out of phase with VCOM signal.

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 6: Definition of color chromaticity (CIE 1931)

Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

12.Reliability
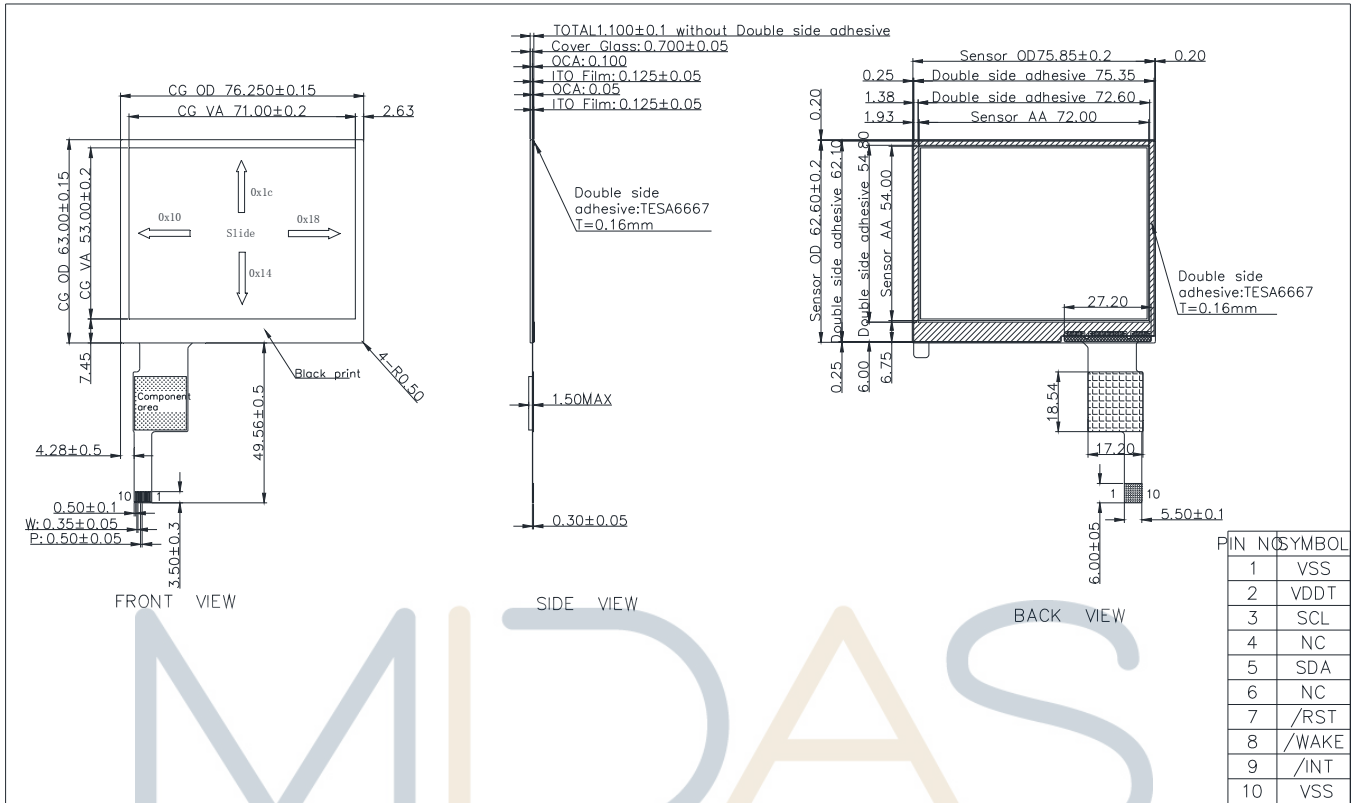
Content of Reliability Test (Wide temperature, -20 ~70)

Environmental Test			
Test Item	**Content of Test**	**Test Condition**	**Note**
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70 200hrs	——
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60 ,90%RH max	60 ,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20　　25　　70 30min　5min　30min 1 cycle	-20 /70 10 cycles	——
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 15mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact) ,±800v(air), RS=330Ω CS=150pF 10 times	——

Note1: No dew condensation to be observed.
Note2: The function test shall be conducted after 4 hours storage at the normal
　　　　Temperature and humidity after remove from the test chamber.
Note3: The packing have to including into the vibration testing.
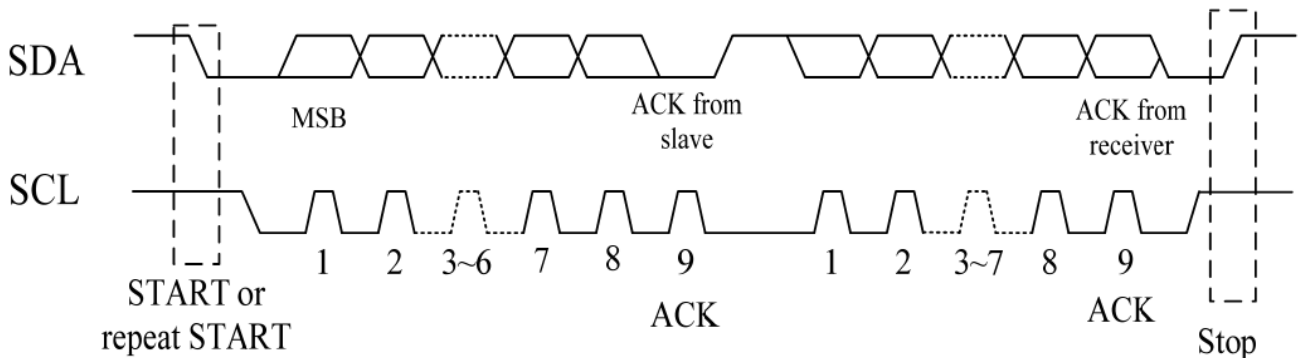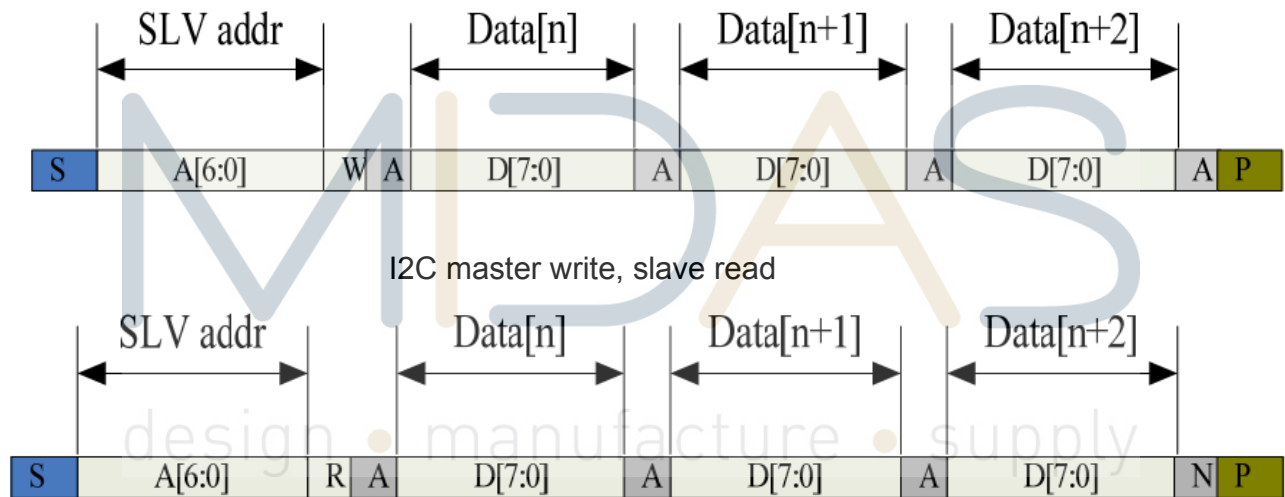
13.Touch Panel Information

CG OD 76.250±0.15
CG VA 71.00±0.2   2.63
CG OD 63.00±0.15
CG VA 53.00±0.2

0x1c
0x10   Slide   0x18
0x14

Black print
4-R0.50

7.45
Component area
49.56±0.5
4.28±0.5

10   1
0.50±0.1
W:0.35±0.05
P:0.50±0.05
3.50±0.3

FRONT VIEW

TOTAL1.100±0.1 without Double side adhesive
Cover Glass:0.700±0.05
OCA:0.100
ITO Film:0.125±0.05
OCA:0.05
ITO Film:0.125±0.05

Double side adhesive:TESA6667 T=0.16mm

1.50MAX

0.30±0.05

SIDE VIEW

Sensor OD75.85±0.2   0.20
0.25   Double side adhesive 75.35
1.38   Double side adhesive 72.60
1.93   Sensor AA 72.00
0.20

Sensor OD 62.60±0.2
Double side adhesive 62.10
Double side adhesive 54.50
Sensor AA 54.00

Double side adhesive:TESA6667 T=0.16mm

0.25
6.00
6.75

27.20

18.54
17.20

1   10
5.50±0.1

6.00±0.05

BACK VIEW

PIN NO	SYMBOL
1	VSS
2	VDDT
3	SCL
4	NC
5	SDA
6	NC
7	/RST
8	/WAKE
9	/INT
10	VSS

13.1. CTP I2C Timing:

I2C Serial Data Transfer Format

I2C master write, slave read

I2C master read, slave write

Mnemonics	Description
S	12C Start or 12C Restart
A[6:0]	Slave address
R/W	READ/WRITE bit, '1' for read, '0' for write
A(N)	ACK(NACK) bit
P	STOP :the indication of the end of a packet(if this bit is missing, S will indicate the end of the current packet and beginning of the next packet)

Lists the meanings of the mnemonics used in the above figures

Parameter	Unit	Min	Max

| SCL frequency	KHz	0	400
Bus free time between a STOP and START condition	us	4.7	\
Hold time (repeated) ST ART condition	us	4.0	\
Data setup time	ns	250	\
Setup time for a repeated START condition	us	4.7	\
Setup time for STOP condition	us	4.0	\

Interface Timing Characteristics

AS FOR STANDARD CTPM, HOST NEED TO USE BOTH INTERRUPT CONTROL SIGNAL AND SERIAL DATA INTERFACE TO GET THE TOUCH DATA.
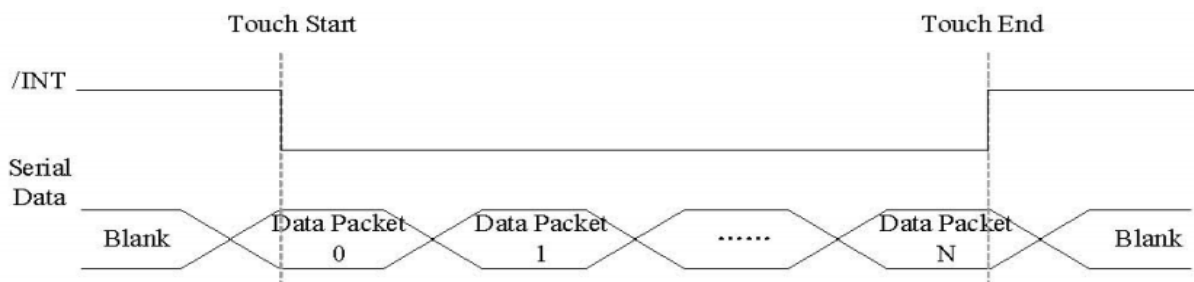
HERE IS THE TIMING TO GET TOUCH DATA.

13.2. WRITE BYTES TO I2C SLAVE
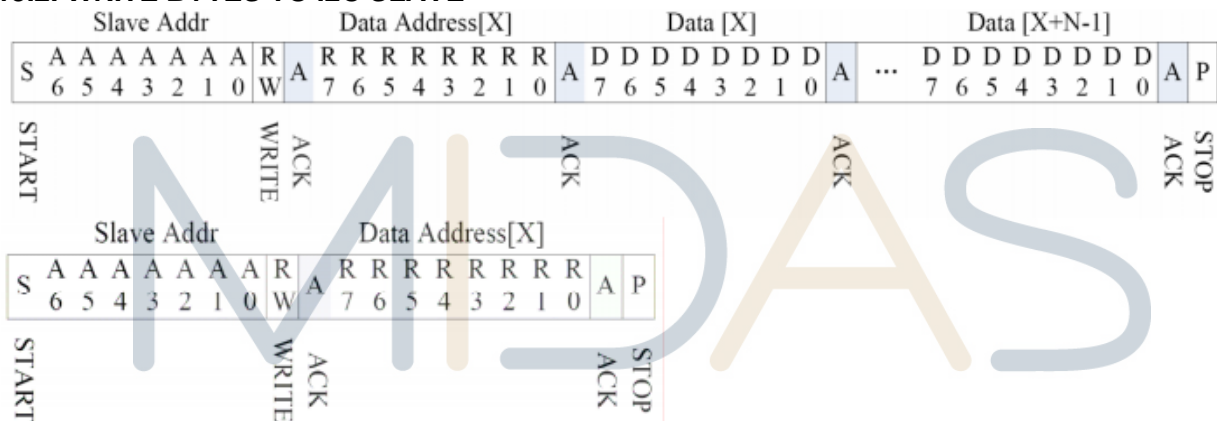
READ X BYTES FROM I2C SLAVE
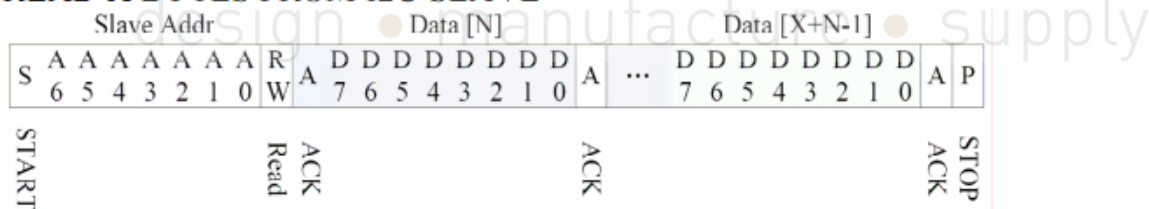
AS FOR STANDARD CTPM, HOST NEED TO USE BOTH INTERRUPT CONTROL SIGNAL AND SERIAL DATA INTERFACE TO GET THE TOUCH DATA, HERE IS THE TIMING TO GET TOUCH DATA.

Address: 0x38

TOUCH DATA READ PROTOCOL

NAME	VALUE	DESCRIPTION
START CH	0X00	START COMMAND FOR CTPM TOUCH DATA PACKET,HOST MUST SEND CTPM A START CH COMMAND BEFORE READ TOUCH DATA
lst READ BYTE~ LAST READ BYTE		TOUCH DATA PACKET SENT BY CTPM,EACH BYTE HAS 8-BIT DATA ,A TOUCH DATA PACKET CONSISTS OF N BYTE

A DATA PACKET STARTS WITH A HEADER AND ENDS WITH CRC CODE,AS FOR 5 POINTS DATA PACKET,THE LENGTH OF THE PACKET IS ALWAYS 26 BYTES IN SPITE OF ACTUAL TOUCH POINTS.

Address	Name	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0	Host Access
00h	Devide__Mode		Device Model[2:0]							RW
01h	Gest__ID	Gesture ID[7:0]								R
02h	TD__Status					Number of touch points[3:0]				R
03h	Touch1__XH	1st Event Flag				1st Touch X Position[11:8]				R
04h	Touch1__XL	1st Touch X Position[7:0]								R
05h	Touch1__YH	1st Touch ID[3:0]				1st Touch Y Position[11:8]				R
06h	Touch1__YL	1st Touch Y Position[7:0]								R
09h	Touch2__XH	2nd Event Flag				2nd Touch X Position[11:8]				R
0Ah	Touch2__XL	2nd Touch X Position[7:0]								R
0Bh	Touch2__YH	2nd Touch ID[3:0]				2ndTouch Y Position[11:8]				R
0Ch	Touch2__YL	2nd Touch Y Position[7:0]								R

0Fh	Touch3__XH	3rdEvent Flag		3rdTouch X Position[11:8]	R
10h	Touch3__XL	3rd Touch X Position[7:0]			R
11h	Touch3__YH	3rdTouch ID[3:0]		3rdTouch Y Position[11:8]	R
12h	Touch3__YL	3rd Touch Y Position[7:0]			R
15h	Touch4__XH	4thEvent Flag		4thTouch X Position[11:8]	R
16h	Touch4__XL	4th Touch X Position[7:0]			R
17h	Touch4__YH	4thTouch ID[3:0]		4thTouch Y Position[11:8]	R
18h	Touch4__YL	4th Touch Y Position[7:0]			R
1Bh	Touch5__XH	5thEvent Flag		5thTouch X Position[11:8]	R
1Ch	Touch5__XL	5th Touch X Position[7:0]			R
1Dh	Touch5__YH	5thTouch ID[3:0]		5thTouch Y Position[11:8]	R
1Eh	Touch5__YL	5th Touch Y Position[7:0]			R

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