

# FMH20N60S1

#### **FUJI POWER MOSFET**

## **Super J-MOS series**

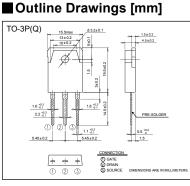
#### N-Channel enhancement mode power MOSFET

#### Features

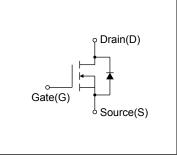
Low on-state resistance Low switching loss easy to use (more controllabe switching dV/dt by R<sub>g</sub>)

#### Applications

UPS Server Telecom Power conditioner system Power supply



#### Equivalent circuit schematic



#### Maximum Ratings and Characteristics

● Absolute Maximum Ratings at T₀=25°C (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks	
Drain Source Voltage	VDS	600	V		
Drain-Source Voltage	VDSX	600	V	V <sub>GS</sub> =-30V	
Continuous Drain Current	lo	±20	А	Tc=25°C Note*1	
Continuous Drain Current		±12.6	А	Tc=100°C Note*1	
Pulsed Drain Current	IDP	±60	А		
Gate-Source Voltage	Vgs	±30	V		
Repetitive and Non-Repetitive	lan	6.6	А	Note *2	
Maximum Avalanche Current	IAR	0.0			
Non-Repetitive	Eas	472.2	mJ	Note *3	
Maximum Avalanche Energy		472.2		Note 5	
Maximum Drain-Source dV/dt	dV <sub>DS</sub> /dt	50	kV/μs	V <sub>DS</sub> ≤ 600V	
Peak Diode Recovery dV/dt	dV/dt	15	kV/μs	Note *4	
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note *5	
Maximum Power Discinction	PD	2.5	W	Ta=25°C	
Maximum Power Dissipation		140	vv	Tc=25°C	
Operating and Storage Temperature range	Tch	150	°C		
Operating and Storage Temperature range	T <sub>stg</sub>	-55 to +150	°C		

 Note \*1 : Limited by maximum channel temperature.

 Note \*2 : Tch≤150°C, See Fig.1 and Fig.2

 Note \*3 : Starting Tch=25°C, IAs=2A, L=216mH, Vob=60V, RG=50Ω, See Fig.1 and Fig.2

 EAs limited by maximum channel temperature and avalanche current.

 Note \*4 : Ir≤-ID, -di/dt=100A/µs, Vob≤400V, Tch≤150°C.

 Note \*5 : Ir≤-ID, dV/dt=15kV/µs, Vob≤400V, Tch≤150°C.

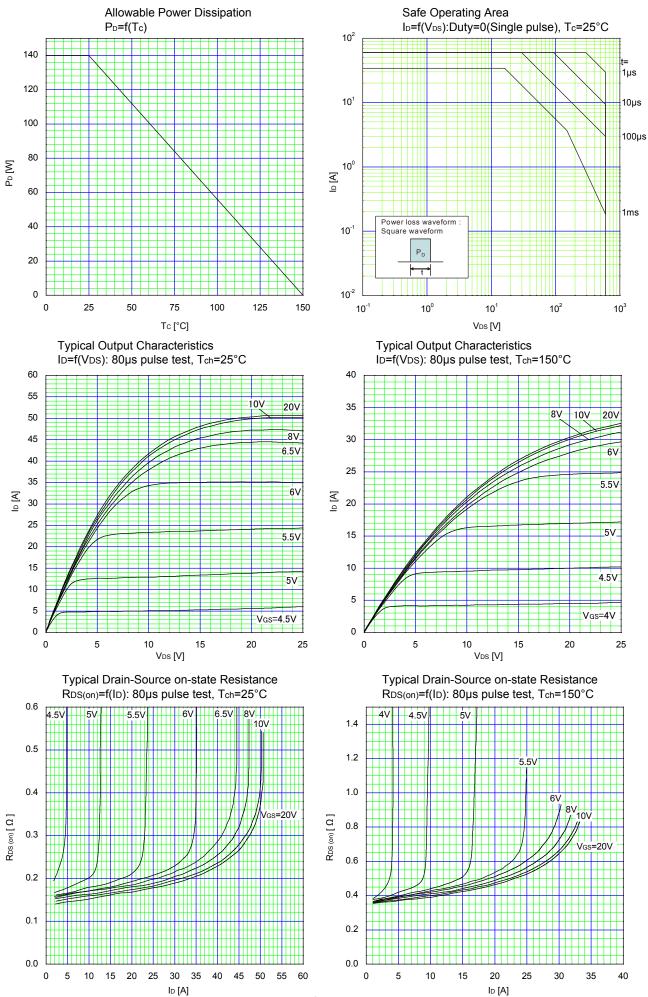
# ● Electrical Characteristics at T₀=25°C (unless otherwise specified) Static Ratings

Description	Symbol Conditions		min.	typ.	max.	Unit	
Drain-Source Breakdown Voltage	BVDSS	I <sub>D</sub> =250μA V <sub>GS</sub> =0V		600	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	ID=250µA VDS=VGS		2.5	3	3.5	V
Zero Gate Voltage Drain Current	loss	V <sub>DS</sub> =600V V <sub>GS</sub> =0V	T <sub>ch</sub> =25°C	-	-	25	- μΑ
		V <sub>DS</sub> =480V V <sub>GS</sub> =0V	T <sub>ch</sub> =125°C	-	-	250	
Gate-Source Leakage Current	loss	V <sub>GS</sub> = ± 30V V <sub>DS</sub> =0V		-	10	100	nA
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	I <sub>D</sub> =10A V <sub>GS</sub> =10V		-	0.161	0.19	Ω
Gate resistance	Rg	f=1MHz, open drain		-	3.7	-	Ω
Forward Transconductance	<b>g</b> <sub>fs</sub>	I <sub>D</sub> =10A V <sub>DS</sub> =25V		8.5	17.5	-	S
Input Capacitance	Ciss	Vos=10V           Vos=0V           f=1MHz           Vos=0480V           Vos=0480V           Vos=0480V           ID=constant		-	1470	-	pF
Output Capacitance	Coss			-	3120	-	
Reverse Transfer Capacitance	Crss			-	280	-	
Effective output capacitance, energy related (Note *6)	C <sub>o(er)</sub>			-	90	-	
Effective output capacitance, time related (Note *7)	Co(tr)			-	305	-	
	td(on)			-	22	-	- ns
Turn-On Time	tr			-	40	-	
	td(off)			-	162	-	
Turn-Off Time	tr		-	22	-		
Total Gate Charge	QG	V₀₀=480V, l₀=20A V₀s=10V See Fig.5		-	48	-	nC
Gate-Source Charge	QGS			-	12.5	-	
Gate-Drain Charge	QGD			-	15	-	
Drain-Source crossover Charge	Qsw			-	8	-	
Avalanche Capability	lav	L=6.02mH, Tch=25°C See Fig.1 and Fig.2		6.6	-	-	А
Diode Forward On-Voltage	Vsd	IF=20A,VGS=0V Tch=25°C		-	0.9	1.35	V
Reverse Recovery Time	trr	I⊧=20A, V₀s=0V V₀b=400V -di/dt=100A/μs T₀h=25°C See Fig.6			370	-	ns
Reverse Recovery Charge	Qrr			-	6.2	-	μC
Peak Reverse Recovery Current	Irp			-	32	-	А

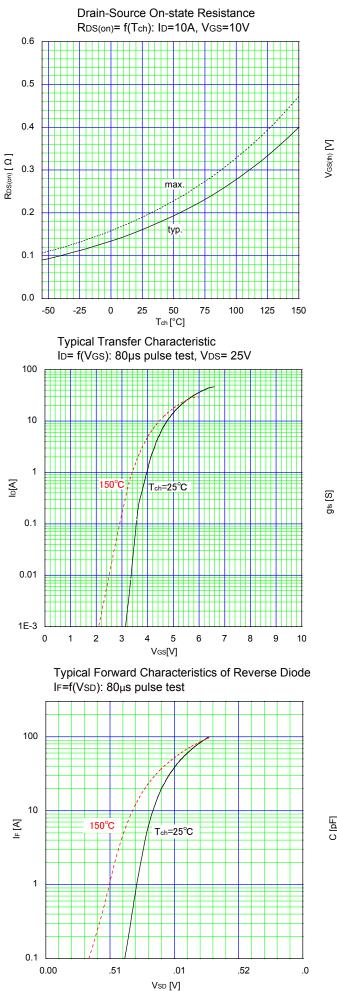
Note \*6 : Co(er) is a fixed capacitance that gives the same stored energy as Coss while Vos is rising from 0 to 80% BVoss. Note \*7 : Co(tr) is a fixed capacitance that gives the same charging times as Coss while Vos is rising from 0 to 80% BVoss.

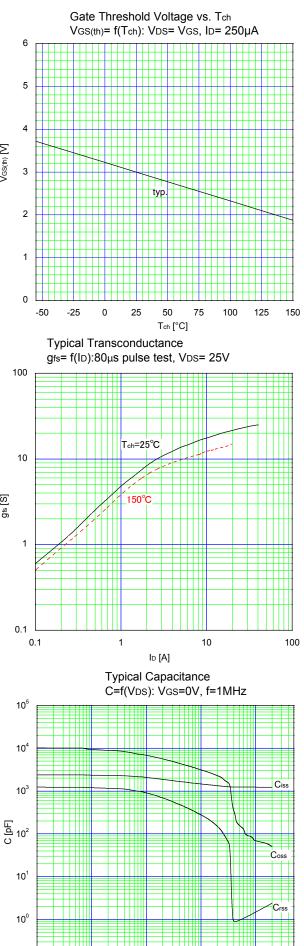
#### Thermal Characteristics

Description	Symbol	min.	typ.	max.	Unit
Channel to Case	Rth(ch-c)			0.89	°C/W
Channel to Ambient	Rth(ch-a)			50	°C/W



3





10<sup>-1</sup>

4

10<sup>-2</sup>

10<sup>-1</sup>

10<sup>0</sup>

VDS [V]

10<sup>1</sup>

10<sup>2</sup>

10<sup>-3</sup>

10-5

10<sup>-4</sup>

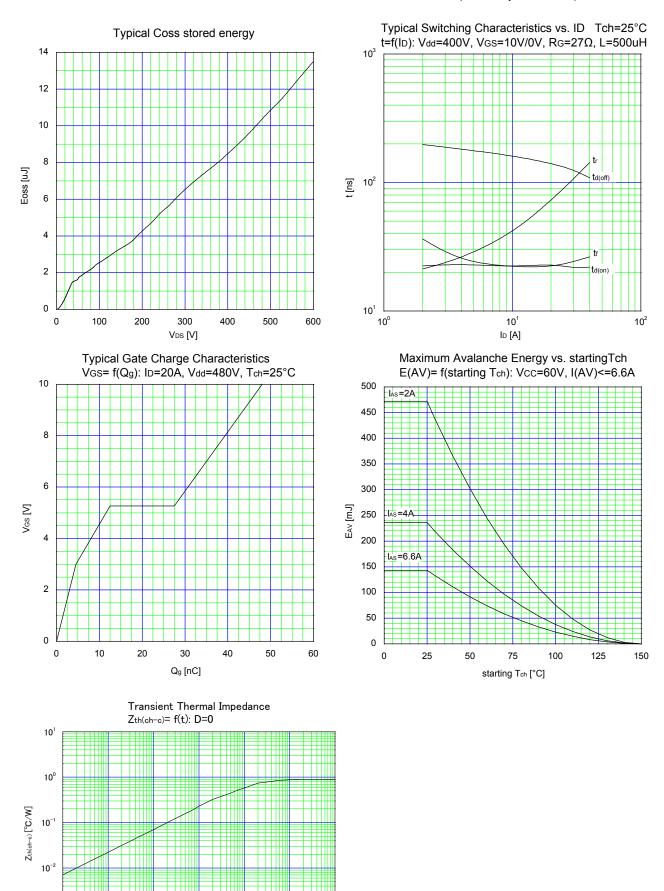
10<sup>-3</sup>

t [sec]

10-2

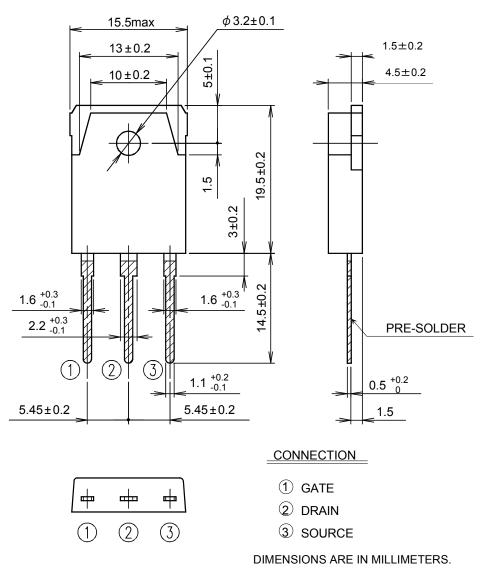
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http://www.fujielectric.com/products/semiconductor/

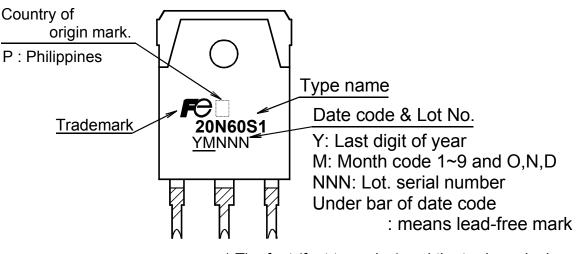


10<sup>0</sup>

#### Outview: TO-3P(Q) Package



#### Marking



\* The font (font type,size) and the trademark-size might be actually different.

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