

FMW20N60S1HF

FUJI POWER MOSFET

Super J-MOS series

N-Channel enhancement mode power MOSFET

Features

Low on-state resistance Low switching loss easy to use (more controllabe switching dV/dt by Rg)

Applications

UPS

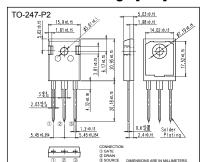
Server

Telecom

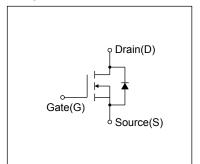
Power conditioner system

Power supply

■ Outline Drawings [mm]



■ Equivalent circuit schematic



■ Maximum Ratings and Characteristics

● Absolute Maximum Ratings at T_c=25°C (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks	
Dania Carras Valtaria	V _{DS}	600	V		
Drain-Source Voltage	VDSX	600	V	V _{GS} =-30V	
Continuous Drain Current	lo	±20	Α	Tc=25°C Note*1	
		±12.6	Α	Tc=100°C Note*1	
Pulsed Drain Current	IDP	±60	Α		
Gate-Source Voltage	V _{GS}	±30	V		
Repetitive and Non-Repetitive Maximum Avalanche Current	lar	6.6	Α	Note *2	
Non-Repetitive Maximum Avalanche Energy	Eas	472.2	mJ	Note *3	
Maximum Drain-Source dV/dt	dV _{DS} /dt	50	kV/μs	V _{DS} ≤ 600V	
Peak Diode Recovery dV/dt	dV/dt	15	kV/µs Note *4		
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note *5	
Maximum Power Dissipation	Po	2.5	10/	Ta=25°C	
		140	W	Tc=25°C	
Onevetion and Stavens Temperature range	Tch	150	°C		
Operating and Storage Temperature range	T _{stg}	-55 to +150	°C		

Note *1 : Limited by maximum channel temperature.

Note *2 : Tch≤150°C, See Fig.1 and Fig.2 Note *3 : Starting Tch=25°C, Ias=2A, L=216mH, Vbb=60V, Rg=50Ω, See Fig.1 and Fig.2

Eas limited by maximum channel temperature and avalanche current. Note *4 : Ir≤-Ip, -di/dt=100A/µs, Vpp≤400V, Tch≤150°C.

Note *5 : IF \leq -ID, dV/dt=15kV/ μ s, VDD \leq 400V, Tch \leq 150°C.

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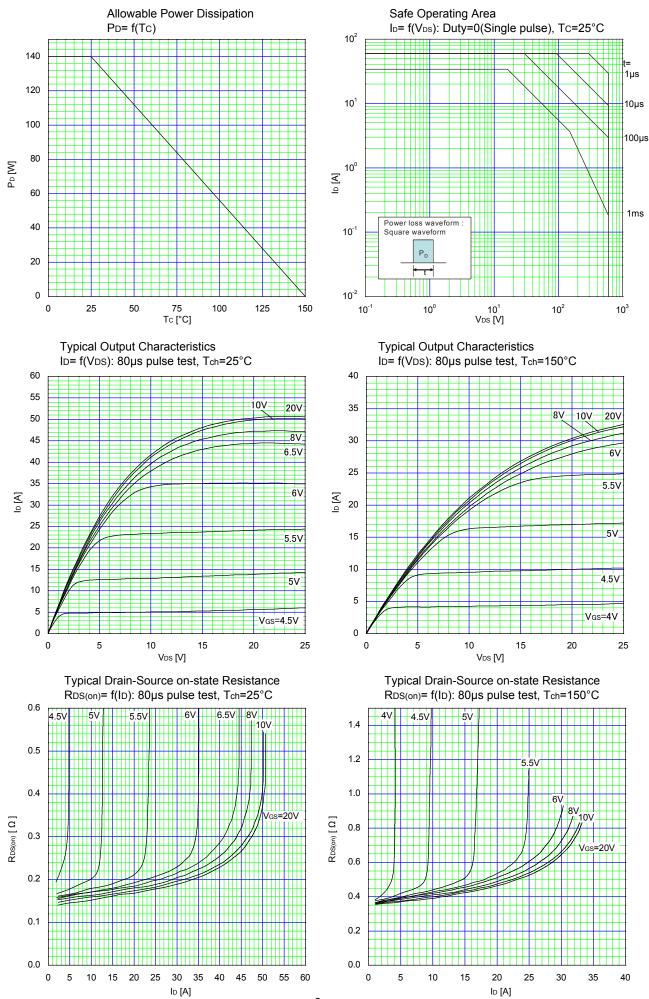
● Electrical Characteristics at T₀=25°C (unless otherwise specified) Static Ratings

Description	Symbol	Conditions		min.	typ.	max.	Unit	
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA V _{GS} =0V		600	-	-	V	
Gate Threshold Voltage	V _{GS(th)}	I _D =250μA V _{DS} =V _{GS}		2.5	3	3.5	V	
Zero Gate Voltage Drain Current		V _{DS} =600V V _{GS} =0V	T _{ch} =25°C	-	-	25	μA	
	Ipss	V _{DS} =480V V _{GS} =0V	T _{ch} =125°C	-	-	250		
Gate-Source Leakage Current	Igss	V _{GS} = ± 30V V _{DS} =0V			10	100	nA	
Drain-Source On-State Resistance	R _{DS(on)}	I _D =10A V _{GS} =10V		-	0.161	0.19	Ω	
Gate resistance	Rg	f=1MHz, open drain		-	3.7	-	Ω	
Forward Transconductance	g _{fs}	I _D =10A V _{DS} =25V		8.5	17.5	-	s	
Input Capacitance	Ciss	V _{DS} =10V		-	1470	-		
Output Capacitance	Coss	V _{GS} =0V	-		3120	-		
Reverse Transfer Capacitance	Crss	f=1MHz			280	-		
Effective output capacitance, energy related (Note *6)	C _{o(er)}	V _{GS} =0V V _{DS} =0480V		-	90	-	pF	
Effective output capacitance, time related (Note *7)	C _{o(tr)}	V _{GS} =0V V _{DS} =0480V ID=constant		-	305	-		
td(on)	t _{d(on)}	V_{DD} =400V, V_{GS} =10V I_D =10A, R_G =27 Ω See Fig.3 and Fig.4		-	22	-	ns	
Turn-On Time	tr			-	40	-		
	t _{d(off)}			-	162	-		
Turn-Off Time	tr			-	22	-		
Total Gate Charge	Q _G	V _{DD} =480V, I _D =20A V _{GS} =10V		-	48	-	nC	
Gate-Source Charge	Q _{GS}			-	12.5	-		
Gate-Drain Charge	Q _{GD}			-	15	-		
Drain-Source crossover Charge	Qsw	— 000 i ig.0	See Fig.5		8	-		
Avalanche Capability	lav	L=6.02mH,T _{ch} =25°C See Fig.1 and Fig.2		6.6	-	-	А	
Diode Forward On-Voltage	VsD	I _F =20A,V _{GS} =0V T _{ch} =25°C		-	0.9	1.35	V	
Reverse Recovery Time	trr	I _F =20A, V _{GS} =0V V _{DD} =400V -di/dt=100A/µs T _{ch} =25°C See Fig.6			370	-	ns	
Reverse Recovery Charge	Qrr			-	6.2	-	μC	
Peak Reverse Recovery Current	Irp			-	32	-	А	

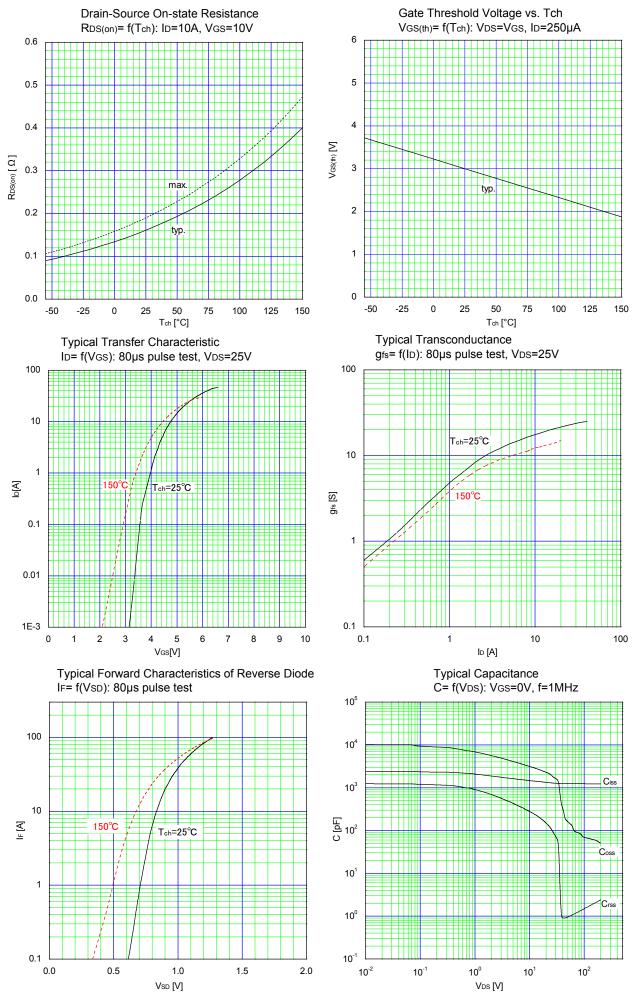
Note *6 : $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 80% BVDss. Note *7 : $C_{o(tr)}$ is a fixed capacitance that gives the same charging times as C_{oss} while V_{DS} is rising from 0 to 80% BVDss.

Thermal Characteristics

Description	Symbol	min.	typ.	max.	Unit
Channel to Case	Rth(ch-c)			0.89	°C/W
Channel to Ambient	Rth(ch-a)			50	°C/W

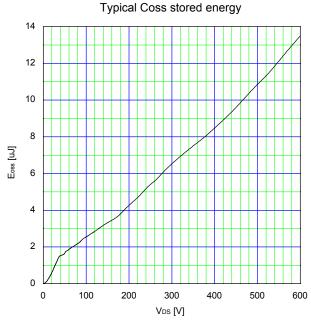


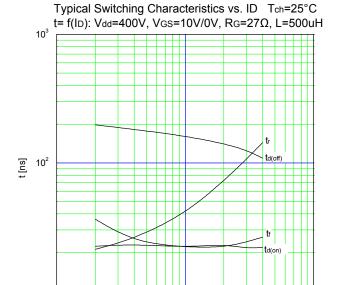
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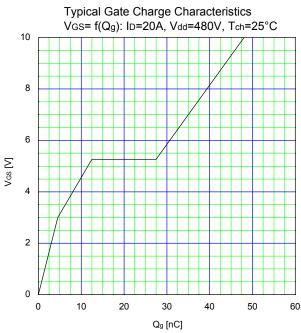


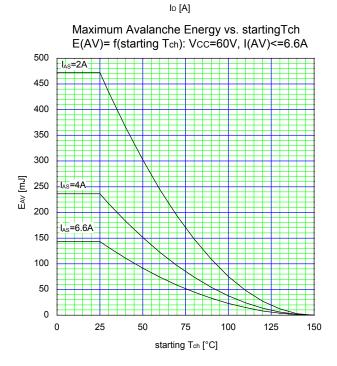


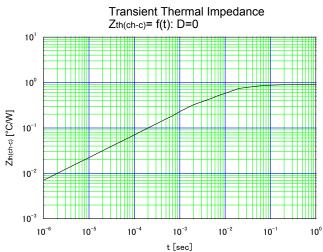
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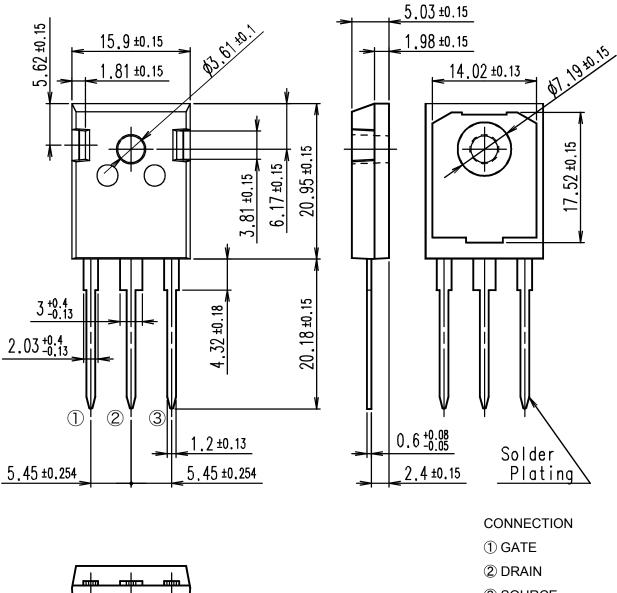
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■ Outview: TO-247-P2 Package

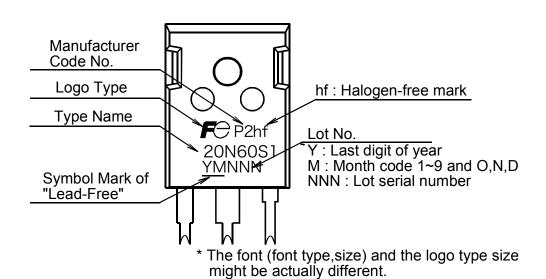


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③ SOURCE

DIMENSIONS ARE IN MILLIMETERS.

■ Marking



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