

# **Power line chokes**

Current-compensated ring core double chokes 250 V AC, 1 ... 10 A, 1.8 ... 68 mH, +60 °C

Series/Type: B82725J
Date: July 2012



Power line chokes B82725J

# Current-compensated ring core double chokes

Rated voltage 250 V AC Rated current 1 ... 10 A Rated inductance 1.8 ... 68 mH

#### Construction

- Current-compensated ring core double choke
- Ferrite core wih epoxy coating (UL 94 V-0)
- Polycarbonate case (UL 94 V-0)
- Polyurethane potting (UL 94 V-0)
- Sector winding

#### **Features**

- High resonance frequency due to special winding technique
- Approx. 1% stray inductance for symmetrical interference suppression
- Suitable for wave soldering
- Design complies with EN 60938-2 (VDE 0565-2) and UL 1283
- UL1) and/or ENEC (VDE) approvals 🕦 🦝 🖎
- RoHS-compatible

#### **Applications**

- Suppression of common-mode interferences
- Switch-mode power applications
- Power inverters

#### **Terminals**

- Base material CuNi18Zn20
- Layer composition Ni, Sn
- Hot-dipped
- Pins 1 × 1 (mm)
- Lead spacing  $17.5 \times 12.5$  (mm)

## Marking

 Manufacturer, approval signs and/or VDE standard number, ordering code, graphic symbol, rated current, rated voltage, rated inductance, date of manufacture (YYWWD.internal ID code)

#### **Delivery mode**

Blister tray in cardboard box

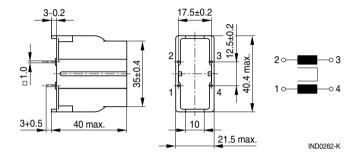




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# Dimensional drawing and pin configuration



Tolerances to ISO 2768-C unless otherwise noted. Dimensions in mm.



# Technical data and measuring conditions

Rated voltage V <sub>R</sub>	250 V AC (50/60 Hz)		
Test voltage V <sub>test</sub>	1500 V AC, 2 s (line/line)		
Rated temperature T <sub>R</sub>	+60 °C		
Rated current I <sub>R</sub>	Referred to 50 Hz and rated temperature		
Rated inductance L <sub>R</sub>	Measured with Agilent 4284A at 10 kHz, 0.1 mA, +20 °C Inductance is specified per winding.		
Inductance tolerance	±30% at +20 °C		
Inductance decrease ΔL/L <sub>0</sub>	< 10% at DC magnetic bias with I <sub>R</sub> , +20 °C		
Stray inductance L <sub>stray,typ</sub>	Measured with Agilent 4284A at 10 kHz, 5 mA, +20 °C, typical values		
DC resistance R <sub>typ</sub>	Measured at +20 °C, typical values, specified per winding		
Solderability (lead-free)	Sn96.5Ag3.0Cu0.5: +(245 $\pm$ 5) °C, (3 $\pm$ 0.3) s Wetting of soldering area $\geq$ 95% (to IEC 60068-2-20, test Ta)		
Resistance to soldering heat (wave soldering)	+(260 ±5) °C, (10 ±1) s (to IEC 60068-2-20, test Tb)		
Climatic category	40/125/56 (to IEC 60068-1)		
Storage conditions (packaged)	–25 °C +40 °C, ≤ 75% RH		
Weight	Approx. 45 g 52 g		
Approval	EN 60938-2, UL 1283		
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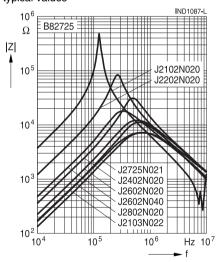
# Characteristics and ordering codes

I <sub>R</sub>	L <sub>R</sub>	L <sub>stray,typ</sub>	R <sub>typ</sub>	$T_{R}$	Ordering code	Approvals	
Α	mH	μН	mΩ	°C		<b>&amp;</b>	717
1	68	650	1050	60	B82725J2102N020	×	×
2	18	150	270	60	B82725J2202N020	×	×
4	6.8	60	75	60	B82725J2402N020	×	×
6	3.9	30	30	60	B82725J2602N020	×	×
6	7.5	70	40	60	B82725J2602N040	-	-
8	2.7	25	20	60	B82725J2802N020	×	×
10	2.5	20	15	60	B82725J2103N021	_	_
10	1.8	17	13	60	B82725J2103N022	×	×

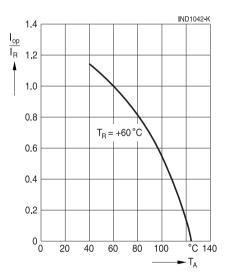
x = approval granted

# Impedance |Z| versus frequency f

measured with windings in parallel at +20 °C, typical values



# Current derating $I_{op}/I_R$ versus ambient temperature $T_A$





## Cautions and warnings

### Current-compensated ring core double chokes

- Please note the recommendations in our Inductors data book (latest edition) and in the data sheets.
  - Particular attention should be paid to the derating curves given there. Derating must be applied
    in case the ambient temperature in the application exceeds the rated temperature of the
    component.
  - Ensure the operation temperature (which is the sum of the ambient temperature and the temperature rise caused by losses / self-heating) of the component in the application does not exceed the maximum value specified in the climatic category.
  - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pin, not the housing.
- If the components are to be washed varnished it is necessary to check whether the washing varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. In particular, it is possible for washing varnish agent residues to have a negative effect in the long-term on wire insulation.
  - Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.
- The following points must be observed if the components are potted in customer applications:
  - Many potting materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
  - It is necessary to check whether the potting material used attacks or destroys the wire insulation, plastics or glue.
  - The effect of the potting material can change the high-frequency behaviour of the components.
- Ferrites are sensitive to direct impact. This can cause the core material to flake, or lead to breakage of the core.
- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.



#### Important notes

#### Current-compensated ring core double chokes

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