



FCB11N60 600V N-Channel MOSFET

Features

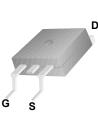
- 650V @T_J = 150°C
- Typ. R_{DS(on)} = 0.32Ω
- Ultra low gate charge (typ. Q_g = 40nC)
- Low effective output capacitance (typ. Coss.eff = 95pF)
- 100% avalanche tested
- RoHS Compliant

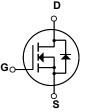


Description

SuperFETTM is, Fairchild's proprietary, new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.

This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. Consequently, SuperFET is very suitable for various AC/DC power conversion in switching mode operation for system miniaturization and higher efficiency.





Absolute Maximum Ratings

Symbol	Parameter			FCB11N60	Unit	
V _{DSS}	Drain-Source Voltage			600	V	
Ι _D	Drain Current	- Continuous (T _C = 25°C) - Continuous (T _C = 100°C)		11 7	A A	
I _{DM}	Drain Current	- Pulsed	(Note 1)	33	A	
V _{GSS}	Gate-Source voltage			± 30	V	
E _{AS}	Single Pulsed Avalanche Energy		(Note 2)	340	mJ	
I _{AR}	Avalanche Current		alanche Current (Note 1) 11		А	
E _{AR}	Repetitive Avalanche Energy		(Note 1)	12.5	mJ	
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	4.5	V/ns	
P _D	Power Dissipation (T _C = 25°C) - Derate above 25°C			125 1.0	W W/°C	
T _{J,} T _{STG}	Operating and Storage Temperature Range			-55 to +150	°C	
Τ _L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds			300	°C	

Thermal Characteristics

Symbol	Parameter	FCB11N60	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.0	°C/W	
$R_{\theta JA}^{*}$	Thermal Resistance, Junction-to-Ambient*	40	°C/W	
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient	62.5	°C/W	
	the minimum pad size recommended (PCB Mount)			

Device Marking		Device Pa		ckage Reel Size Tap		e Width		Quantity		
FCB11N			D ²	PAK 330mm		24m		800		
Electrica	l Char	racteristics _{Tc}	= 25°C unle	ss otherwise no	ted					
Symbol	Parameter			Conditions		Min	Тур	Max	Units	
Off Characte	eristics									
BV _{DSS} [Drain-Source Breakdown Voltage		V_{GS} = 0V, I _D = 250µA, T _J = 25°C		600			V		
			V_{GS} = 0V, I _D = 250µA, T _J = 150°C			650		V		
200	Breakdown Voltage Temperature Coefficient		$I_D = 250\mu A$, Referenced to 25°C			0.6		V/°C		
03	Drain-Source Avalanche Breakdown Voltage		V _{GS} = 0V, I _D = 11A				700		V	
I _{DSS} Z	Zero Gate Voltage Drain Current			V _{DS} = 600V, V _{GS} = 0V V _{DS} = 480V, T _C = 125°C					1 10	μΑ μΑ
I _{GSSF} (Gate-Bod	ate-Body Leakage Current, Forward		$V_{GS} = 30V, V_{DS} = 0V$				100	nA	
	Gate-Bod	ly Leakage Current, Reverse		$V_{GS} = -30V, V_{DS} = 0V$				-100	nA	
On Characte	ristics				-					
V _{GS(th)}	Gate Threshold Voltage			$V_{DS} = V_{GS}, I_D = 250 \mu A$			3.0		5.0	V
R _{DS(on)} S	Static Drain-Source On-Resistance		V _{GS} = 10V, I _D = 5.5A			0.32	0.38	Ω		
g _{FS} F	Forward T	d Transconductance		V _{DS} = 40\	/, I _D = 5.5A	(Note 4)		9.7		S
Dynamic Cha	aracteris	tics								
C _{iss} I	Input Capacitance Output Capacitance		$V_{DS} = 25V, V_{GS} = 0V,$			1148	1490	pF		
C _{oss} (f = 1.0MHz				671	870	pF	
	Reverse Transfer Capacitance						63		pF	
C _{oss} (Output Capacitance		V _{DS} = 480V, V _{GS} = 0V, f = 1.0MHz			35		pF		
C _{oss} eff. E	Effective Output Capacitance		V_{DS} = 0V to 400V, V_{GS} = 0V			95		pF		
Switching Cl	haracteri	stics								
t _{d(on)}	Turn-On Delay Time		$V_{DD} = 300V, I_{D} = 11A$			34	80	ns		
t _r 1	Turn-On F	Rise Time		$R_{G} = 25\Omega$			98	205	ns	
t _{d(off)}	Turn-Off E	Delay Time					119	250	ns	
	Turn-Off F	all Time				(Note 4, 5)		56	120	ns
3	Total Gate	e Charge		V_{DS} = 480V, I _D = 11A V_{GS} = 10V (Note 4, 5)				40	52	nC
Q _{gs} (Gate-Sou	rce Charge					7.2		nC	
gu	Gate-Drai	9					21		nC	
Drain-Source	e Diode C	Characteristics and I	<i>l</i> laximun	n Ratings						
I _S I	Maximum Continuous Drain-Source Diod		de Forward Current				11	Α		
I _{SM}	Maximum Pulsed Drain-Source Diode Fo							33	Α	
V _{SD} [Drain-Sou	Irce Diode Forward Vo	oltage	V_{GS} = 0V,	-				1.4	V
	Reverse F	Recovery Time		$V_{GS} = 0V,$				390		ns
Q _{rr} F	Reverse F	Recovery Charge		dI _F /dt =100A/µs (Note 4)			5.7		μC	

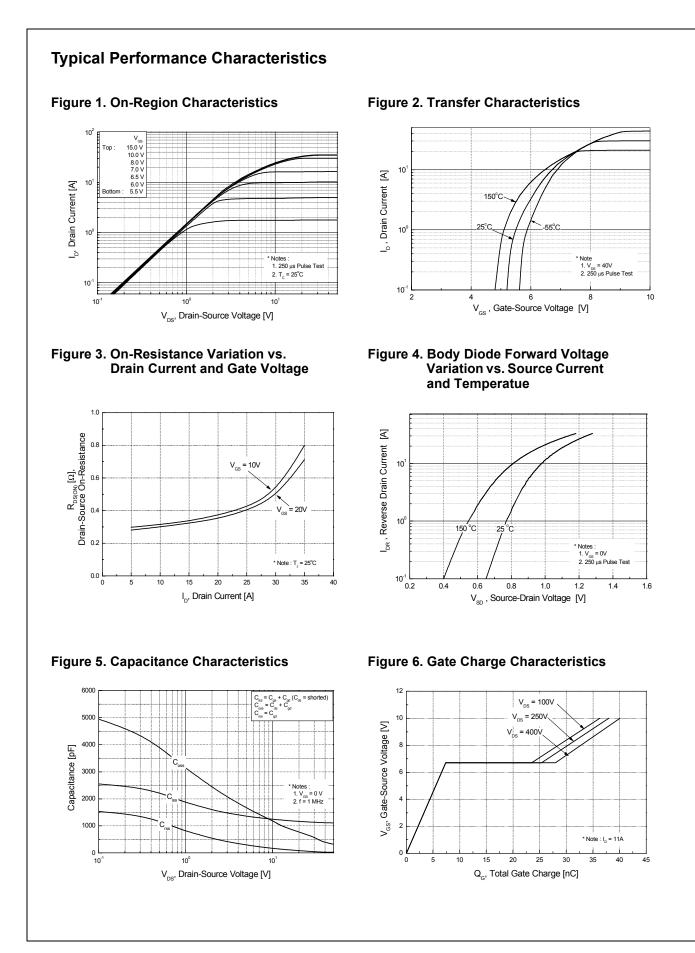
1. Repetitive Rating: Pulse width limited by maximum junction temperature

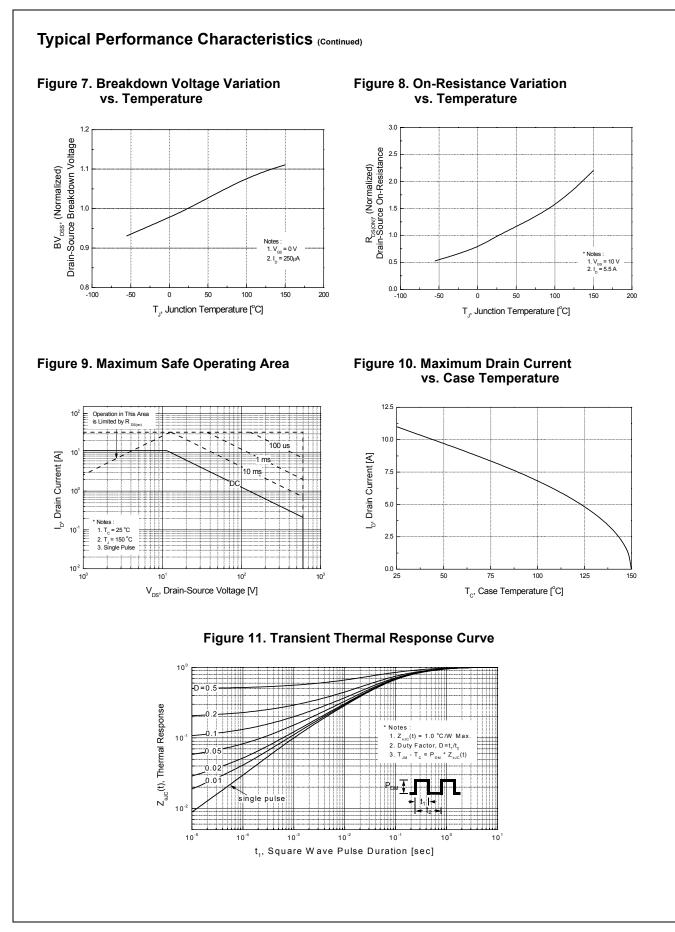
2. I_{AS} = 5.51A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}C$

3. I_{SD} \leq 11A, di/dt \leq 200A/µs, V_{DD} \leq BV_{DSS}, Starting T_J = 25°C

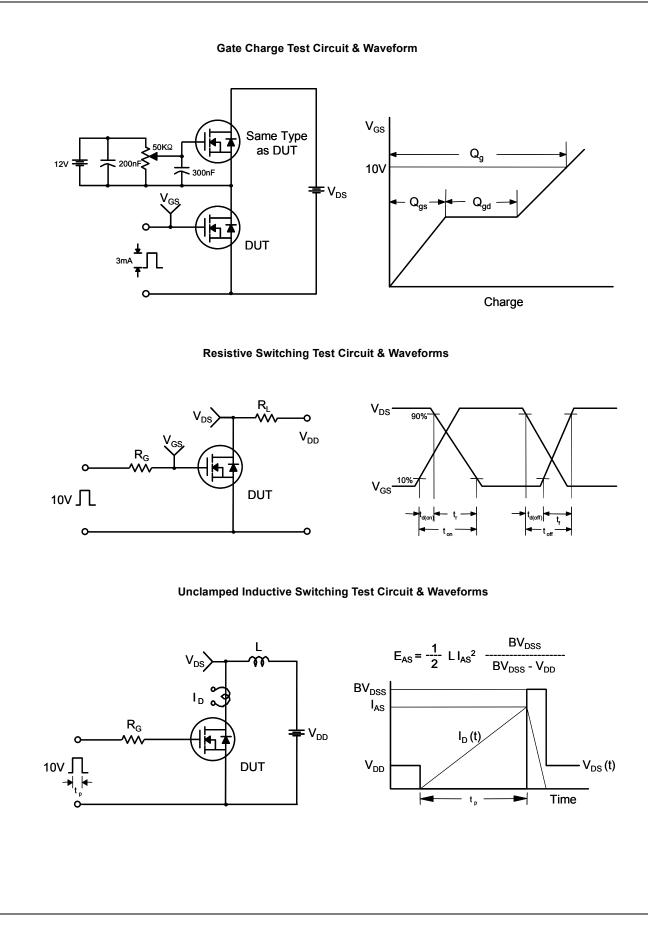
4. Pulse Test: Pulse width $\leq 300 \mu s,$ Duty Cycle $\leq 2\%$

5. Essentially Independent of Operating Temperature Typical Characteristics



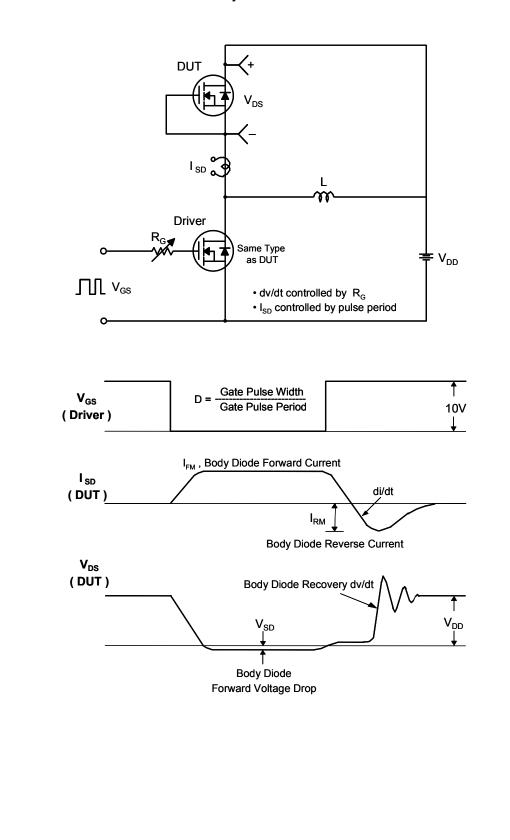


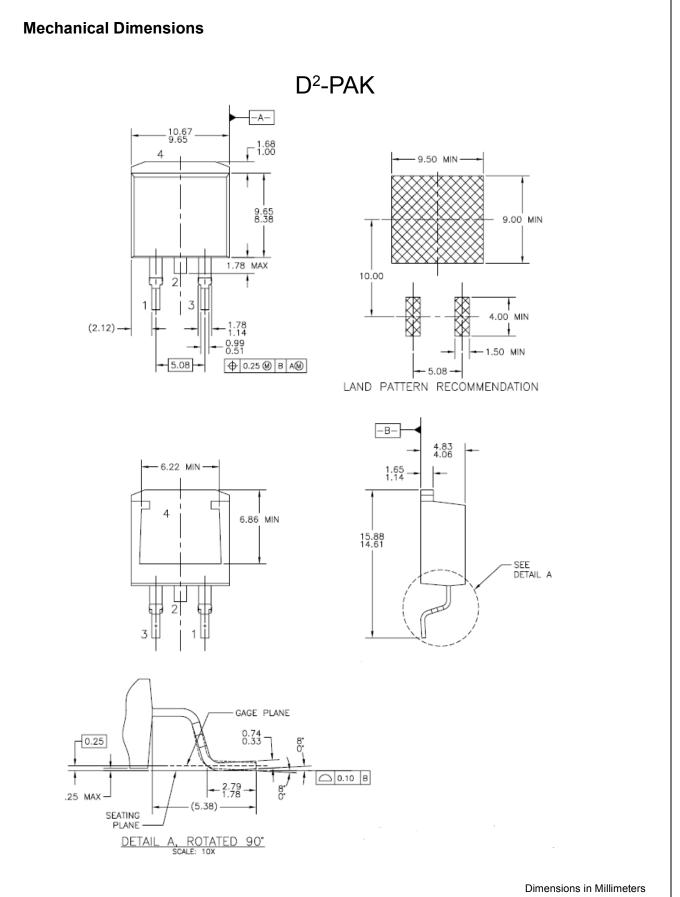
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Peak Diode Recovery dv/dt Test Circuit & Waveforms







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