



# MCH6536

## Bipolar Transistor (-12V)15V, (-1A)1.4A, Low $V_{CE(sat)}$ Complementary Dual MCPH6

ON Semiconductor®

<http://onsemi.com>

### Applications

- MOSFET gate drivers, low-frequency power amplifier, high-speed switching, motor drivers

### Features

- Composite type with a PNP transistor and an NPN transistor contained in one package facilitating high-density mounting
- Ultrasmall package permitting applied sets to be small and slim
- Small ON-resistance ( $R_{on}$ )

### Specifications ( ) : PNP

#### Absolute Maximum Ratings at $T_a=25^\circ\text{C}$

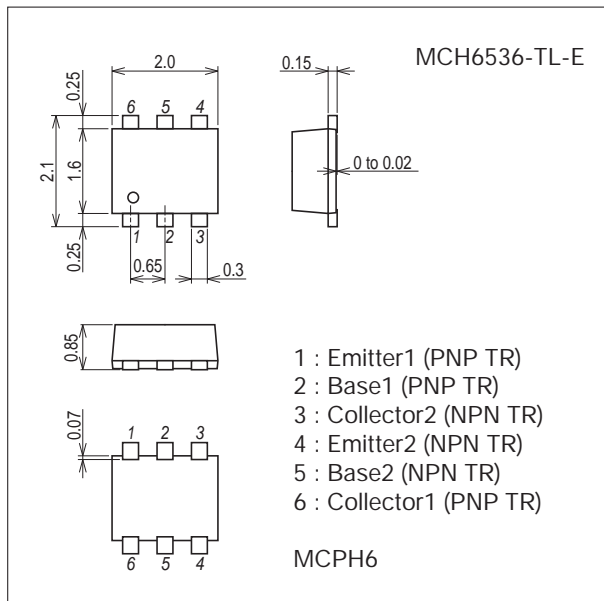
Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	$V_{CBO}$		(-15)20	V
Collector-to-Emitter Voltage	$V_{CEO}$		(-12)15	V
Emitter-to-Base Voltage	$V_{EBO}$		(-)-5	V
Collector Current	$I_C$		(-500)700	mA
Collector Current (Pulse)	$I_{CP}$		(-1.0)1.4	A
Collector Dissipation	$P_C$	When mounted on ceramic substrate (600mm <sup>2</sup> ×0.8mm)	0.5	W
Total Power Dissipation	$P_T$	When mounted on ceramic substrate (600mm <sup>2</sup> ×0.8mm)	0.55	W
Junction Temperature	$T_j$		150	°C
Storage Temperature	$T_{stg}$		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### Package Dimensions

unit : mm (typ)

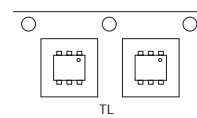
7022A-012



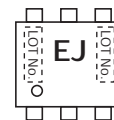
### Product & Package Information

- Package : MCPH6
- JEITA, JEDEC : SC-88, SC-70-6, SOT-363
- Minimum Packing Quantity : 3,000 pcs./reel

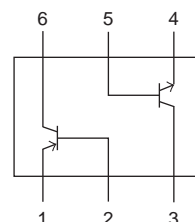
### Packing Type : TL



### Marking



### Electrical Connection



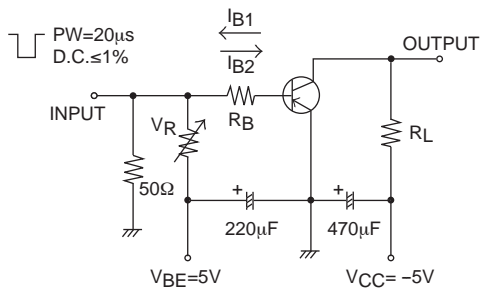
# MCH6536

## Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	$I_{CBO}$	$V_{CB}=(-12)15V, I_E=0A$			(-)100	nA
Emitter Cutoff Current	$I_{EBO}$	$V_{EB}=(-)4V, I_C=0A$			(-)100	nA
DC Current Gain	$h_{FE}$	$V_{CE}=(-)2V, I_C=(-)10mA$	300		(700)800	
Gain-Bandwidth Product	$f_T$	$V_{CE}=(-)2V, I_C=(-)50mA$		(490)330		MHz
Output Capacitance	$C_{ob}$	$V_{CB}=(-)10V, f=1MHz$		(4)3.2		pF
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=(-)200mA, I_B=(-)10mA$		(-)150	(-)300	mV
Base-to-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C=(-)200mA, I_B=(-)10mA$		(-)0.9	(-)1.2	V
Collector-to-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C=(-)10\mu A, I_E=0A$	(-15)20			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C=(-)1mA, R_{BE}=\infty$	(-12)15			V
Emitter-to-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E=(-)10\mu A, I_C=0A$	(-)5			V
Turn-ON Time	$t_{on}$	See specified Test Circuit.		30		ns
Storage Time	$t_{stg}$			(57)77		ns
Fall Time	$t_f$			(30)40		ns

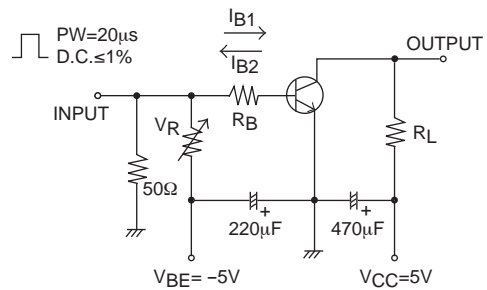
## Switching Time Test Circuit

[PNP]



$$I_C = 20I_{B1} = -20I_{B2} = -400mA$$

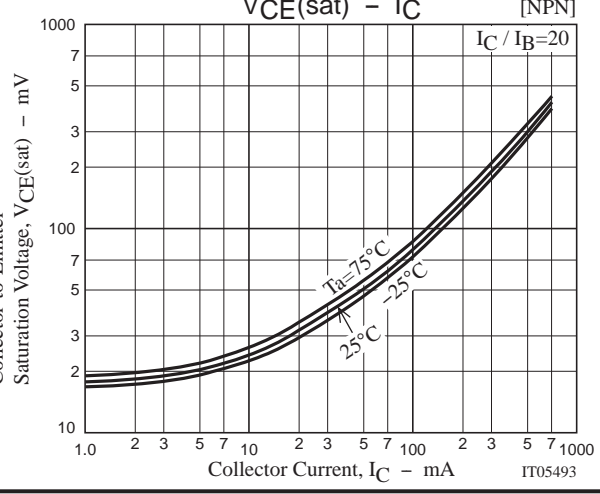
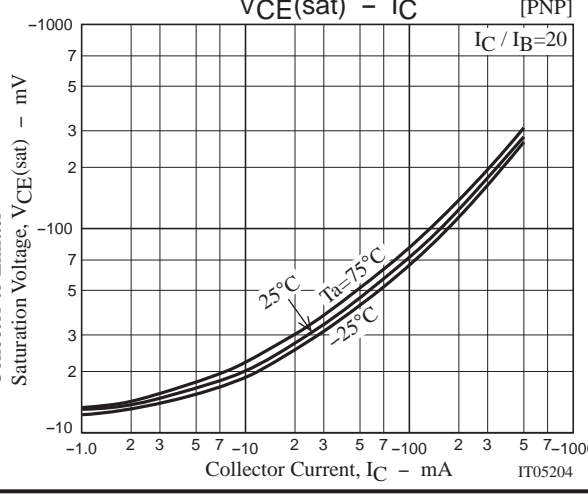
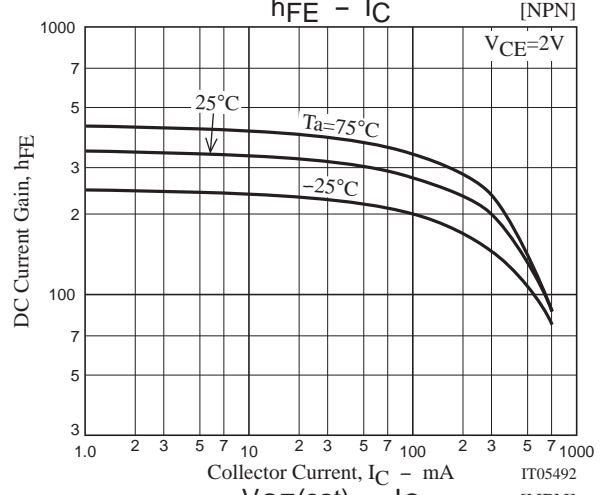
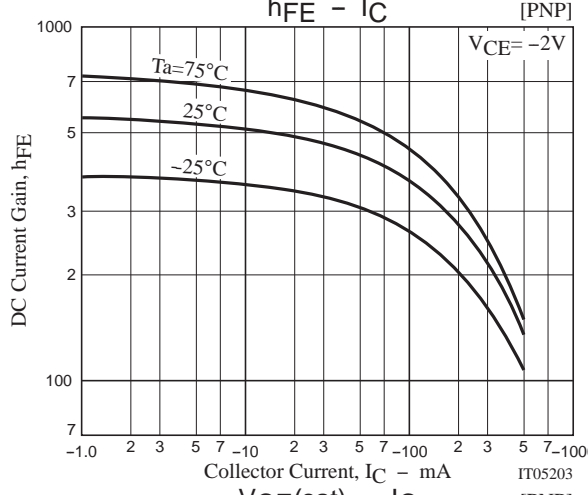
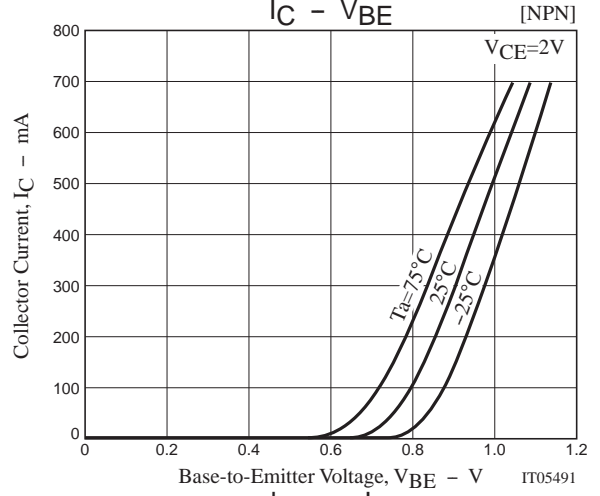
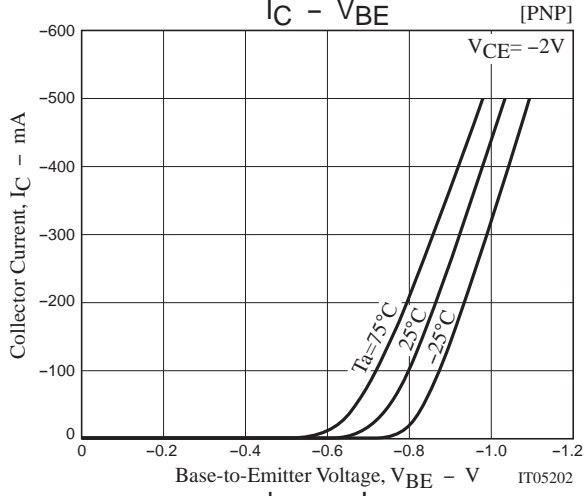
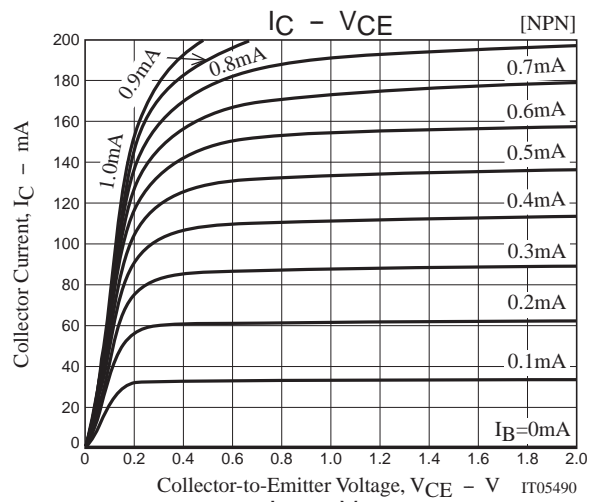
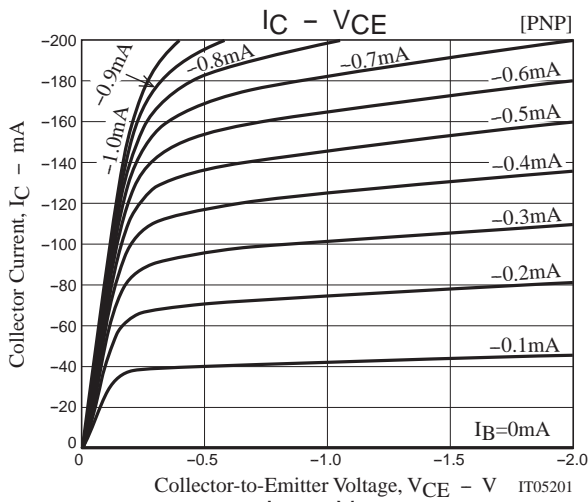
[NPN]

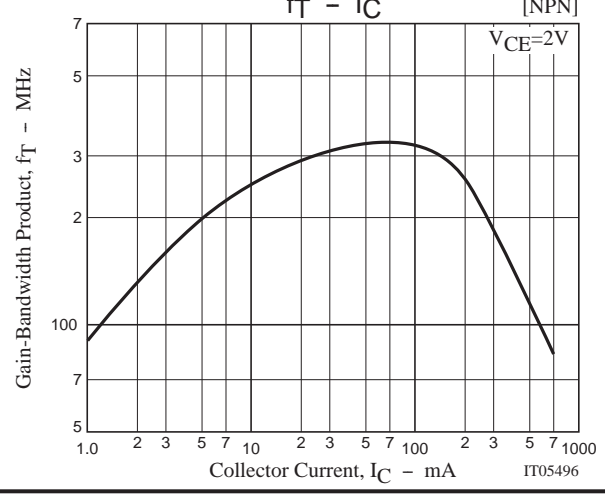
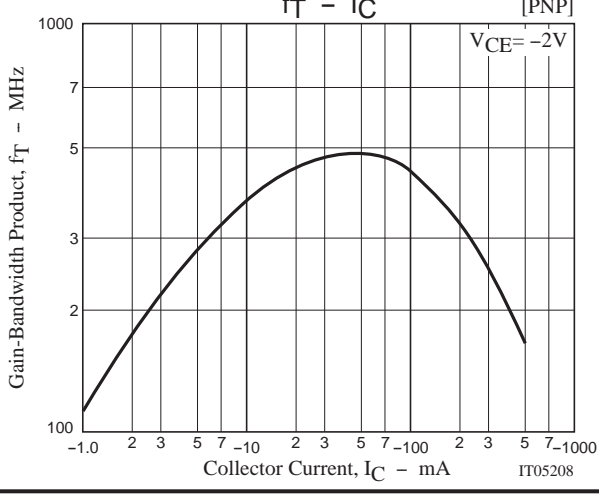
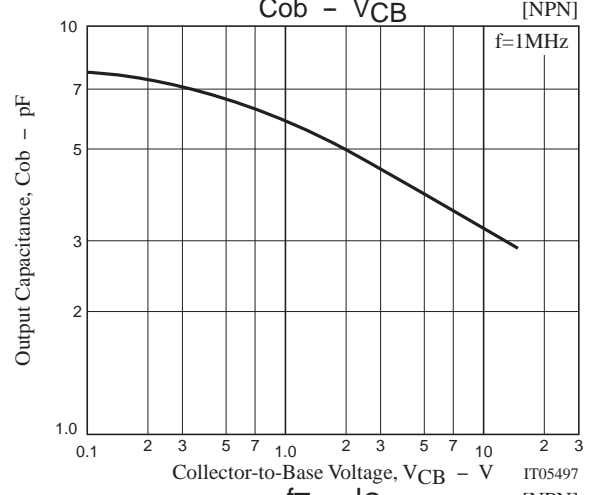
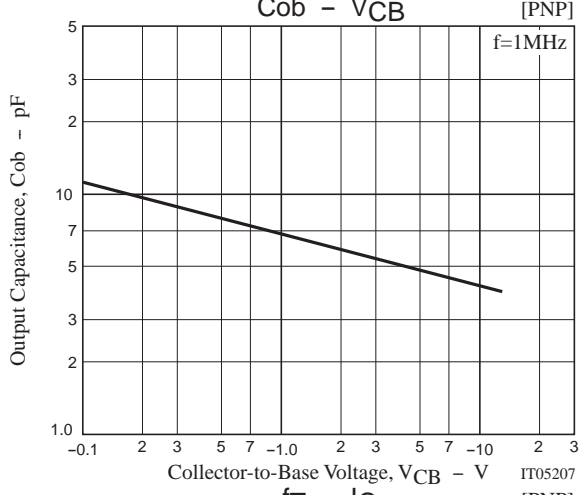
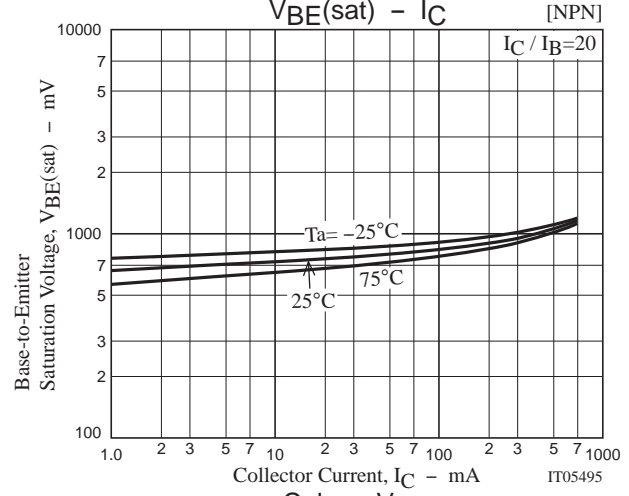
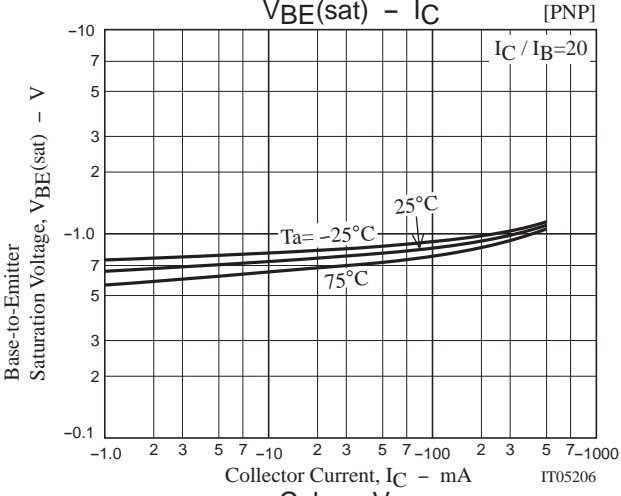
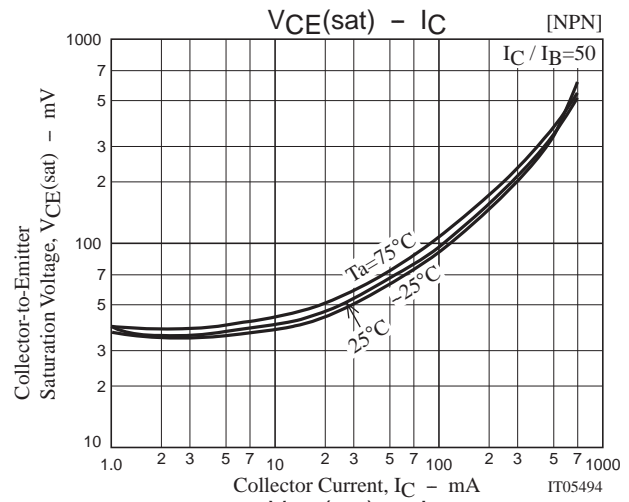
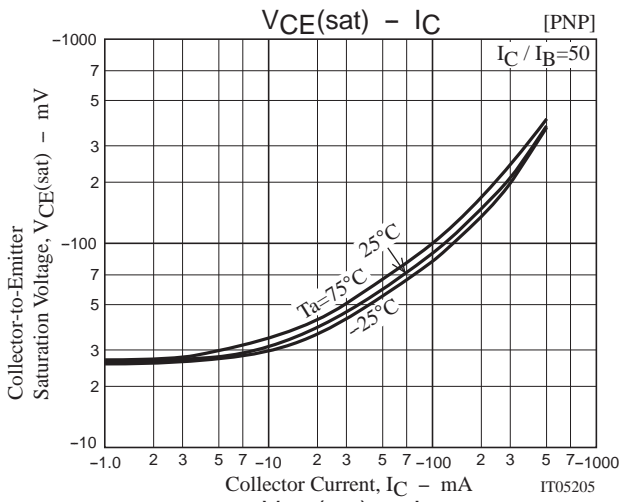


$$I_C = 20I_{B1} = -20I_{B2} = 500mA$$

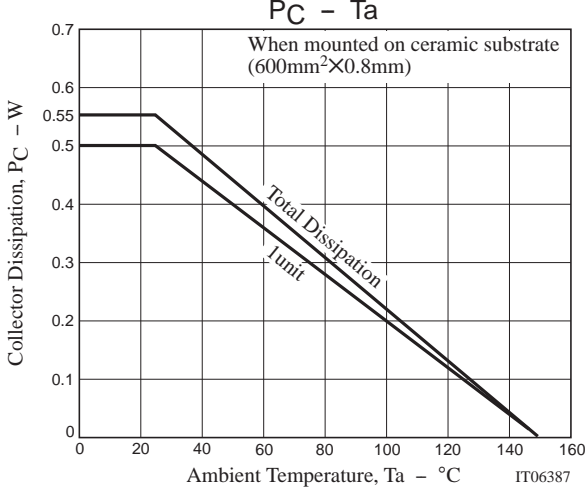
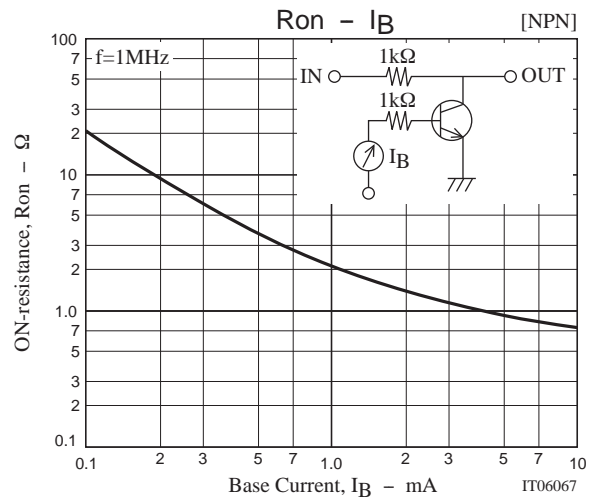
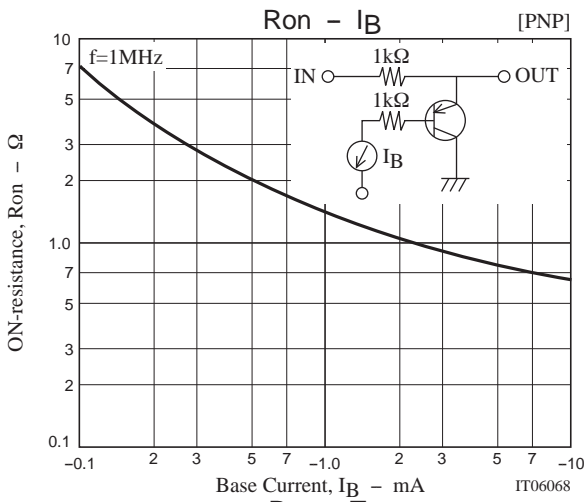
## Ordering Information

Device	Package	Shipping	memo
MCH6536-TL-E	MCPH6	3,000pcs./reel	Pb Free





# MCH6536



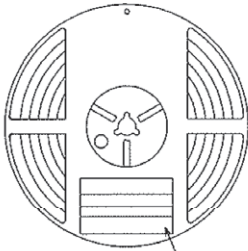
Embossed Taping Specification

MCH6536-TL-E

1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
MCPH6	MCP4	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

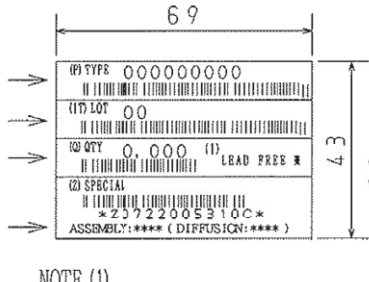
Packing method



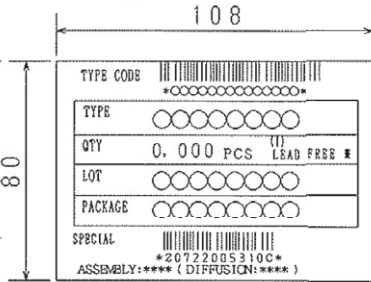
Type No.  
LOT No.  
Quantity  
Origin

Reel label

Reel label, Inner box label  
(unit :mm)



Outer box label  
It is a label at the time of factory shipments.  
The form of a label may change in physical distribution process.



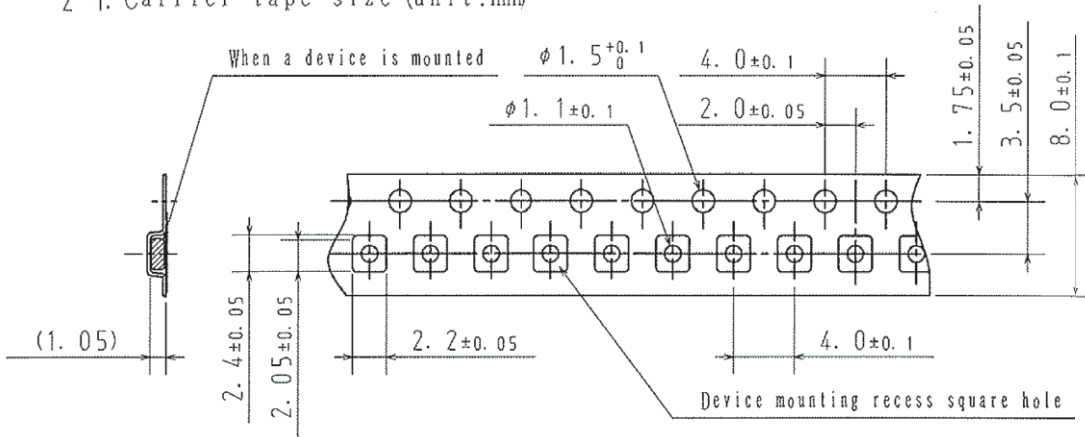
NOTE (1)

The LEAD FREE \* description shows that the surface treatment of the terminal is lead free.

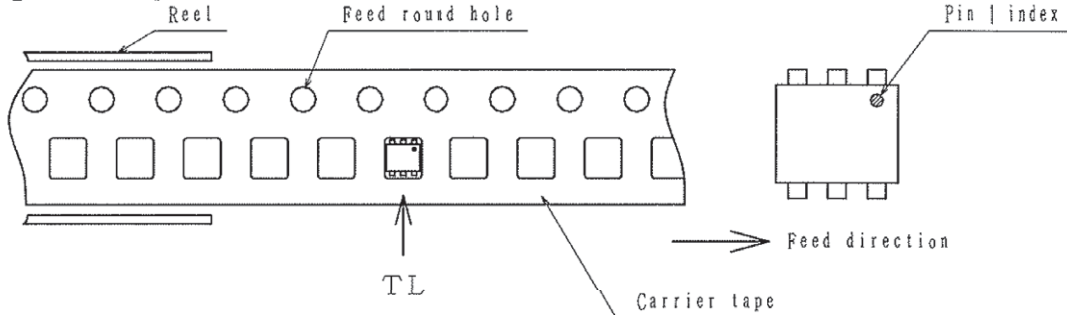
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



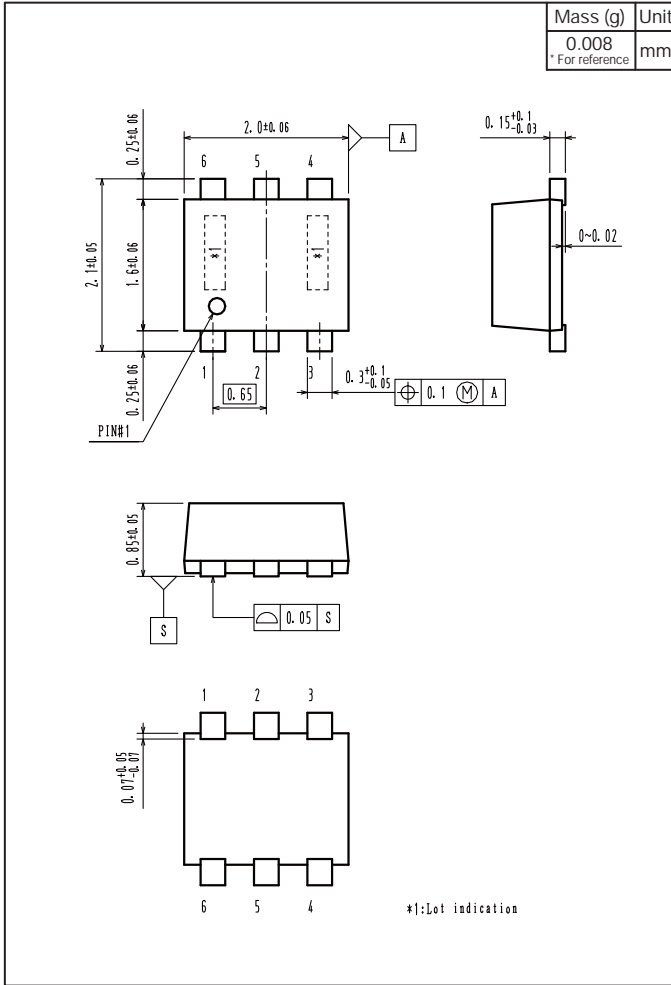
2-2. Device placement direction



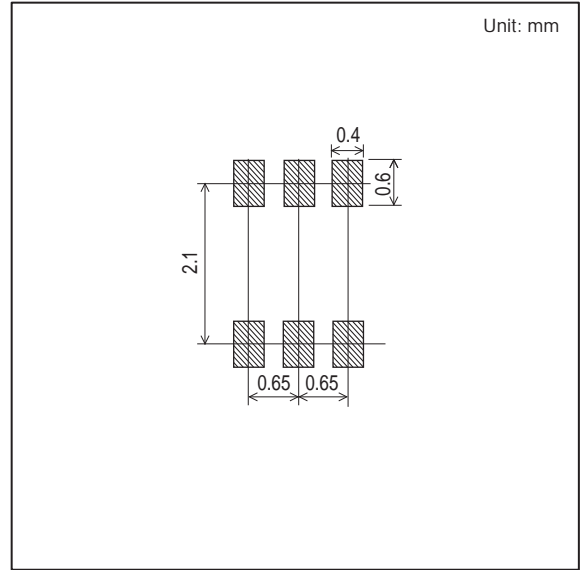
Those with pin | index on the feed hole side.....TL

# MCH6536

## Outline Drawing MCH6536-TL-E



## Land Pattern Example



ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.



## X-ON Electronics

Largest Supplier of Electrical and Electronic Components

*Click to view similar products for [Bipolar Transistors - Pre-Biased](#) category:*

*Click to view products by [ON Semiconductor](#) manufacturer:*

Other Similar products are found below :

[MMUN2217LT1G](#) [FP101-TL-E](#) [RN1607\(TE85L,F\)](#) [DRC9A14E0L](#) [DTA124GKAT146](#) [DTA144WETL](#) [DTA144WKAT146](#)

[DTC113EET1G](#) [DTC115TETL](#) [DTC115TKAT146](#) [DTC124TETL](#) [DTC144ECA-TP](#) [DTC144VUAT106](#) [MUN5241T1G](#)

[BCR158WH6327XTSA1](#) [NSBA114TDP6T5G](#) [NSBA143TF3T5G](#) [NSBA143ZF3T5G](#) [NSBC114EF3T5G](#) [NSBC114YF3T5G](#)

[NSBC123TF3T5G](#) [NSBC143TF3T5G](#) [NSVMUN2212T1G](#) [NSVMUN5111DW1T3G](#) [NSVMUN5314DW1T3G](#) [NSVUMC2NT1G](#)

[SMMUN2134LT1G](#) [SMUN2212T1G](#) [SMUN5235T1G](#) [SMUN5330DW1T1G](#) [SSVMUN5312DW1T2G](#) [2SC3650-TD-E](#) [RN1303\(TE85L,F\)](#)

[RN4605\(TE85L,F\)](#) [BCR135SH6327XT](#) [TTEPROTOTYPE79](#) [UMC3NTR](#) [DTA113EET1G](#) [EMA2T2R](#) [EMH15T2R](#) [SDTA114YET1G](#)

[SMMUN2111LT3G](#) [SMMUN2113LT1G](#) [SMMUN2114LT1G](#) [SMMUN2211LT3G](#) [SMUN2214T3G](#) [SMUN5113DW1T1G](#)

[SMUN5335DW1T1G](#) [NSBA114YF3T5G](#) [NSBC114TF3T5G](#)